

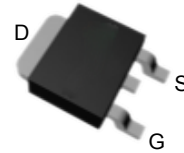
Features

- 60V/90A,
 $R_{DS(ON)} = 5.2\text{m}\Omega$ (typ.) @ $V_{GS} = 10\text{V}$
- Reliable and Rugged
- Lead Free and Green Devices Available
(RoHS Compliant)

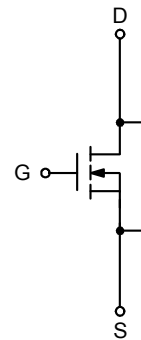
Applications

- Secondary Side Synchronous Rectification
- DC-DC Converter
- Motor Control
- Load Switching

Pin Description



Top View of TO-252



N-Channel MOSFET

Absolute Maximum Ratings (T_A = 25°C Unless Otherwise Noted)

Symbol	Parameter		Rating	Unit
Common Ratings				
V _{DSS}	Drain-Source Voltage		60	V
V _{GSS}	Gate-Source Voltage		±20	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	
I _S	Diode Continuous Forward Current	T _C =25°C	90	A
I _D	Continuous Drain Current	T _C =25°C	90	A
		T _C =100°C	67	
I _{DM} ^b	Pulsed Drain Current	T _C =25°C	360	
P _D	Maximum Power Dissipation	T _C =25°C	125	W
		T _C =100°C	50	
R _{θJC}	Thermal Resistance-Junction to Case		1	°C/W
R _{θJA} ^c	Thermal Resistance-Junction to Ambient		50	°C/W
I _{AS} ^d	Avalanche Current, Single pulse	L=0.5mH	40	A
E _{AS} ^d	Avalanche Energy, Single pulse	L=0.5mH	400	mJ

Note a : Current limited by bond wire.

Note b : Pulse width limited by max. junction temperature.

Note c : Surface Mounted on 1in² pad area.

Note d : UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature T_J=25°C).

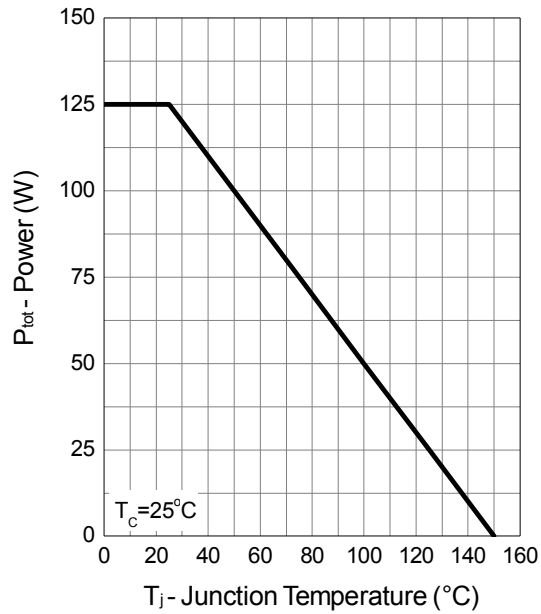
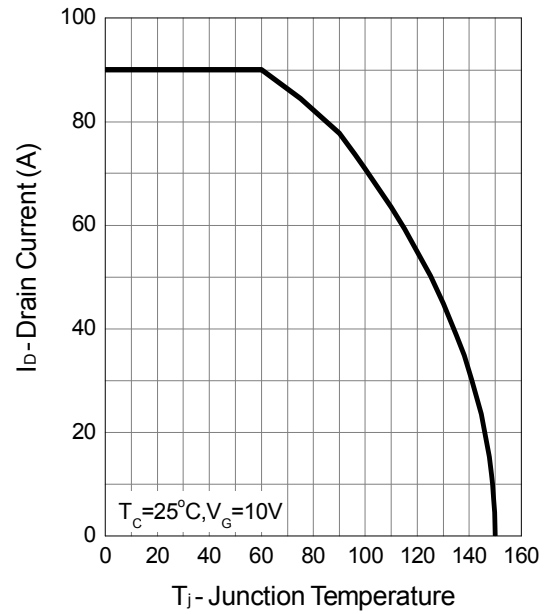
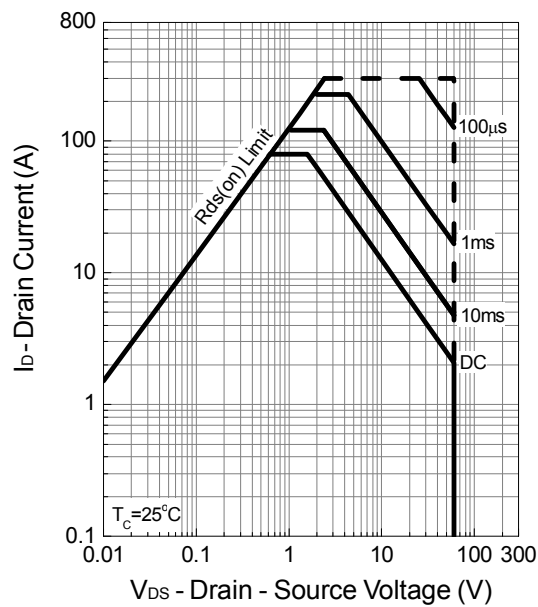
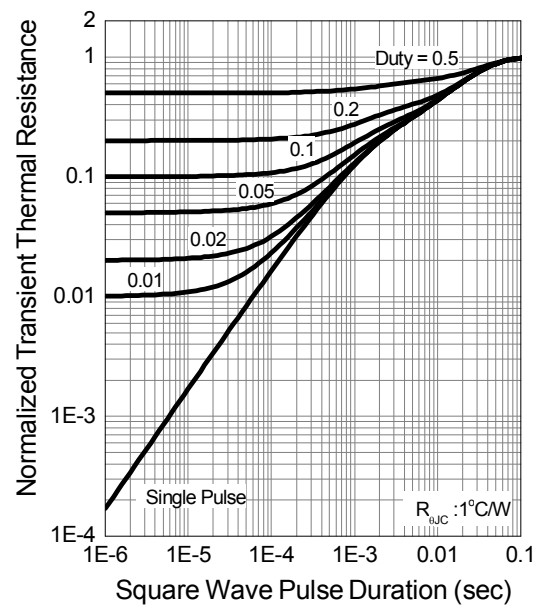
Electrical Characteristics (T_A = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =48V, V _{GS} =0V T _J =85°C	-	-	1 30	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	2	3	4	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} ^e	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =40A	-	5.2	6.8	mΩ
Diode Characteristics						
V _{SD} ^e	Diode Forward Voltage	I _{SD} =20A, V _{GS} =0V	-	0.8	1.3	V
t _{rr}	Reverse Recovery Time	I _{SD} =30A, dI _{SD} /dt=100A/μs	-	36	-	ns
Q _{rr}	Reverse Recovery Charge		-	53	-	nC
Dynamic Characteristics ^f						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz	-	1	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =30V, Frequency=1.0MHz	-	2550	3130	pF
C _{oss}	Output Capacitance		-	215	-	
C _{rss}	Reverse Transfer Capacitance		-	200	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =30V, R _L =30Ω, I _{DS} =1A, V _{GEN} =10V, R _G =6Ω	-	27	49	ns
t _r	Turn-on Rise Time		-	15	27	
t _{d(OFF)}	Turn-off Delay Time		-	55	99	
t _f	Turn-off Fall Time		-	40	72	
Gate Charge Characteristics ^f						
Q _g	Total Gate Charge	V _{DS} =30V, V _{GS} =10V, I _{DS} =30A	-	55	77	
Q _{gs}	Gate-Source Charge		-	15	-	
Q _{gd}	Gate-Drain Charge		-	16	-	

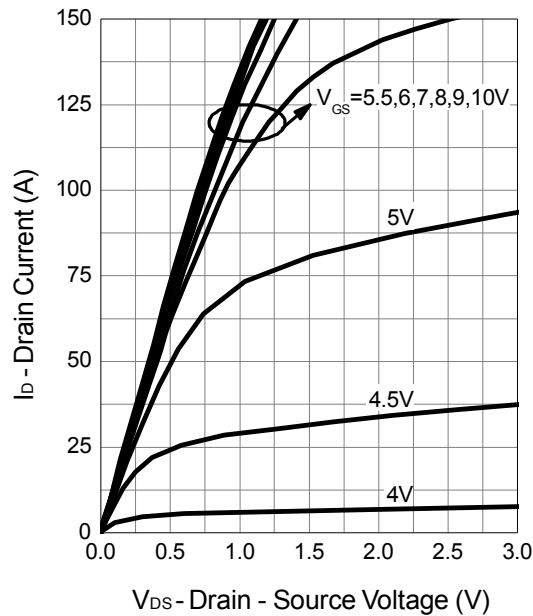
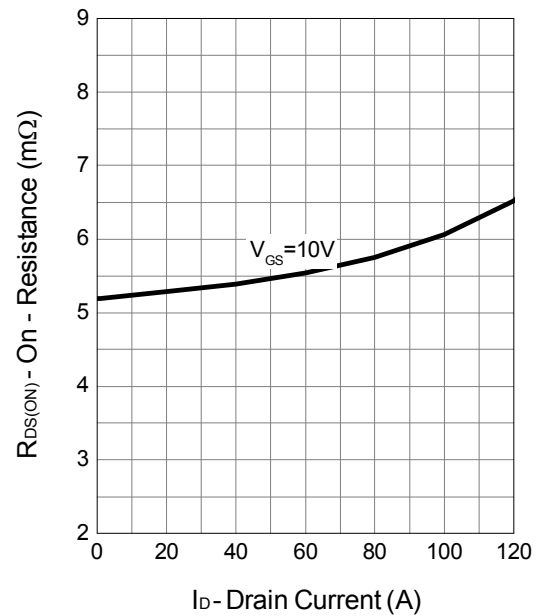
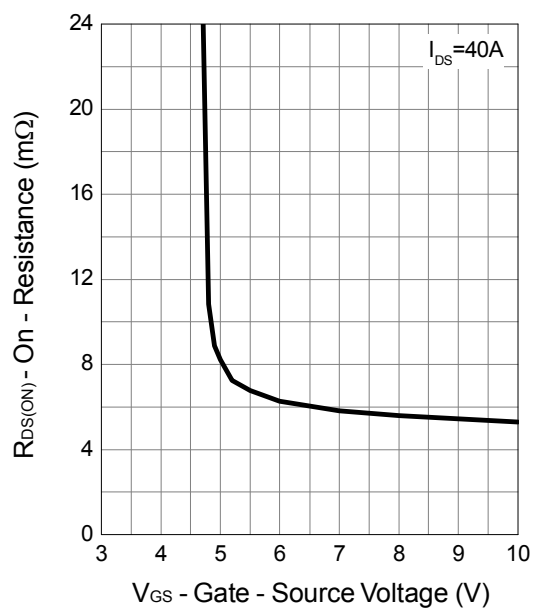
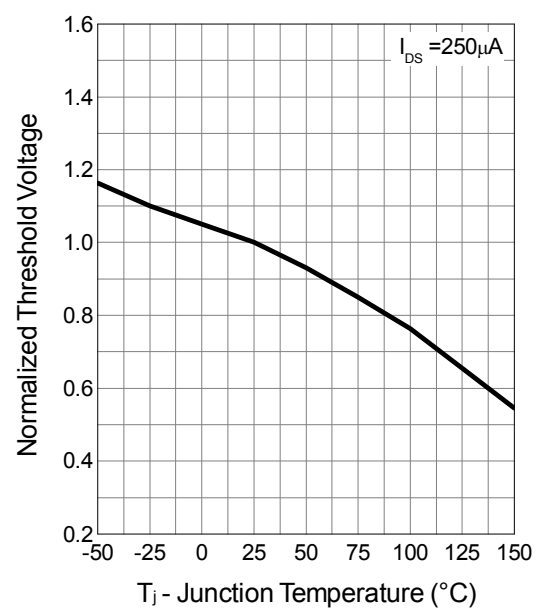
Note e : Pulse test ; pulse width≤300μs, duty cycle≤2%.

Note f : Guaranteed by design, not subject to production testing.

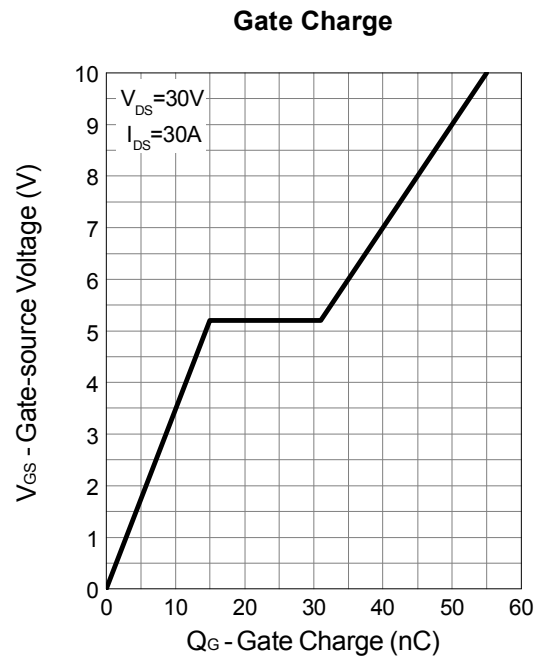
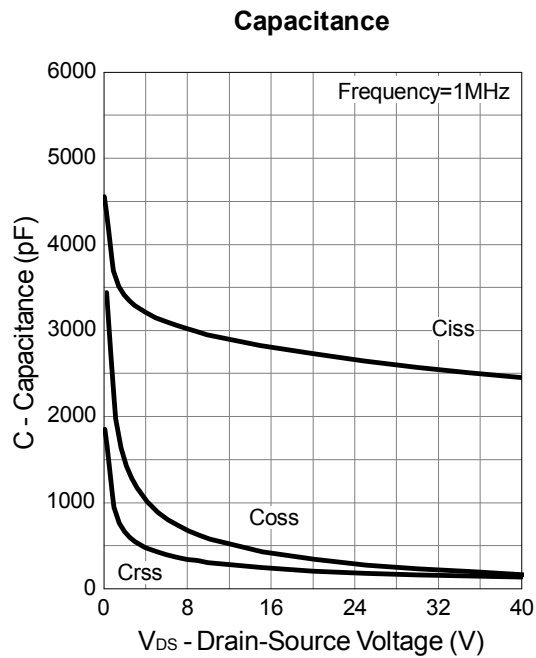
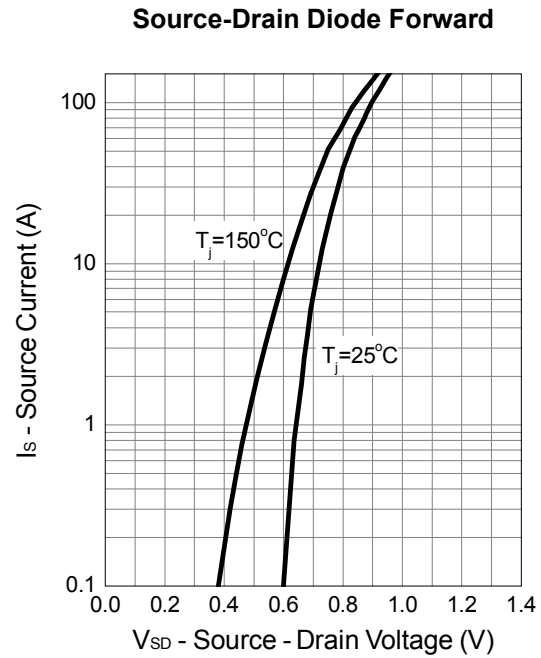
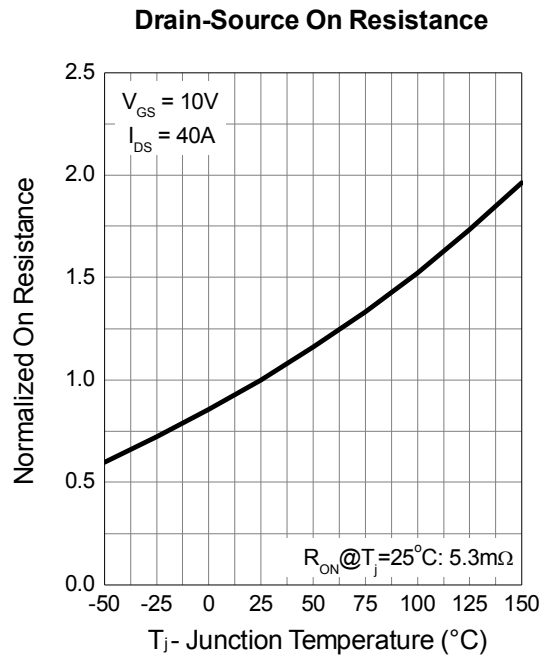
Typical Operating Characteristics

Power Dissipation

Drain Current

Safe Operation Area

Thermal Transient Impedance


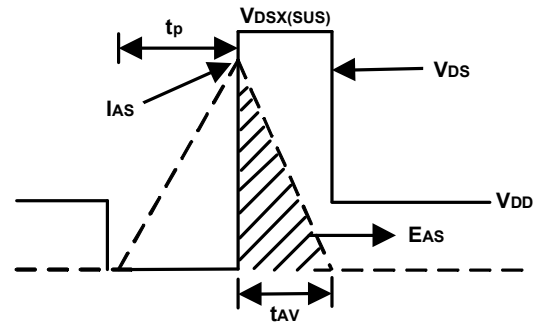
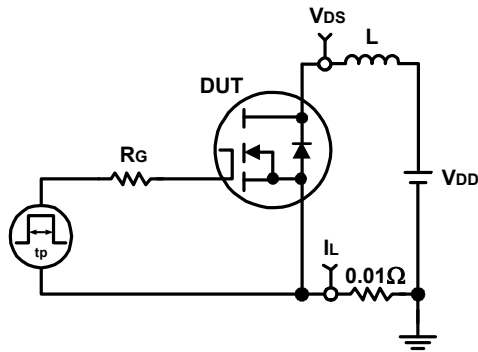
Typical Operating Characteristics (Cont.)

Output Characteristics

Drain-Source On Resistance

Gate-Source On Resistance

Gate Threshold Voltage


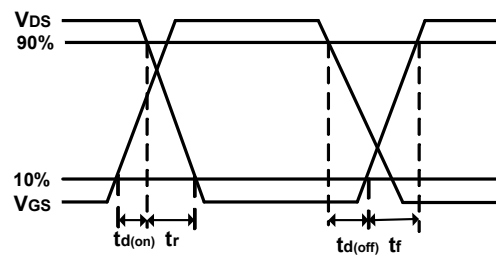
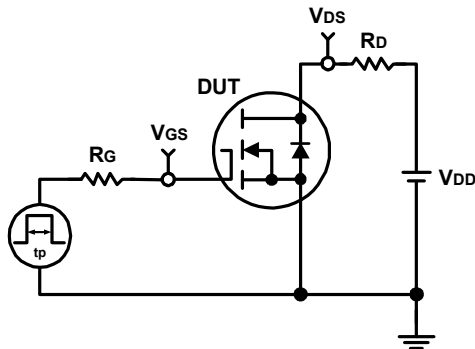
Typical Operating Characteristics (Cont.)



Avalanche Test Circuit and Waveforms

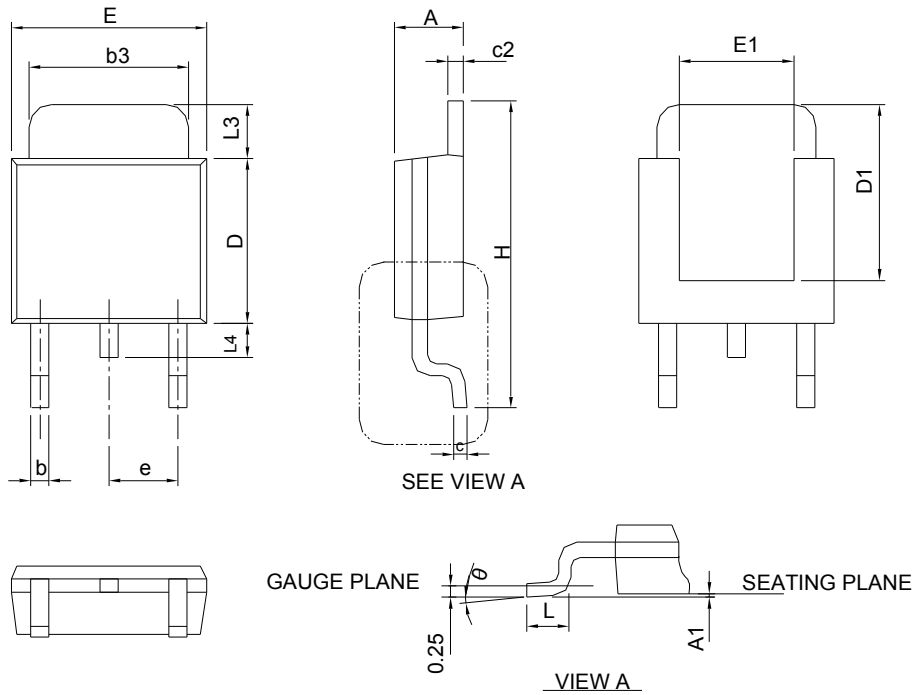


Switching Time Test Circuit and Waveforms



Package Information

TO-252



SYMBOL	TO-252-3			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.39	0.086	0.094
A1	-	0.13	-	0.005
b	0.50	0.89	0.020	0.035
b3	4.95	5.46	0.195	0.215
c	0.46	0.61	0.018	0.024
c2	0.46	0.89	0.018	0.035
D	5.33	6.22	0.210	0.245
D1	4.57	6.00	0.180	0.236
E	6.35	6.73	0.250	0.265
E1	3.81	6.00	0.150	0.236
e	2.29 BSC		0.090 BSC	
H	9.40	10.41	0.370	0.410
L	0.90	1.78	0.035	0.070
L3	0.89	2.03	0.035	0.080
L4	-	1.02	-	0.040
θ	0°	8°	0°	8°

Note : Follow JEDEC TO-252 .

RECOMMENDED LAND PATTERN

