

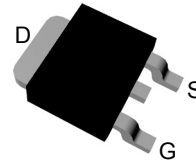
Features

- 500V/4A
 $R_{DS(ON)}=1.6\Omega(\text{typ.})@V_{GS}=10V$
- 100% UIS + R_g Tested
- Reliable and Rugged
- Lead Free and Green Devices Available
(RoHS Compliant)

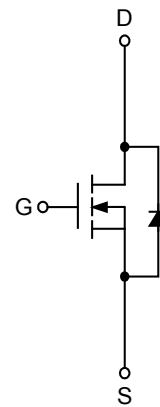
Applications

- AC/DC Power Conversion in Switched Mode Power Supplies (SMPS)
- Uninterruptible Power Supply (UPS)
- Adapter

Pin Description



TO-252



N-Channel MOSFET

Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ Unless Otherwise Noted)

Symbol	Parameter		Rating	Unit
Common Ratings				
V _{DSS}	Drain-Source Voltage		500	V
V _{GSS}	Gate-Source Voltage		±30	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	
I _S	Diode Continuous Forward Current	T _C =25°C	4	A
I _D	Continuous Drain Current	T _C =25°C	4	A
		T _C =100°C	1.6	
I _{DM} ^a	Pulsed Drain Current	T _C =25°C	12	
P _D	Maximum Power Dissipation	T _C =25°C	104	W
		T _C =100°C	41	
R _{θJC}	Thermal Resistance-Junction to Case		1.2	°C/W
I _D	Continuous Drain Current	T _A =25°C	0.66	A
		T _A =70°C	0.53	
P _D	Maximum Power Dissipation	T _A =25°C	2.5	W
		T _A =70°C	1.6	
R _{θJA} ^b	Thermal Resistance-Junction to Ambient		60	°C/W

Note a : Pulse width limited by max. junction temperature.

Note b : Surface Mounted on 1in^2 pad area.

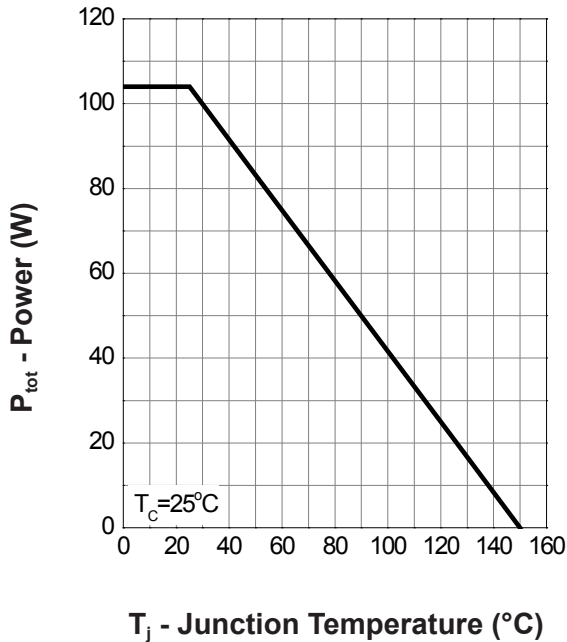
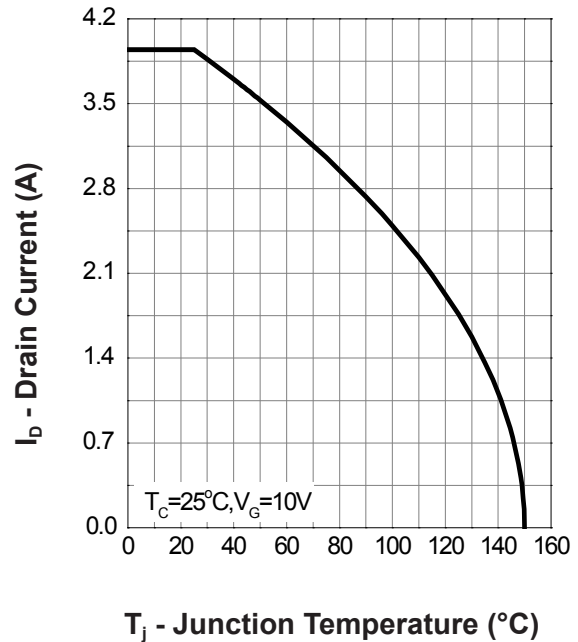
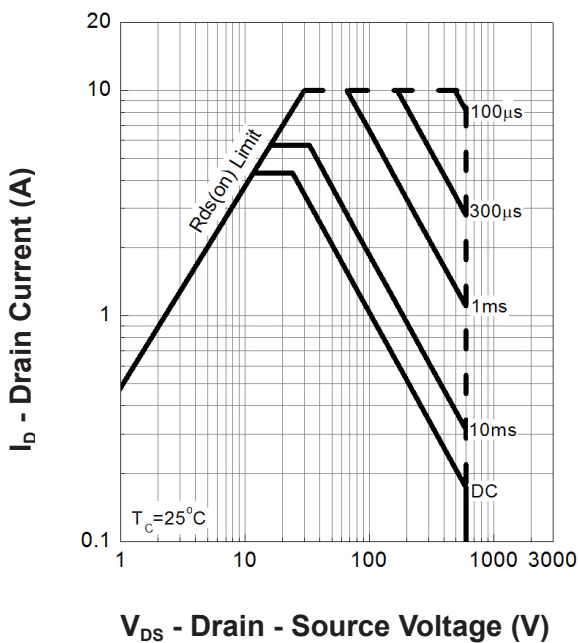
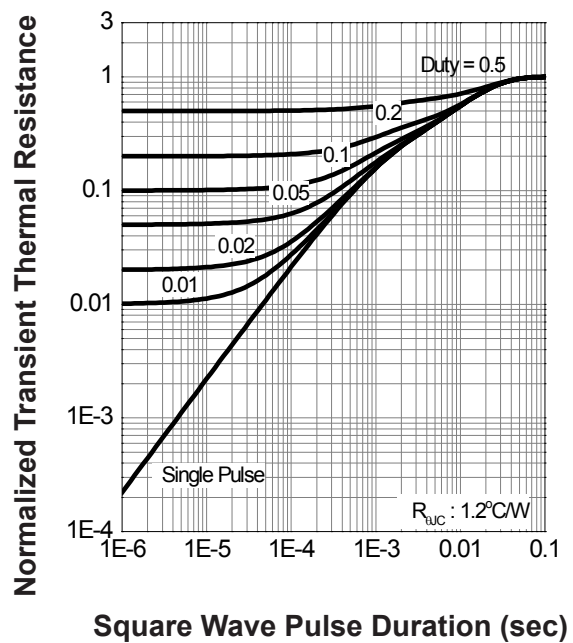
Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	500	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =400V, V _{GS} =0V	-	-	1	μA
		T _J =85°C	-	-	30	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	2	3	4	V
I _{GSS}	Gate Leakage Current	V _{GS} =±30V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} ^c	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =2A	-	1.6	1.9	Ω
Diode Characteristics						
V _{SD} ^c	Diode Forward Voltage	I _{SD} =2A, V _{GS} =0V	-	0.8	1.5	V
t _{rr}	Reverse Recovery Time	I _{SD} =2A, dI _{SD} /dt=100A/μs	-	185	-	ns
Q _{rr}	Reverse Recovery Charge		-	1000	-	nC
Dynamic Characteristics ^f						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	-	1.5	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =300V, Frequency=1.0MHz	-	610	800	pF
C _{oss}	Output Capacitance		-	27	-	
C _{rss}	Reverse Transfer Capacitance		-	15	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =30V, R _L =30Ω, I _{DS} =1A, V _{GEN} =10V, R _G =6Ω	-	12	22	ns
t _r	Turn-on Rise Time		-	5	9	
t _{d(OFF)}	Turn-off Delay Time		-	16	29	
t _f	Turn-off Fall Time		-	6	11	
Gate Charge Characteristics ^f						
Q _g	Total Gate Charge	V _{DS} =300V, V _{GS} =10V, I _{DS} =2A	-	11.2	15.7	nC
Q _{gs}	Gate-Source Charge		-	3.7	-	
Q _{gd}	Gate-Drain Charge		-	2.9	-	

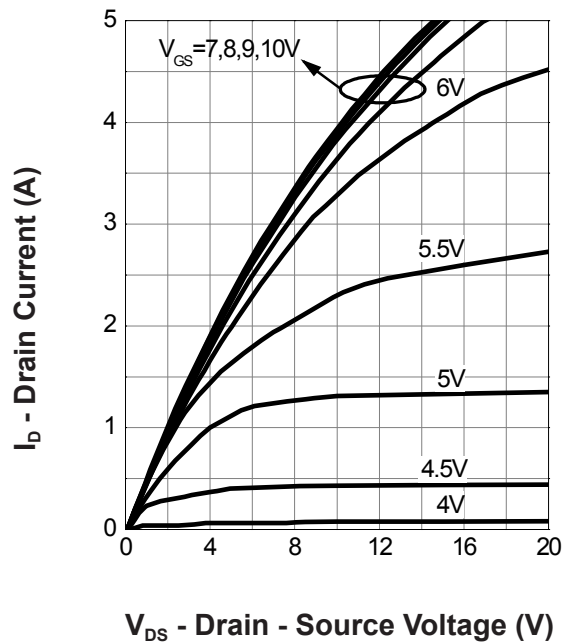
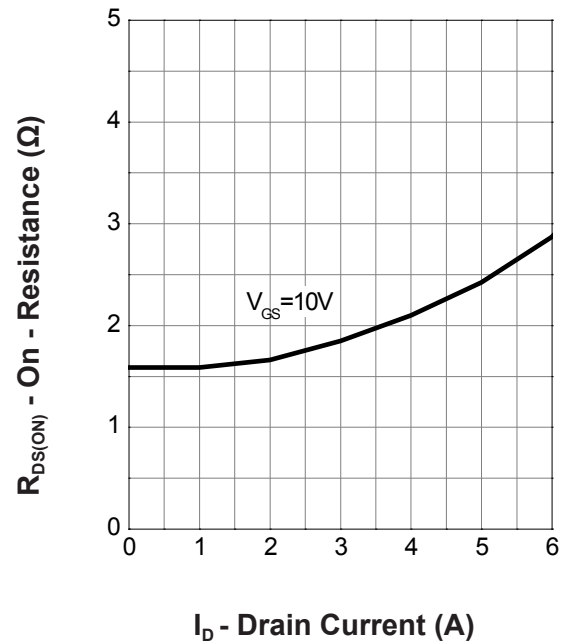
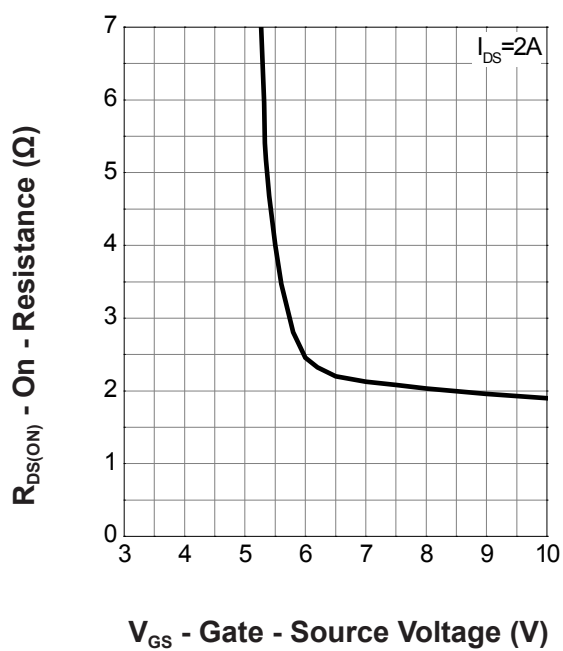
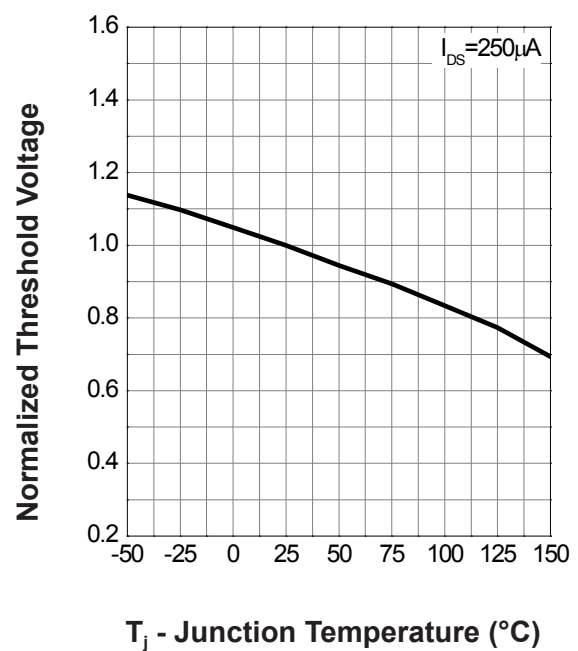
Note c : Pulse test ; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

Note d : Guaranteed by design, not subject to production testing.

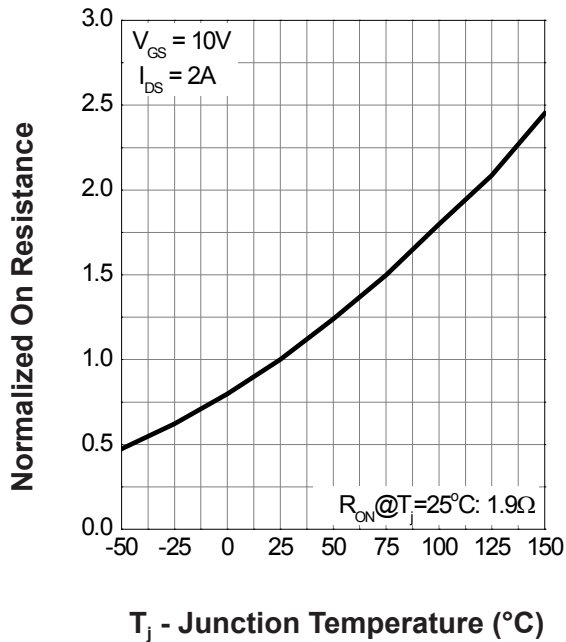
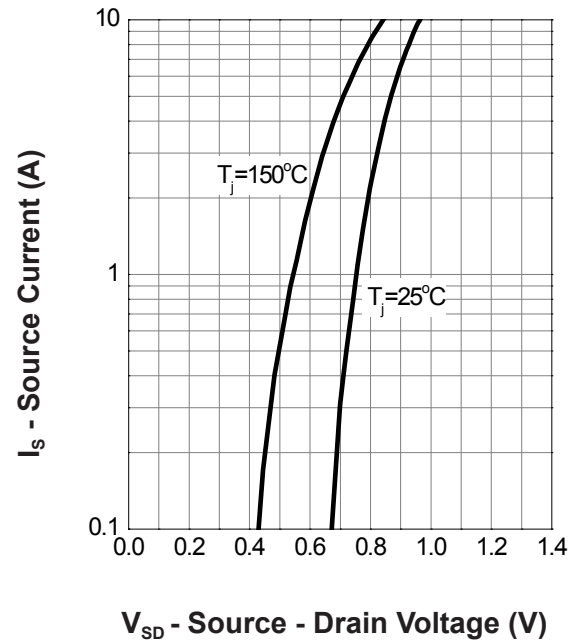
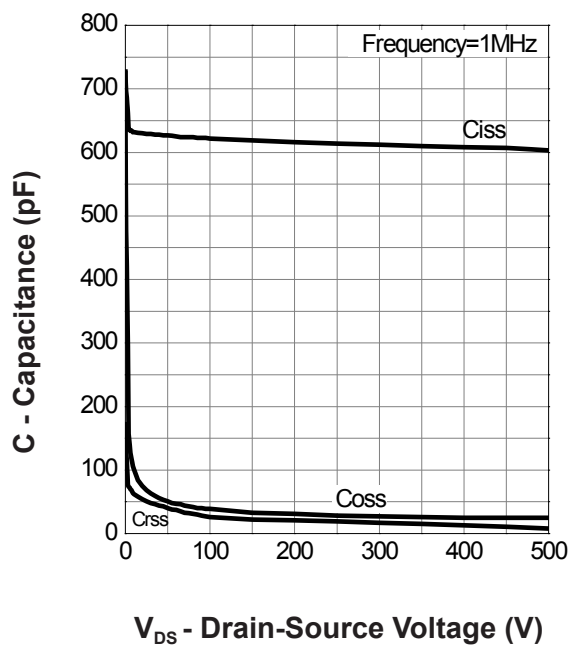
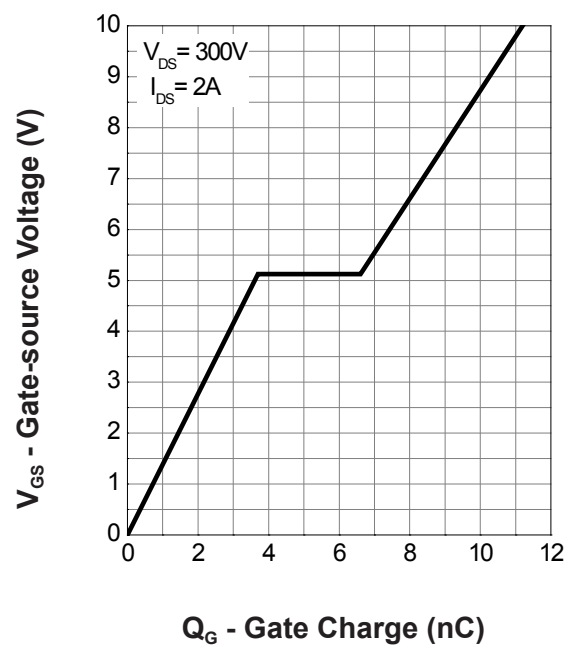
Typical Operating Characteristics

Power Dissipation

Drain Current

Safe Operation Area

Thermal Transient Impedance


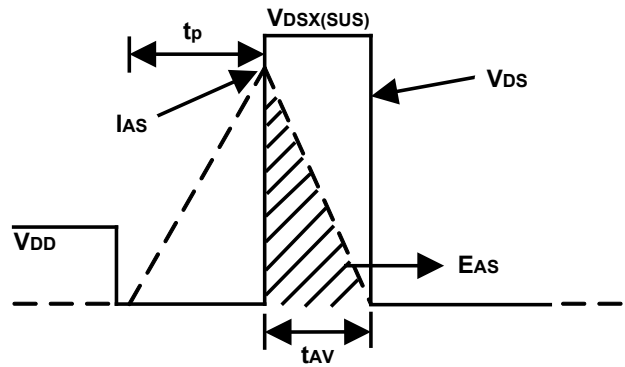
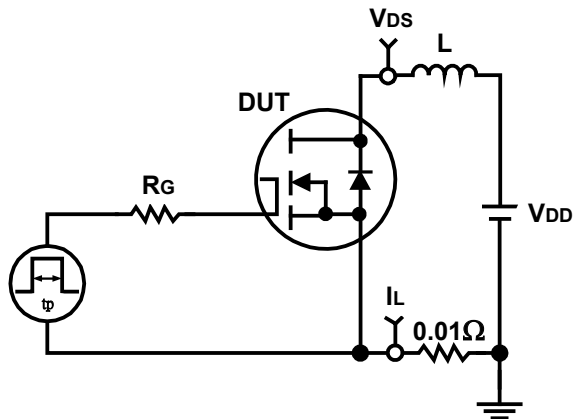
Typical Operating Characteristics(Cont.)

Output Characteristics

Drain-Source On Resistance

Gate-Source On Resistance

Gate Threshold Voltage


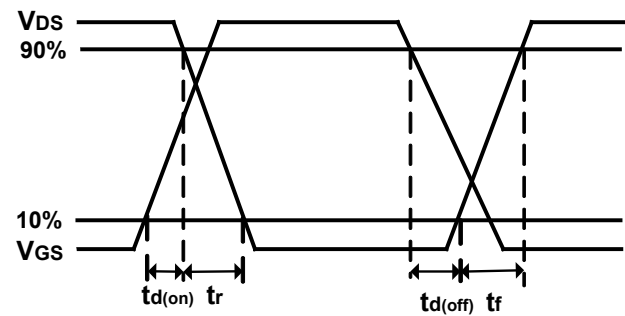
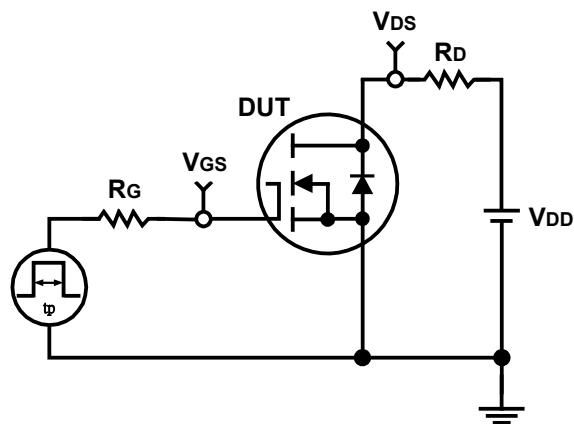
Typical Operating Characteristics(Cont.)

Drain-Source On Resistance

Source-Drain Diode Forward

Capacitance

Gate Charge


Avalanche Test Circuit and Waveforms

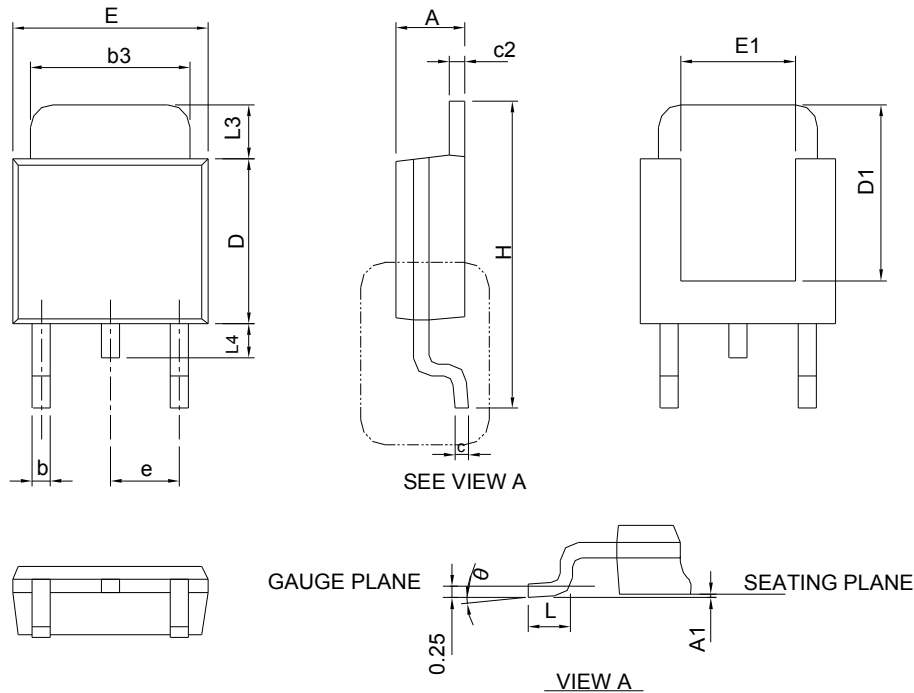


Switching Time Test Circuit and Waveforms



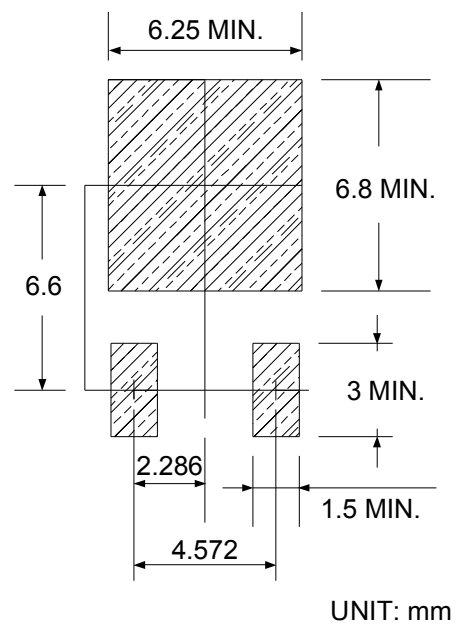
Package Information

TO-252



SYMBOL	TO-252			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.39	0.086	0.094
A1	-	0.13	-	0.005
b	0.50	0.89	0.020	0.035
b3	4.95	5.46	0.195	0.215
c	0.46	0.61	0.018	0.024
c2	0.46	0.89	0.018	0.035
D	5.33	6.22	0.210	0.245
D1	4.57	6.00	0.180	0.236
E	6.35	6.73	0.250	0.265
E1	3.81	6.00	0.150	0.236
e	2.29 BSC		0.090 BSC	
H	9.40	10.41	0.370	0.410
L	0.90	1.78	0.035	0.070
L3	0.89	2.03	0.035	0.080
L4	-	1.02	-	0.040
θ	0°	8°	0°	8°

RECOMMENDED LAND PATTERN



Note : Follow JEDEC TO-252