

Features

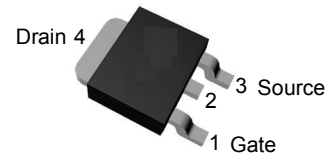
- -100V/-18A,
 $R_{DS(ON)} = 8.0\text{m}\Omega(\text{typ.}) @ V_{GS} = -10\text{V}$
 $R_{DS(ON)} = 8.5\text{m}\Omega(\text{typ.}) @ V_{GS} = -4.5\text{V}$
- Reliable and Rugged
- Lead Free and Green Devices Available
 (RoHS Compliant)
- 100% UIS Tested
- ESD Protection
- HBM ESD protection level pass 8KV

Note : The diode connected between the gate and source serves only as protection against ESD. No gate overvoltage rating is implied.

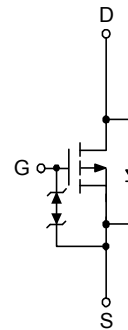
Applications

- Power Management for Industrial DC / DC Converters.

Pin Description



Top View of TO-252-2



P-Channel MOSFET

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter		Rating	Unit
Common Ratings				
V _{DSS}	Drain-Source Voltage		-100	V
V _{GSS}	Gate-Source Voltage		±20	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	°C
I _S	Diode Continuous Forward Current		-9	A
I _{AS} ^a	Avalanche Current, Single pulse	L=0.5mH	-16	A
E _{AS} ^a	Avalanche Energy, Single pulse	L=0.5mH	64	mJ
I _{DP} ^b	Pulse Drain Current Tested	T _C =25°C	-74 ^c	A
I _D	Continuous Drain Current	T _C =25°C	-18	
		T _C =100°C	-11	
P _D	Maximum Power Dissipation	T _C =25°C	62.5	W
		T _C =100°C	25	
R _{θJC}	Thermal Resistance-Junction to Case		2	°C/W
I _D	Continuous Drain Current	T _A =25°C	-3.5	A
		T _A =70°C	-2.8	
P _D	Maximum Power Dissipation	T _A =25°C	2.2	W
		T _A =70°C	1.4	
R _{θJA} ^d	Thermal Resistance-Junction to Ambient	Steady State	55	°C/W

Note a : UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature $T_J=25^\circ\text{C}$).

Note b : Pulse width limited by max. junction temperature.

Note c : Wire limited.

Note d : $R_{\theta JA}$ steady state $t=999\text{s}$. $R_{\theta JA}$ is measured with the device mounted on 1in^2 , FR-4 board with 2oz. Copper.

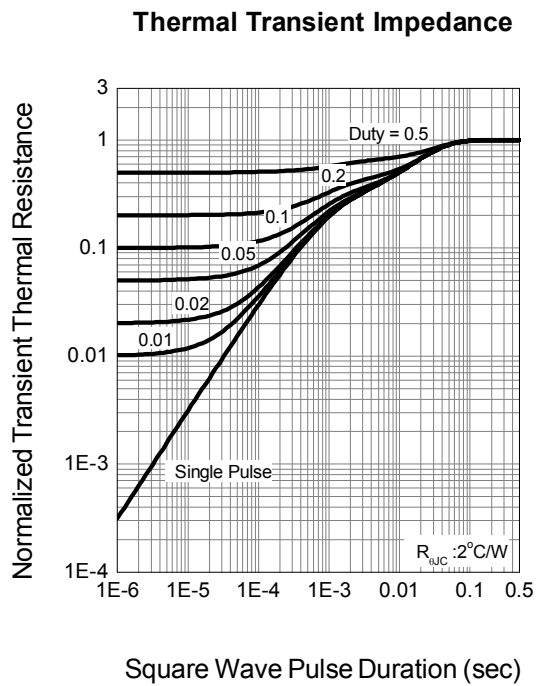
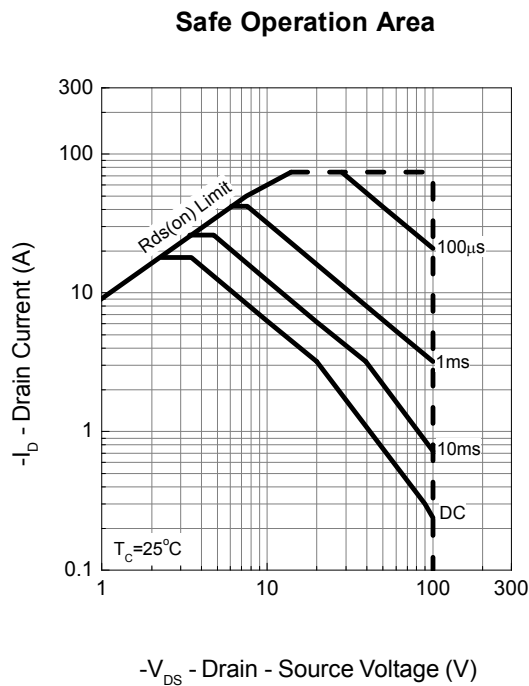
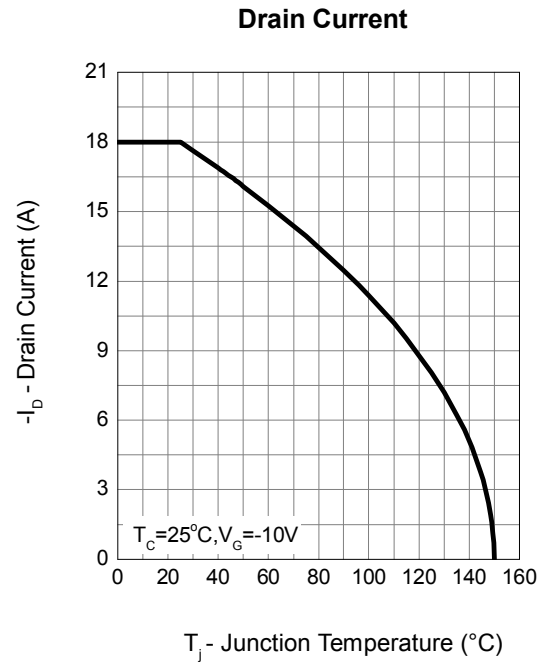
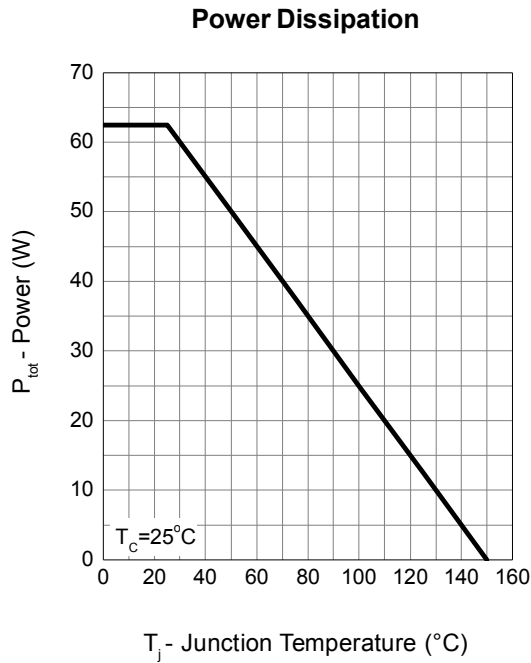
Electrical Characteristics (T_A = 25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =-250μA	-100	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-80V, V _{GS} =0V T _J =85°C	-	-	-1 -30	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =-250μA	-1	-2	-3	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±20	μA
R _{DS(ON)} ^e	Drain-Source On-state Resistance	V _{GS} =-10V, I _{DS} =-9A V _{GS} =-4.5V, I _{DS} =-6A	-	80 85	90 102	mΩ mΩ
Diode Characteristics						
V _{SD} ^e	Diode Forward Voltage	I _{SD} =-1A, V _{GS} =0V	-	-0.7	-1	V
t _{rr}	Reverse Recovery Time	I _{SD} =-9A, dI _{SD} /dt=100A/μs	-	38	-	ns
Q _{rr}	Reverse Recovery Charge		-	65	-	nC
Dynamic Characteristics ^f						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz	-	5	10	Ω
C _{iSS}	Input Capacitance	V _{GS} =0V, V _{DS} =-30V, Frequency=1.0MHz	-	1780	2314	pF
C _{oss}	Output Capacitance		-	120	-	
C _{rss}	Reverse Transfer Capacitance		-	68	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =-30V, R _L =30Ω, I _{DS} =-1A, V _{GEN} =-10V, R _G =6Ω	-	11	20	ns
t _r	Turn-on Rise Time		-	9	16	
t _{d(OFF)}	Turn-off Delay Time		-	50	90	
t _f	Turn-off Fall Time		-	22	40	
Gate Charge Characteristics ^f						
Q _g	Total Gate Charge	V _{DS} =-50V, V _{GS} =-10V, I _{DS} =-9A	-	39	55	nC
Q _{gs}	Gate-Source Charge		-	6.3	-	
Q _{gd}	Gate-Drain Charge		-	7.9	-	

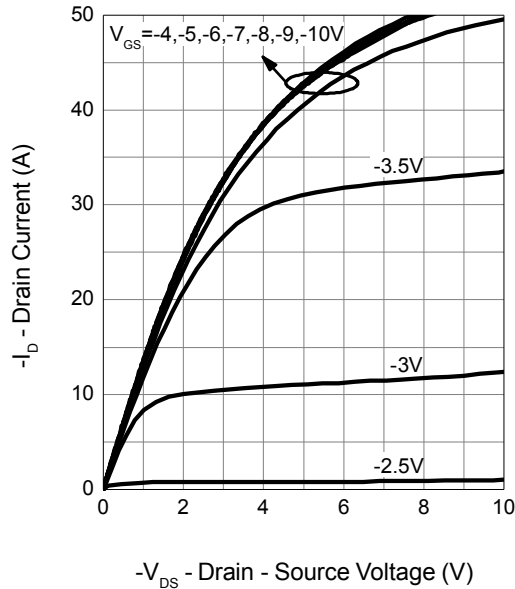
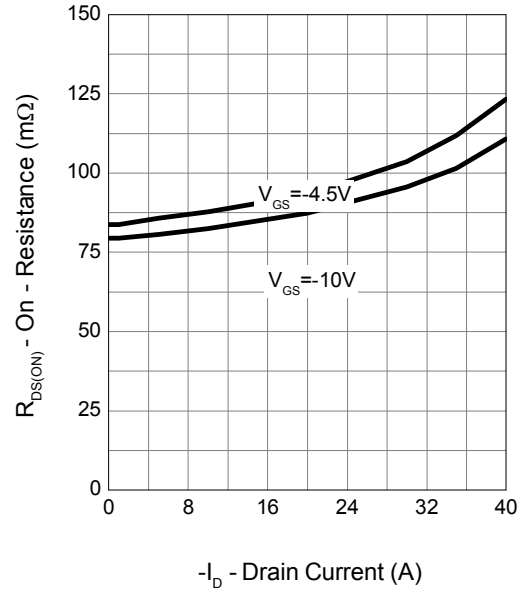
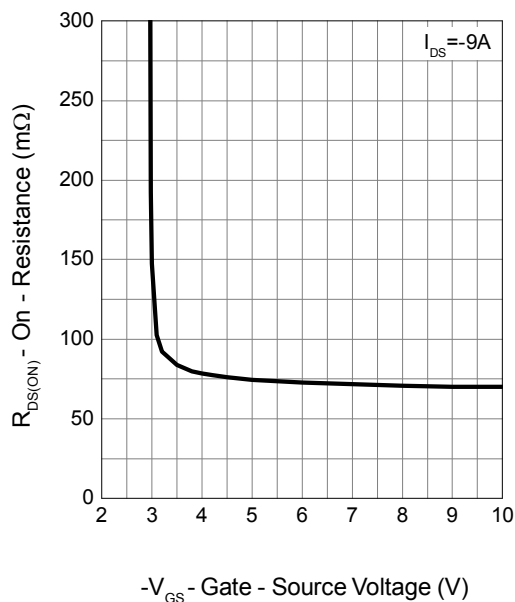
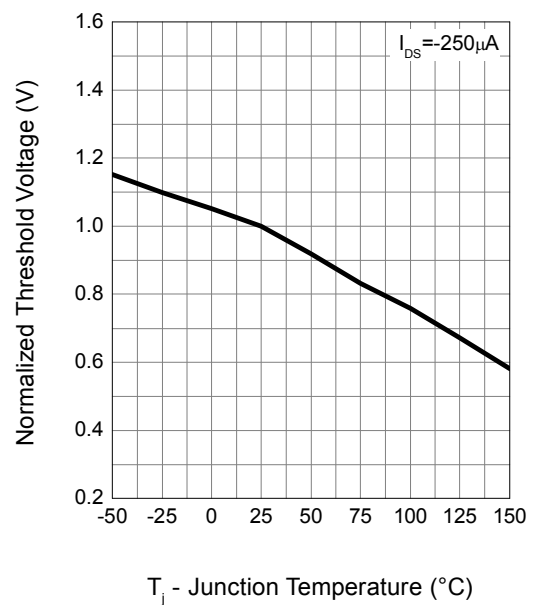
Note e : Pulse test; pulse width≤300μs, duty cycle≤2%.

Note f : Guaranteed by design, not subject to production testing.

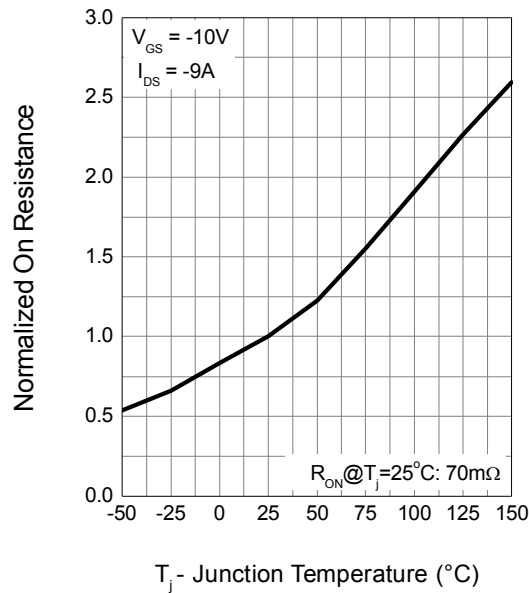
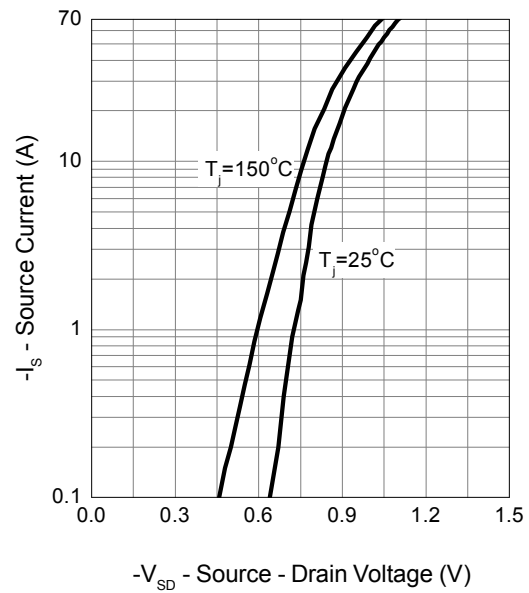
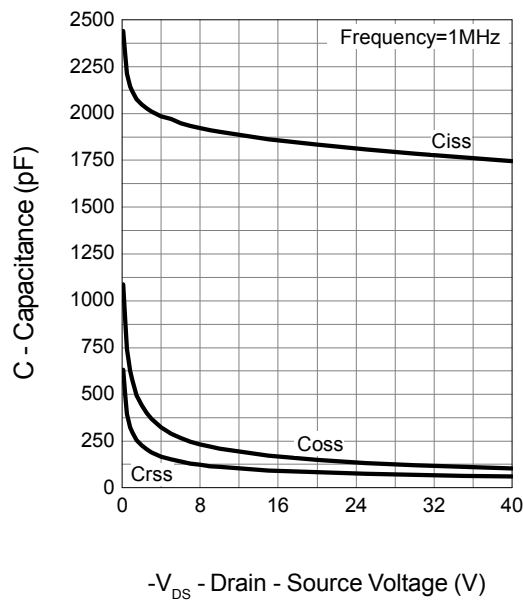
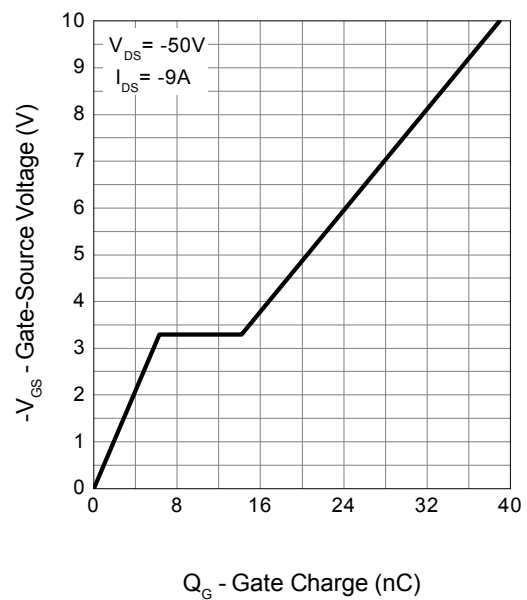
Typical Operating Characteristics



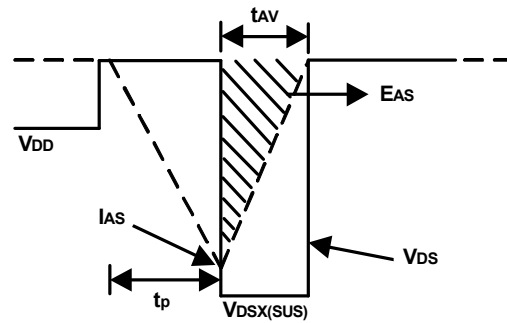
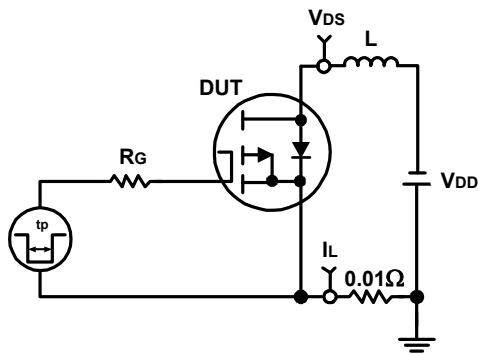
Typical Operating Characteristics (Cont.)

Output Characteristics

Drain-Source On Resistance

Gate-Source On Resistance

Gate Threshold Voltage


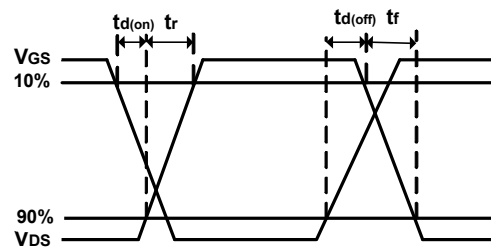
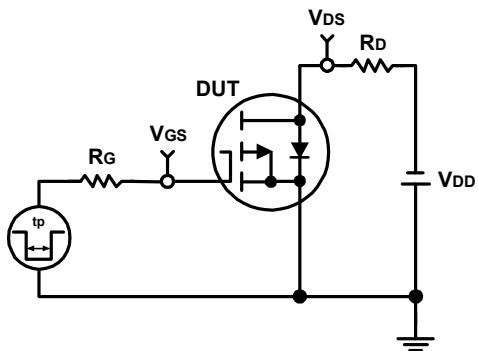
Typical Operating Characteristics (Cont.)

Drain-Source On Resistance

Source-Drain Diode Forward

Capacitance

Gate Charge


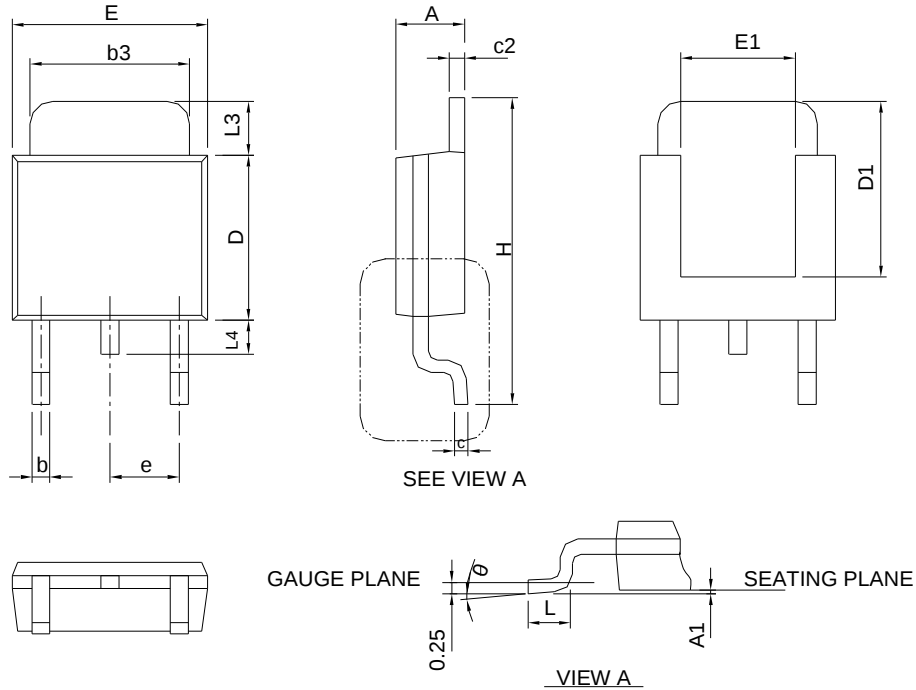
Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



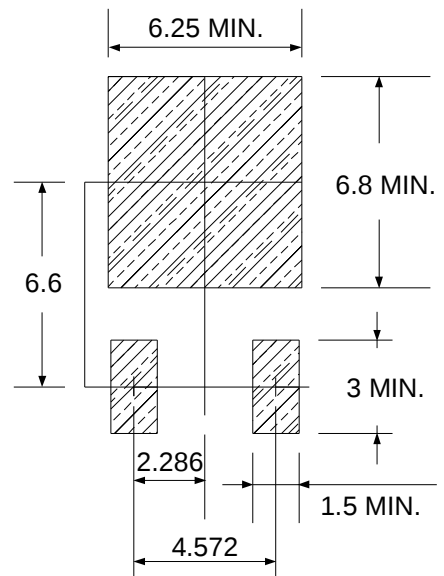
Package Information



SYMBOL	TO-252-2			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.39	0.086	0.094
A1	-	0.13	-	0.005
b	0.50	0.89	0.020	0.035
b3	4.95	5.46	0.195	0.215
c	0.46	0.61	0.018	0.024
c2	0.46	0.89	0.018	0.035
D	5.33	6.22	0.210	0.245
D1	4.57	6.00	0.180	0.236
E	6.35	6.73	0.250	0.265
E1	3.81	6.00	0.150	0.236
e	2.29 BSC		0.090 BSC	
H	9.40	10.41	0.370	0.410
L	0.90	1.78	0.035	0.070
L3	0.89	2.03	0.035	0.080
L4	-	1.02	-	0.040
θ	0°	8°	0°	8°

Note : Follow JEDEC TO-252 .

RECOMMENDED LAND PATTERN



UNIT: mm