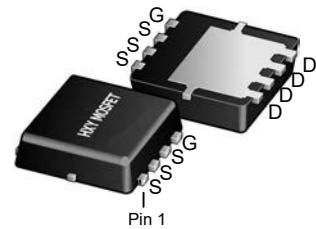




## Description

The HSISH101DNT1GE3 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

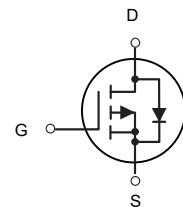


## General Features

$V_{DS} = -30V$   $I_D = -70A$

$R_{DS(ON)} < 9.3m\Omega$  @  $V_{GS} = -10V$

DFN3X3-8L



## Application

Battery protection

P-Channel MOSFET

Load switch

Uninterruptible power supply

## Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
HSISH101DNT1GE3	DFN3X3-8L	HXY MOSFET	5000

## Absolute Maximum Ratings ( $T_c = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	-70	A
$I_D @ T_c = 75^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	-35	A
$IDM$	Pulsed Drain Current <sup>2</sup>	-175	A
$EAS$	Single Pulse Avalanche Energy <sup>3</sup>	31	mJ
$P_D @ T_c = 25^\circ C$	Total Power Dissipation <sup>4</sup>	31.2	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	4	$^\circ C/W$



**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA	-30	-	-	V
Gate-body Leakage current	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -24V, V <sub>GS</sub> = 0V	-	-	-1	μA
			-	-	-5	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	-1.0	-1.6	-2.5	V
Drain-Source On-Resistance <sup>2</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = -10V, I <sub>D</sub> = -12A	-	6.5	9.3	mΩ
		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -8A	-	9.5	14.5	
Forward Transconductance	g <sub>f</sub>	V <sub>DS</sub> = -5V, I <sub>D</sub> = -20A	-	28	-	S
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = -15V, V <sub>GS</sub> = 0V, f = 1MHz	-	4320	-	pF
Output Capacitance	C <sub>oss</sub>		-	529	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	487	-	
<b>Switching Characteristics</b>						
Gate Resistance	R <sub>g</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz	-	4.0	-	Ω
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> = -10V, V <sub>DS</sub> = -15V, I <sub>D</sub> = -15A	-	45	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	8.5	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	12.8	-	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> = -10V, V <sub>DD</sub> = -15V, R <sub>G</sub> = 2.5Ω, I <sub>D</sub> = -15A	-	18.9	-	nS
Rise Time	t <sub>r</sub>		-	15.7	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	64.8	-	
Fall Time	t <sub>f</sub>		-	36.5	-	
<b>Drain-Source Body Diode Characteristics</b>						
Diode Forward Voltage <sup>2</sup>	V <sub>SD</sub>	I <sub>S</sub> = -1A, V <sub>GS</sub> = 0V	-	-	-1	V
Continuous Source Current <sup>1,5</sup>	I <sub>S</sub>	V <sub>G</sub> = V <sub>D</sub> = 0V, Force Current	-	-	-65	A

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V<sub>DD</sub>= -25V, V<sub>GS</sub>= -10V, L= 0.1mH, I<sub>AS</sub>= -25A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.



## Typical Characteristics

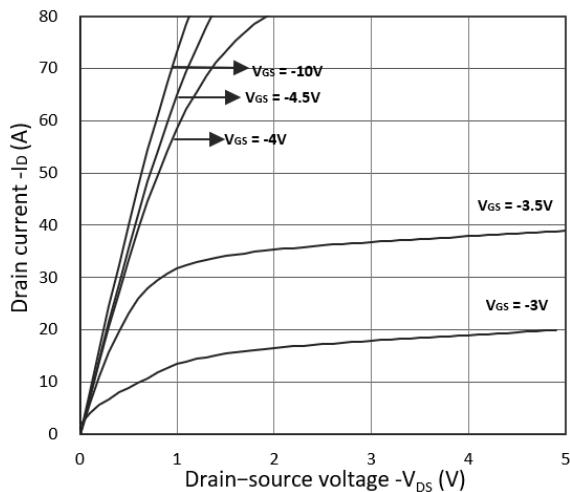


Figure 1. Output Characteristics

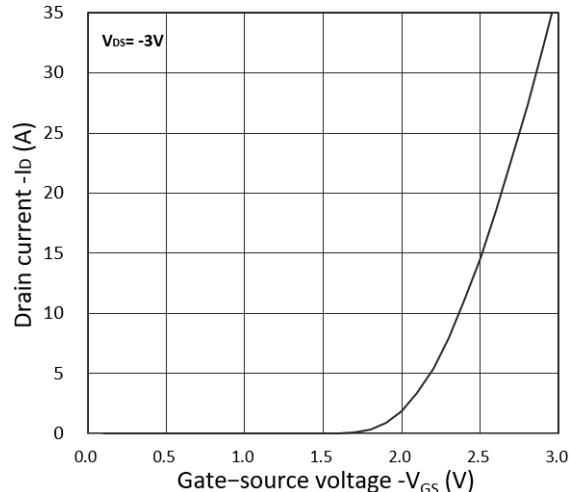


Figure 2. Transfer Characteristics

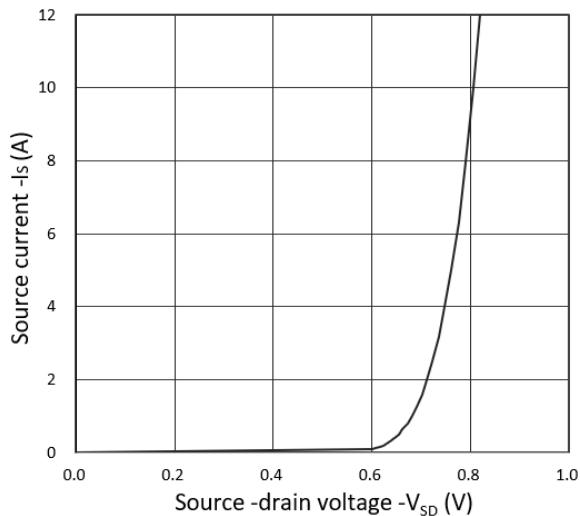


Figure 3. Forward Characteristics of Reverse

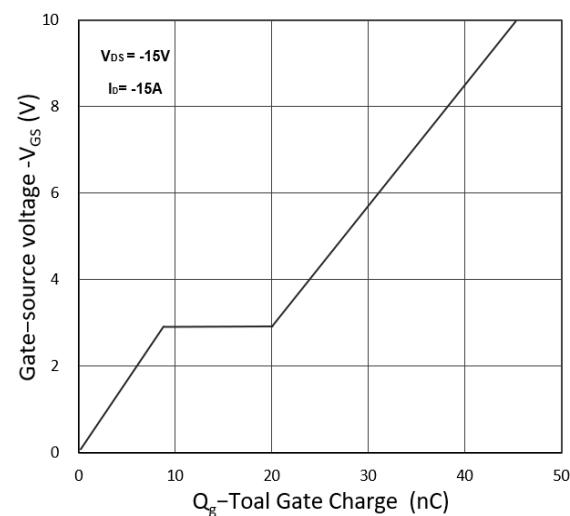


Figure 4. Gate Charge Characteristics

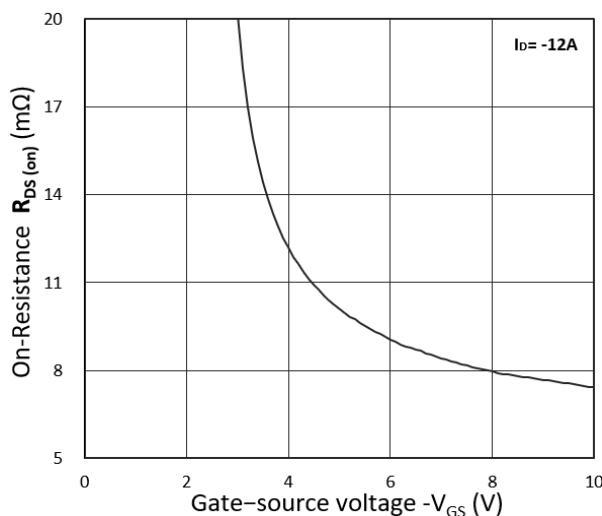


Figure 5.  $R_{DS(on)}$  vs.  $V_{GS}$

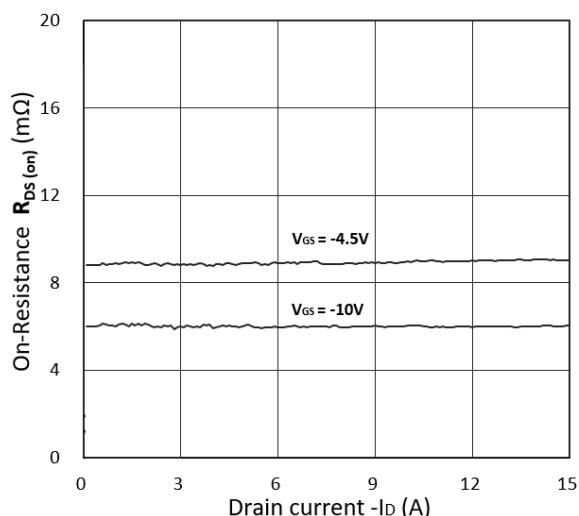


Figure 6.  $R_{DS(on)}$  vs.  $I_D$

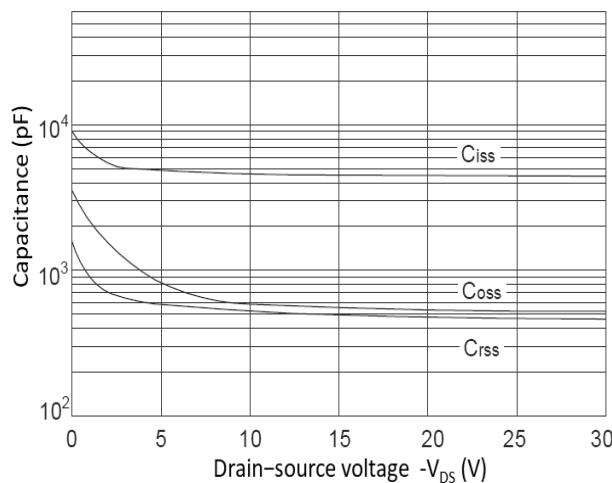


Figure 7. Capacitance Characteristics

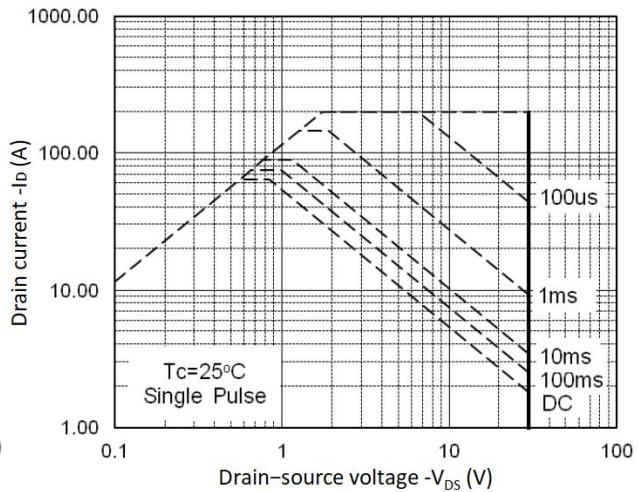


Figure 8. Safe Operating Area

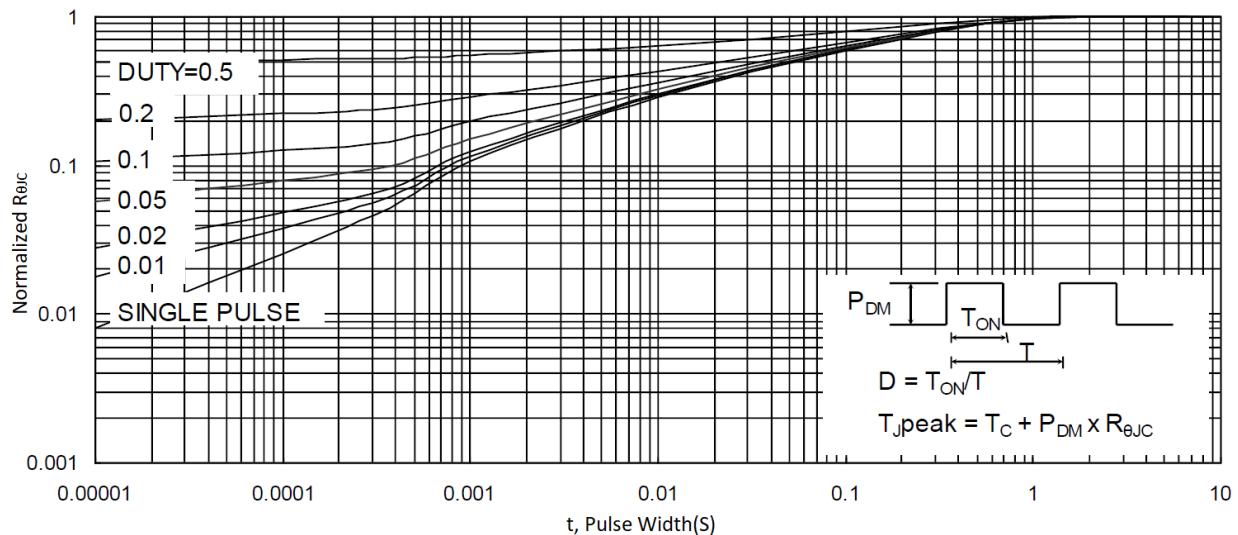


Figure 9. Normalized Maximum Transient Thermal Impedance

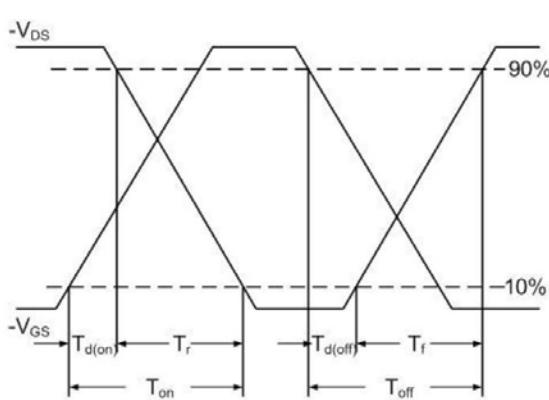


Figure 10. Switching Time Waveform

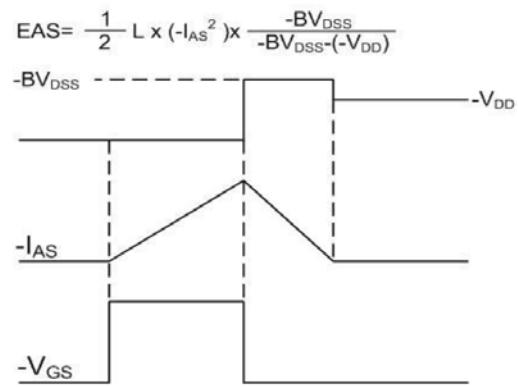


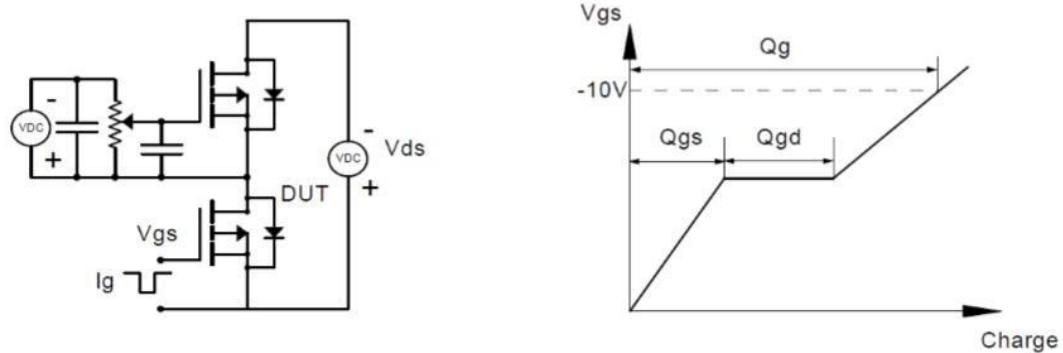
Figure 11. Unclamped Inductive Switching

Waveform

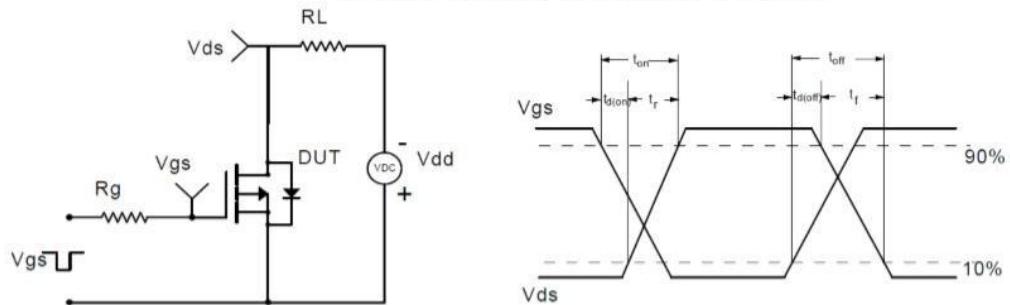


## Test Circuit

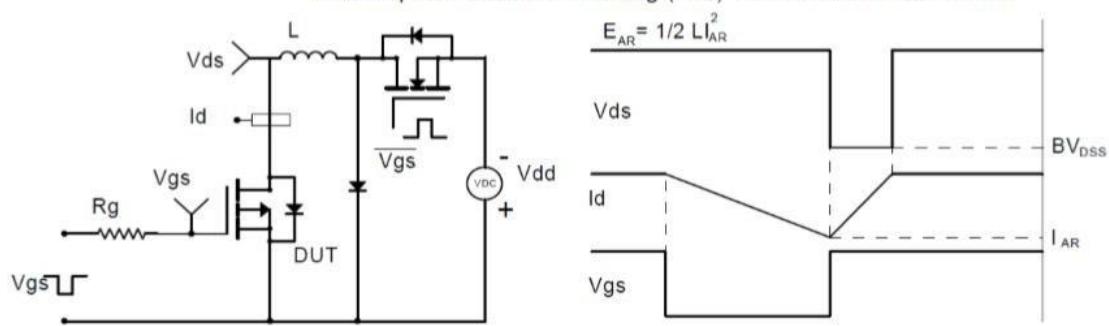
Gate Charge Test Circuit & Waveform



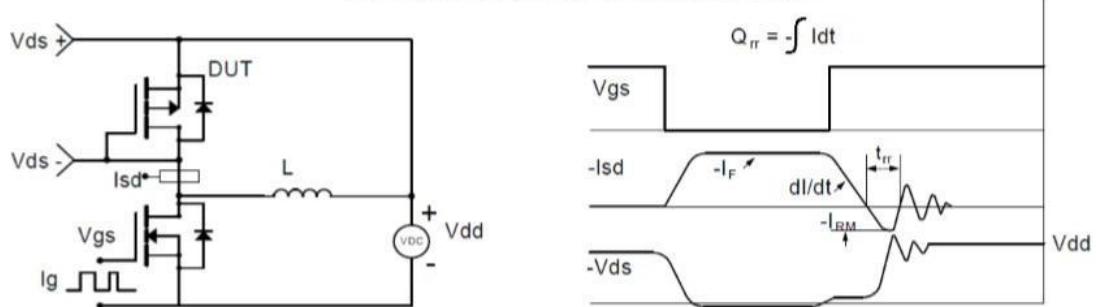
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

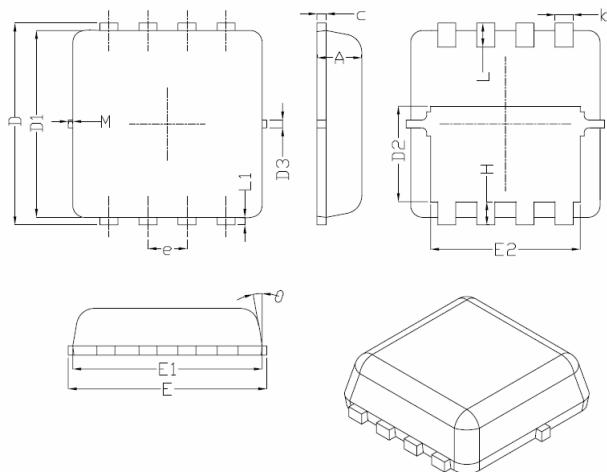


Diode Recovery Test Circuit & Waveforms





### DFN3X3-8L Package Information



Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max.
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.48	1.58	1.68
D3	-	0.13	-
E	3.20	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	-	0.13	-
M	*	*	0.15
θ		10°	12°



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