

LESD8D15CT5G ESD PROTECTION DIODE

Discription

The LESD8D15CT5G is designed to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium. Because of its small size, it is suited for use in cellular phones, digital cameras and many other portable applications where board space is at a premium.

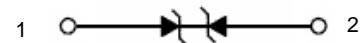
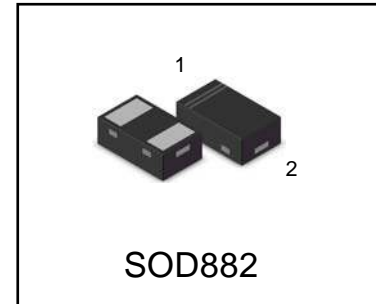
Applications

- | Cellular phones audio
- | Digital cameras
- | Portable application
- | Mobile telephone

Features

- | Low Leakage
- | Response Time is Typically < 1 ns
- | IEC61000-4-2 Level 4 ESD Protection
- | These are Pb-Free Devices
- | We declare that the material of product compliance with RoHS requirements.

LESD8D15CT5G



Ordering information

Device	Marking	Shipping
LESD8D15CT5G	5T	10000/Tape&Reel

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
IEC 61000-4-2 (ESD) Air Contact Contact discharge		±20 ±20	kV kV
Total Power Dissipation on FR-5 Board (Note 1) @ T _A =25°C	PD	200	mW
Junction and Storage Temperature Range	T _J ,T _{STG}	-55 to 150	°C
Lead Solder Temperature – Maximum (10 Second Duration)	TL	260	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Rating are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

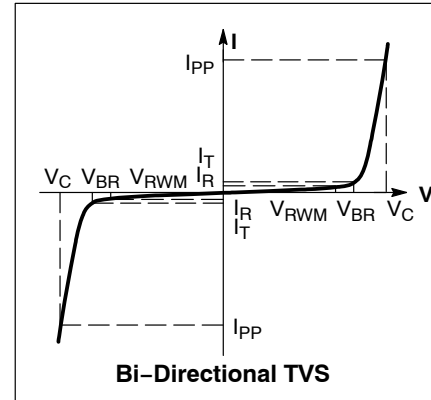
1. FR-5 = 1.0*0.75*0.62 in.

LESD8D15CT5G

ELECTRICAL CHARACTERISTICS

($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Working Peak Reverse Voltage
I_R	Maximum Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
P_{pk}	Peak Power Dissipation
C	Capacitance @ $V_R = 0$ and $f = 1.0$ MHz



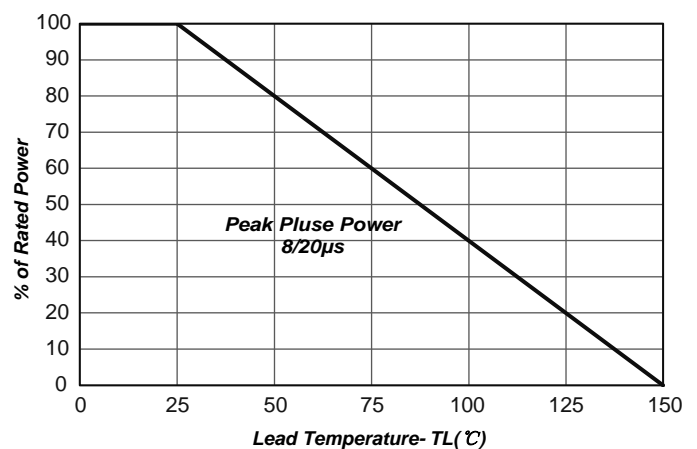
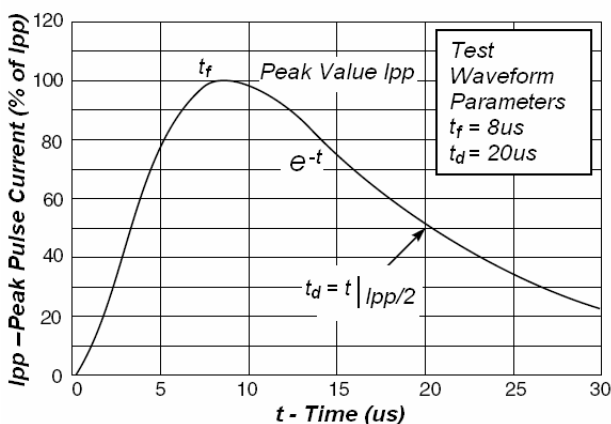
ELECTRICAL CHARACTERISTICS

Device	V_{RWM} (V)	$I_{R1}(\mu\text{A})$ @ V_{RWM}	V_{BR} (V) @ I_T (Note 2)		I_T	V_C (V) @ $I_{PP} = 4$ A (Note 3)	V_C (V) @ MAX I_{PP} (Note 3)	I_{PP} (A) (Note 3)	P_{PK} (W) (Note 3)	C (pF)
	Max	Max	Min	Max	mA	Max	Max	Max	Max	Max
LESD8D15CT5G	15	0.3	16	19.5	1.0	25	27	5	135	45

Other voltage available upon request.

2. V_{BR} is measured with a pulse test current I_T at an ambient temperature of 25°C

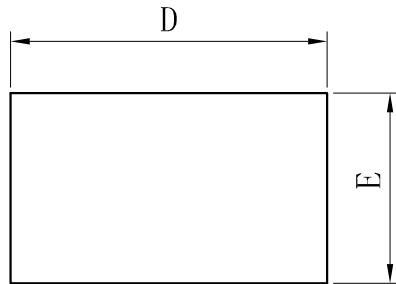
3. Surge current waveform per Figure 1.



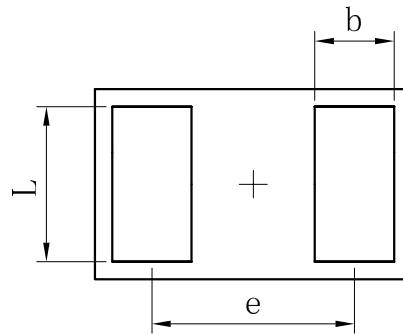
LESD8D15CT5G

OUTLINE AND DIMENSIONS

SOD882

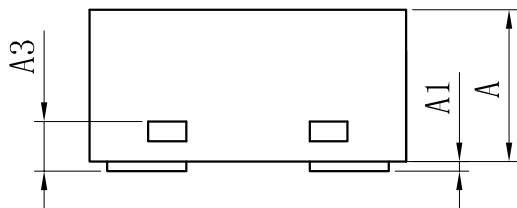


TOP VIEW



BOTTOM VIEW

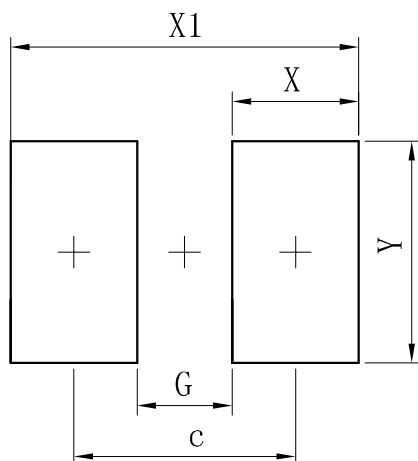
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Dim	Min	Typ	Max
D	0.95	1.00	1.05
E	0.55	0.60	0.65
e	—	0.64	—
L	0.44	0.49	0.54
b	0.20	0.25	0.30
A	0.43	0.48	0.53
A1	0	—	0.05
A3	0.127REF.		
All Dimensions in mm			



SIDE VIEW

SOLDERING FOOTPRINT

SOD882



Dimensions	(mm)
c	0.70
G	0.30
X	0.40
X1	1.10
Y	0.70