



Features

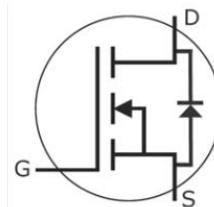
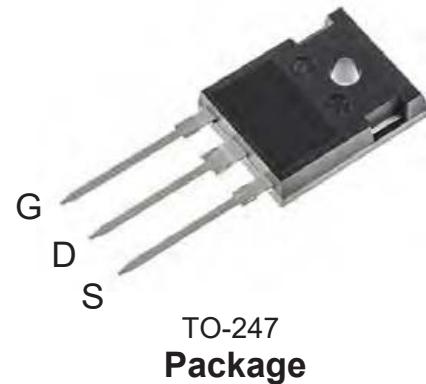
- 3rd generation SiC MOSFET technology
- Optimized package with separate driver source pin
- High blocking voltage with low on-resistance
- High-speed switching with low capacitances
- Fast intrinsic diode with low reverse recovery (Q_{rr})
- Halogen free, RoHS compliant

Benefits

- Reduce switching losses and minimize gate ringing
- Higher system efficiency
- Reduce cooling requirements
- Increase power density
- Increase system switching frequency

Applications

- Renewable energy
- EV battery chargers
- High voltage DC/DC converters
- Switch Mode Power Supplies



Ordering Part Number	Package	Marking
HC1M30065D	TO-247	HC1M30065D

Maximum Ratings ($T_c = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	650	V
Continuous drain current $V_{GS}=18\text{V}$ $T_c = 25^\circ\text{C}$ $V_{GS}=18\text{V}$ $T_c = 100^\circ\text{C}$	I_D	90 64	A
Pulsed drain current ($T_c = 25^\circ\text{C}$, t_p limited by T_{jmax})	$I_{D\text{ pulse}}$	241	A
Avalanche energy, single pulse ($L=10\text{mH}$)	E_{AS}	1125	mJ
Gate-Source voltage	V_{GSOP}	-4/+18	V
Gate-Source voltage (dynamic, Absolute maximum values)	V_{GSmax}	-8/+22	V
Power dissipation ($T_c = 25^\circ\text{C}$)	P_{tot}	428	W
Operating junction and storage temperature	T_j , T_{stg}	-55...+175	°C

- Example of acceptable V_{GS} waveform





Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal resistance, junction – case. Max	R_{thJC}	0.35	°C/W
Thermal resistance, junction – ambient. Max	R_{thJA}	40	

Electrical Characteristic (at $T_j = 25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

Static Characteristic

Drain-source breakdown voltage	BV_{DSS}	650	-	-	V	$\text{V}_{GS}=0\text{V}, \text{I}_D=250\text{uA}$
Gate threshold voltage	$\text{V}_{GS(\text{th})}$	2	-	4	V	$\text{V}_{DS}=\text{V}_{GS}, \text{I}_D=7.2\text{mA}$
Zero gate voltage drain current	I_{DSS}	-	1	100	μA	$\text{V}_{DS}=650\text{V}, \text{V}_{GS}=0\text{V}$
		-	10	-		$\text{T}_j=25^\circ\text{C}$
Gate-source leakage current	I_{GSS}	-		250	nA	$\text{V}_{GS}=18\text{V}, \text{V}_{DS}=0\text{V}$
Drain-source on-state resistance	$\text{R}_{DS(\text{on})}$	-	30	40	$\text{m}\Omega$	$\text{V}_{GS}=18\text{V}, \text{I}_D=26.4\text{A},$
		-	39	-		$\text{T}_j=25^\circ\text{C}$
						$\text{T}_j=175^\circ\text{C}$
Transconductance	g_{fs}	-	9	-	S	$\text{V}_{DS}=20\text{V}, \text{I}_D=13.2\text{A}$

Dynamic Characteristic

Input Capacitance	C_{iss}	-	2079	-	pF	$\text{V}_{DS} = 650\text{V}$ $\text{V}_{GS} = 0\text{V}$ $\text{T}_j = 25^\circ\text{C}$ $\text{V}_{AC} = 25\text{mV}$ $f = 1\text{MHz}$
Output Capacitance	C_{oss}	-	180	-		
Reverse Transfer Capacitance	C_{rss}	-	22	-		
Gate Total Charge	Q_G	-	98	-	nC	$\text{V}_{DS} = 400\text{V}$ $\text{V}_{GS} = 0/18\text{V}$ $\text{I}_D = 26.4\text{A}$
Gate-Source charge	Q_{gs}	-	23.2	-		
Gate-Drain charge	Q_{gd}	-	41.2	-		
Turn-On Switching Energy	E_{ON}	-	127	-	μJ	$\text{V}_{DD} = 400\text{V}$ $\text{V}_{GS} = -4/+18\text{V}$ $\text{I}_D = 26.2\text{A}$ $\text{R}_G = 5\Omega$ $\text{L} = 100\text{uH}$
Turn-Off Switching Energy	E_{OFF}	-	41	-		
Turn-on delay time	$\text{t}_{d(on)}$	-	17.6	-		
Rise time	t_r	-	7	-	ns	
Turn-off delay time	$\text{t}_{d(off)}$	-	29.4	-		
Fall time	t_f	-	10	-		
Gate resistance	R_G	-	1.0	-	Ω	$\text{V}_{AC} = 25\text{mV}, f=1\text{MHz}$



Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}		3.2		V	$V_{GS}=0V, I_{SD}=13.2A, T_J=25^{\circ}C$
			3.3			$V_{GS}=0V, I_{SD}=13.2A, T_J=175^{\circ}C$
Continuous Diode Forward Current	I_S		83		A	$V_{GS}=-4V, T_C=25^{\circ}C$
Body Diode Reverse Recovery Time	t_{rr}	-	35	-	ns	$V_R = 400V, I_D = 26.4A$
Body Diode Reverse Recovery Charge	Q_{rr}	-	161	-	nC	$di/dt = 1000A/\mu S$



Typical Performance Characteristics

Fig 1. Output Characteristic ($T_J = -55^\circ\text{C}$)

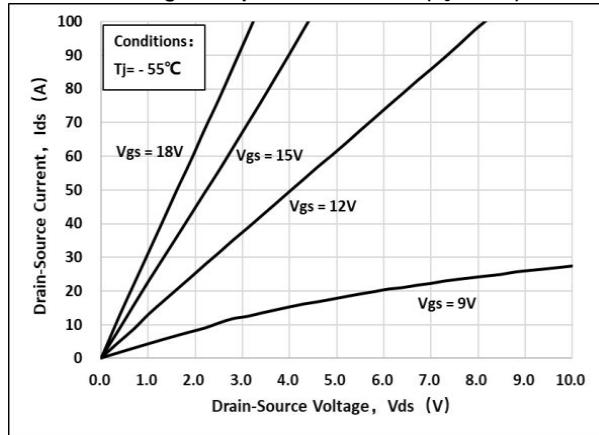


Fig 2. Output Characteristic ($T_J = 25^\circ\text{C}$)

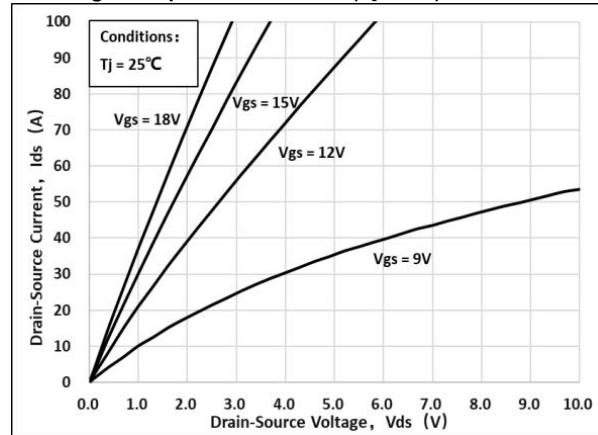


Fig 3. Output Characteristic ($T_J = 175^\circ\text{C}$)

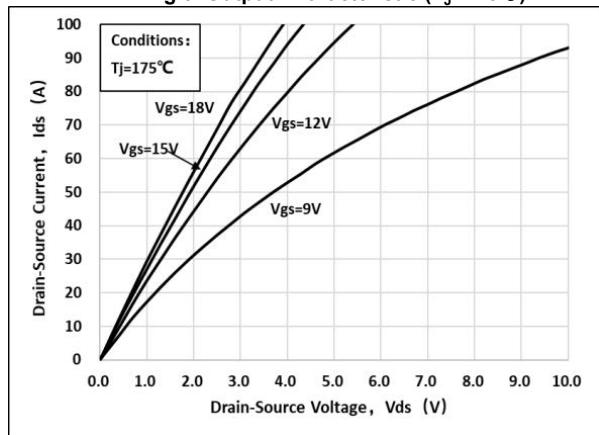


Fig 4: R_{dson} Vs Id_s Characteristic

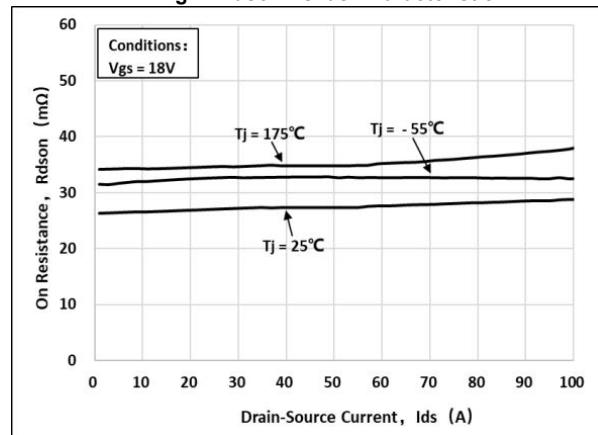


Fig 5: $R_{ds(on)}$ vs. Temperature

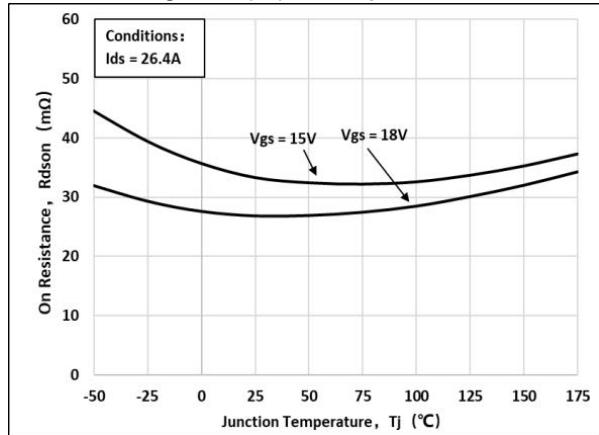


Fig 6: Transfer Characteristic

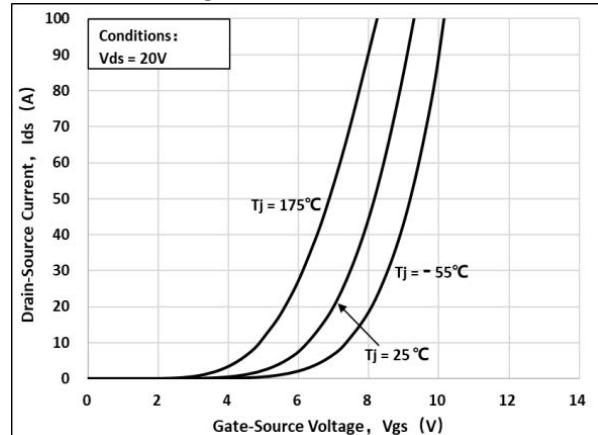




Fig 7: Body-diode Characteristic ($T_j = -55^\circ\text{C}$)

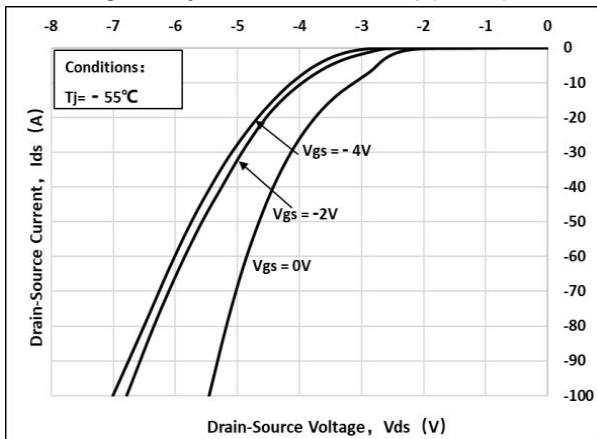


Fig 8: Body-diode Characteristic ($T_j = 25^\circ\text{C}$)

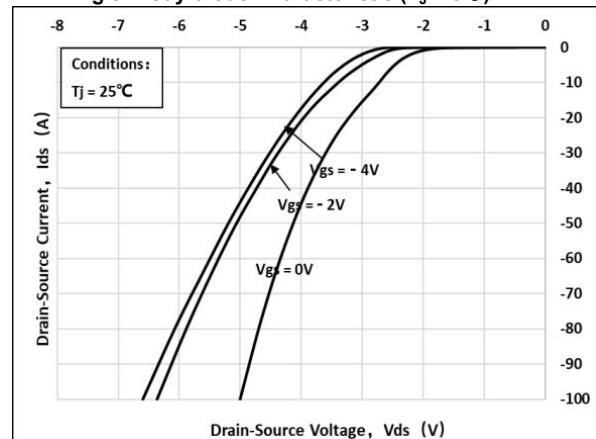


Fig 9: Body-diode Characteristic ($T_j = 175^\circ\text{C}$)

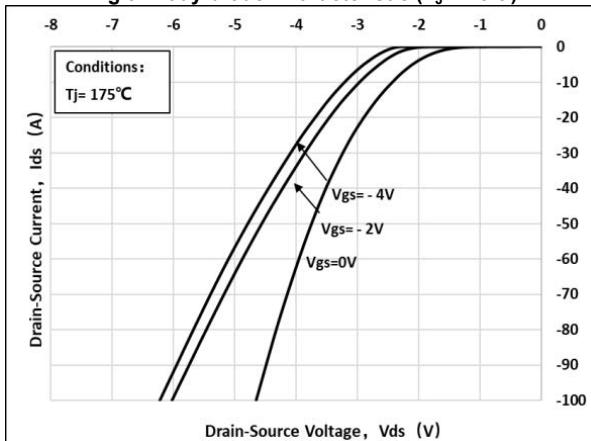


Fig 10: V_{TH} Vs T Temperature Characteristic

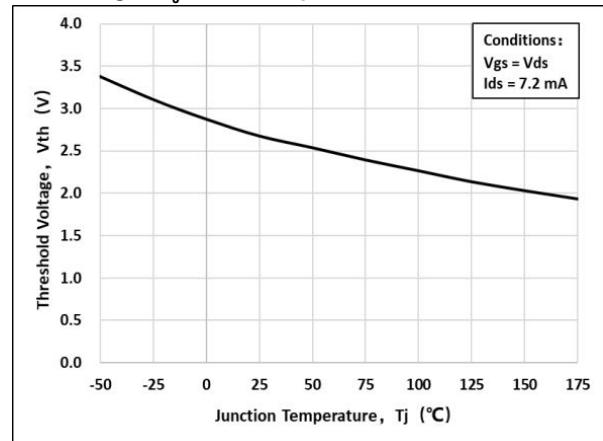


Fig 11: Gate Charge Characteristics

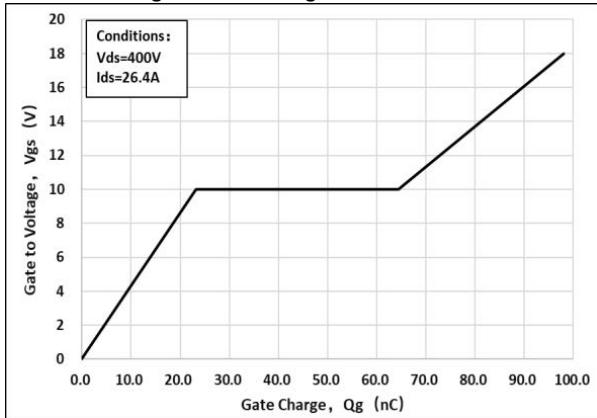


Fig 12: 3rd Quadrant Characteristic ($T_j = -55^\circ\text{C}$)

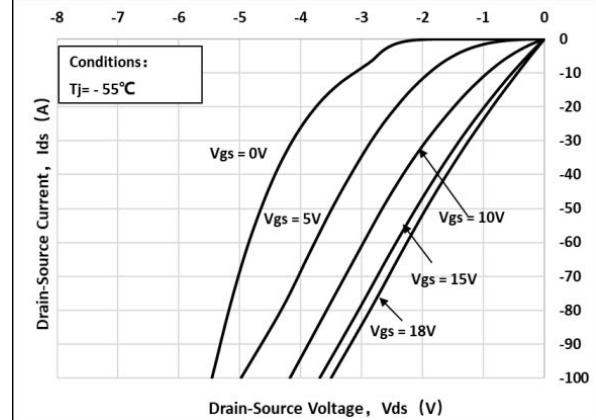




Fig 13: 3rd Quadrant Characteristic($T_j=25^\circ\text{C}$)

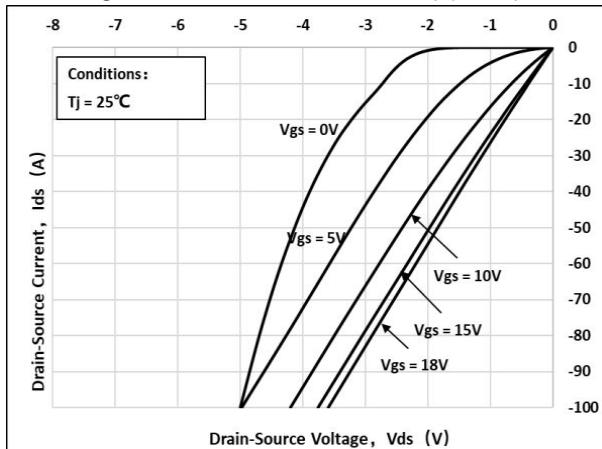


Fig 14: 3rd Quadrant Characteristic($T_j=175^\circ\text{C}$)

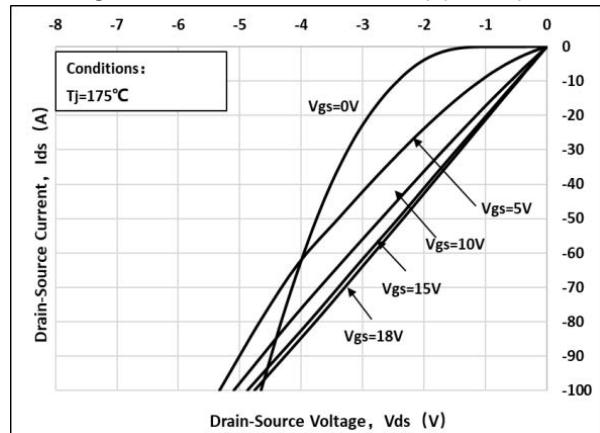


Fig 15: Capacitance Characteristic

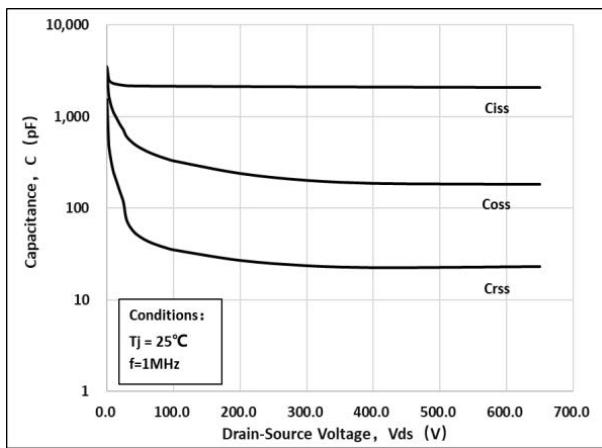


Fig 16: Safe Operating Area

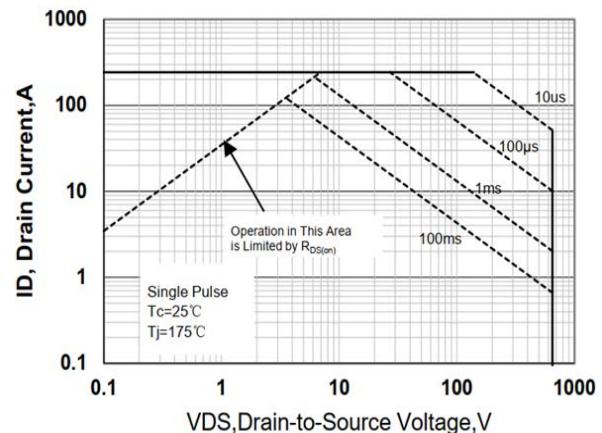
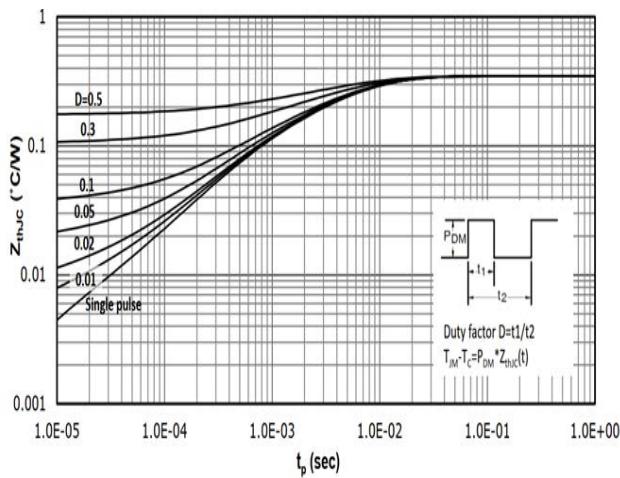


Fig 17: Transient Thermal Impedance





Test Circuit & Waveform

Figure A. Definition of switching times

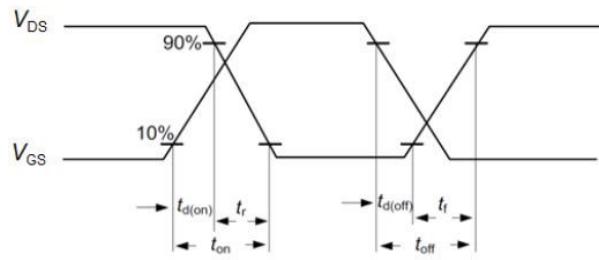


Figure B. Dynamic test circuit

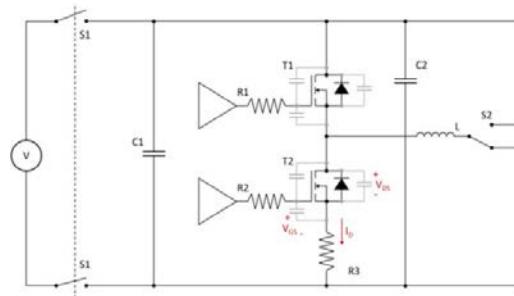
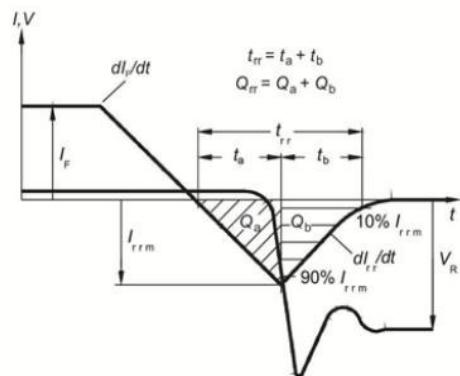


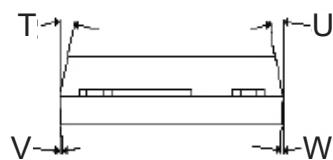
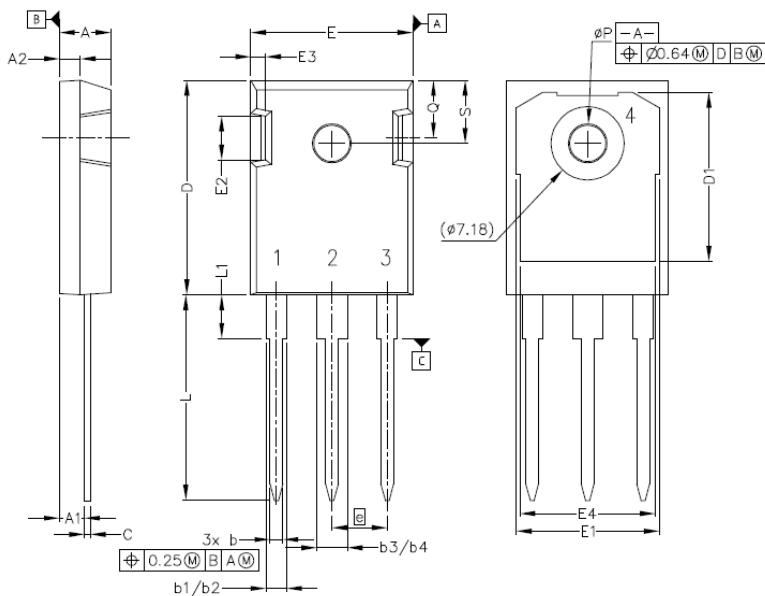
Figure C. Definition of body diodeswitching characteristics





Package Dimensions

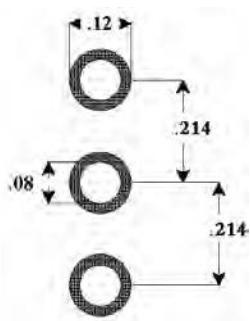
Package TO-247



Pinout Information:

- Pin 1 = Gate
- Pin 2, 4 = Drain
- Pin 3 = Source

Recommended Solder Pad Layout



TO-247

POS	Inches		Millimeters	
	Min	Max	Min	Max
A	.190	.205	4.83	5.21
A1	.090	.100	2.29	2.54
A2	.075	.085	1.91	2.16
b	.042	.052	1.07	1.33
b1	.075	.095	1.91	2.41
b2	.075	.085	1.91	2.16
b3	.113	.133	2.87	3.38
b4	.113	.123	2.87	3.13
c	.022	.027	0.55	0.68
D	.819	.831	20.80	21.10
D1	.640	.695	16.25	17.65
D2	.037	.049	0.95	1.25
E	.620	.635	15.75	16.13
E1	.516	.557	13.10	14.15
E2	.145	.201	3.68	5.10
E3	.039	.075	1.00	1.90
E4	.487	.529	12.38	13.43
e	.214 BSC		5.44 BSC	
N	3		3	
L	.780	.800	19.81	20.32
L1	.161	.173	4.10	4.40
ØP	.138	.144	3.51	3.65
Q	.216	.236	5.49	6.00
S	.238	.248	6.04	6.30
T	9°	11°	9°	11°
U	9°	11°	9°	11°
V	2°	8°	2°	8°
W	2°	8°	2°	8°



Attention

- Any and all HUA XUAN YANG ELECTRONICS products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your HUA XUAN YANG ELECTRONICS representative nearest you before using any HUA XUAN YANG ELECTRONICS products described or contained herein in such applications.
- HUA XUAN YANG ELECTRONICS assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein.
- Specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- HUA XUAN YANG ELECTRONICS CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all HUA XUAN YANG ELECTRONICS products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HUA XUAN YANG ELECTRONICS CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. HUA XUAN YANG ELECTRONICS believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the HUA XUAN YANG ELECTRONICS product that you intend to use.