

IRFR9310PBF-VB Datasheet

P-Channel 500V (D-S) Power MOSFET

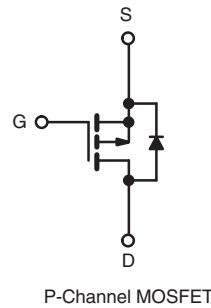
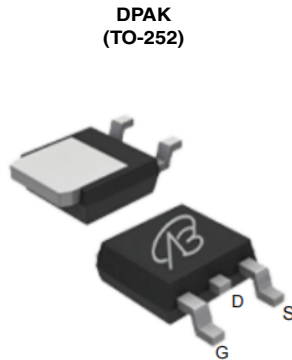
PRODUCT SUMMARY		
V_{DS} (V)	- 500	
$R_{DS(on)}$ (Ω)	$V_{GS} = -10$ V	4.0
Q_g (Max.) (nC)	13	
Q_{gs} (nC)	3.2	
Q_{gd} (nC)	5.0	
Configuration	Single	

FEATURES

- P-Channel
- Surface Mount
- Straight Lead
- Advanced Process Technology
- Fast Switching
- Fully Avalanche Rated



RoHS
COMPLIANT
HALOGEN
FREE
Available



ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER			SYMBOL	LIMIT	UNIT
Drain-Source Voltage			V _{DS}	- 500	V
Gate-Source Voltage			V _{GS}	± 20	
Continuous Drain Current	V _{GS} at - 10 V	T _C = 25 °C	I _D	- 4.0	A
		T _C = 100 °C		- 3.1	
Pulsed Drain Current ^a			I _{DM}	-15	
Linear Derating Factor				0.40	W/°C
Single Pulse Avalanche Energy ^b			E _{AS}	92	mJ
Repetitive Avalanche Current ^a			I _{AR}	- 4	A
Repetitive Avalanche Energy ^a			E _{AR}	15.0	mJ
Maximum Power Dissipation	T _C = 25 °C		P _D	80	W
Peak Diode Recovery dV/dt ^c			dV/dt	- 24	V/ns
Operating Junction and Storage Temperature Range			T _J , T _{stg}	- 55 to + 150	°C
Soldering Recommendations (Peak Temperature) ^d	for 10 s			300	

Notes

- Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- Starting $T_J = 25^\circ\text{C}$, $L = 57$ mH, $R_g = 25\ \Omega$, $I_{AS} = -1.8$ A (see fig. 12).
- $I_{SD} \leq -1.1$ A, $dI/dt \leq 450$ A/ μs , $V_{DD} \leq V_{DS}$, $T_J \leq 150^\circ\text{C}$.
- 1.6 mm from case.

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
Maximum Junction-to-Ambient	R_{thJA}	-	-	110	°C/W
Maximum Junction-to-Ambient (PCB Mount) ^a	R_{thJA}	-	-	50	
Maximum Junction-to-Case (Drain)	R_{thJC}	-	-	2.5	

Note

a. When mounted on 1" square PCB (FR-4 or G-10 material).

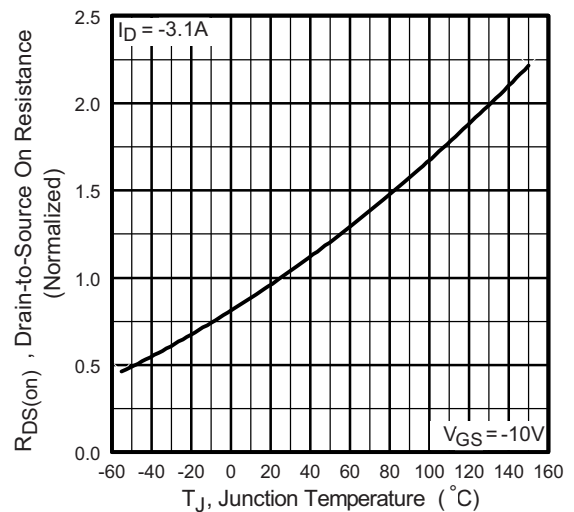
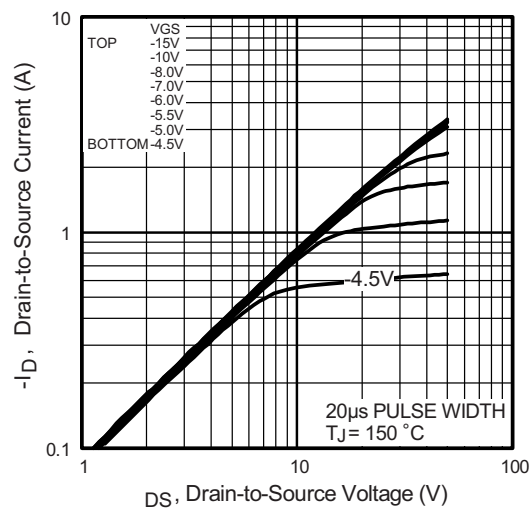
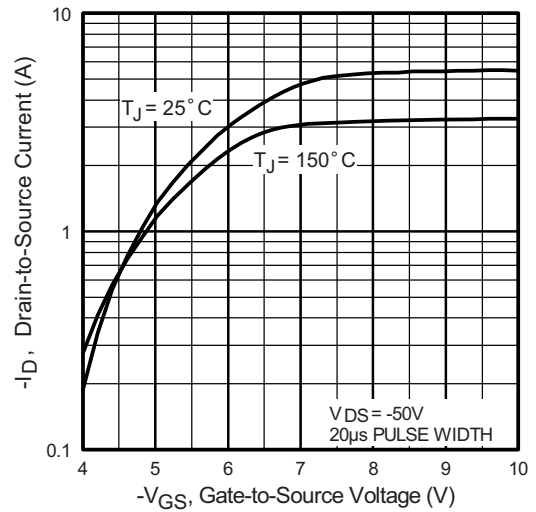
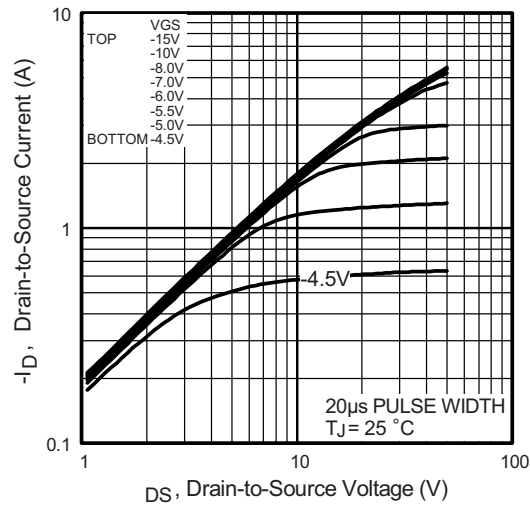
SPECIFICATIONS ($T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

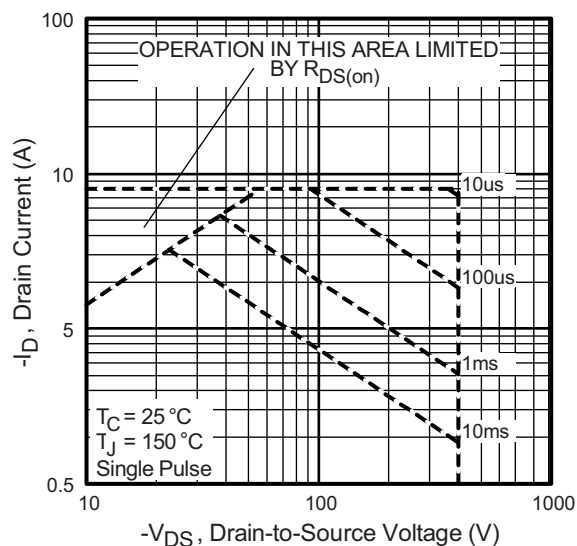
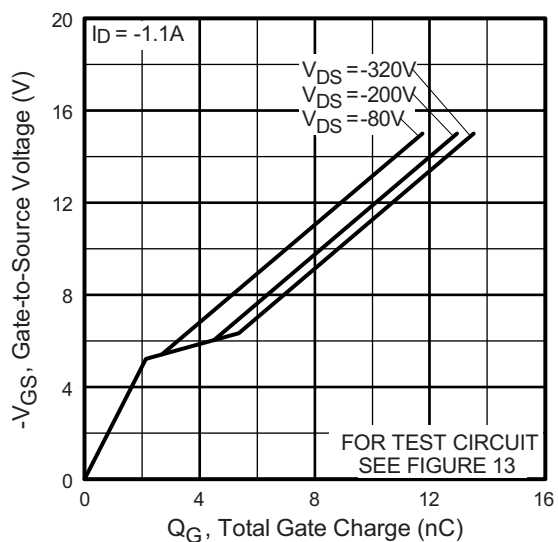
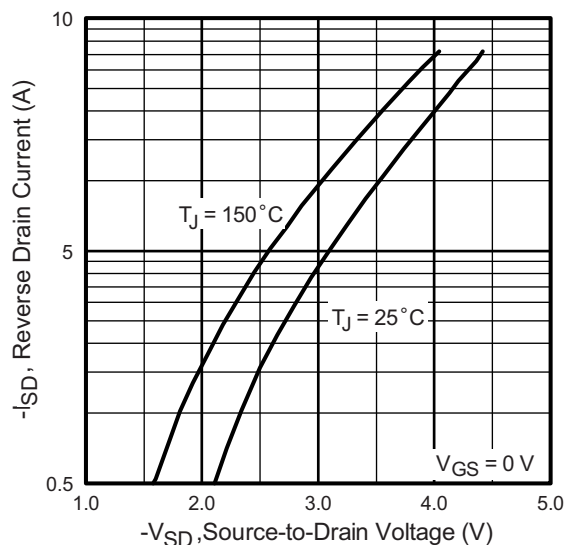
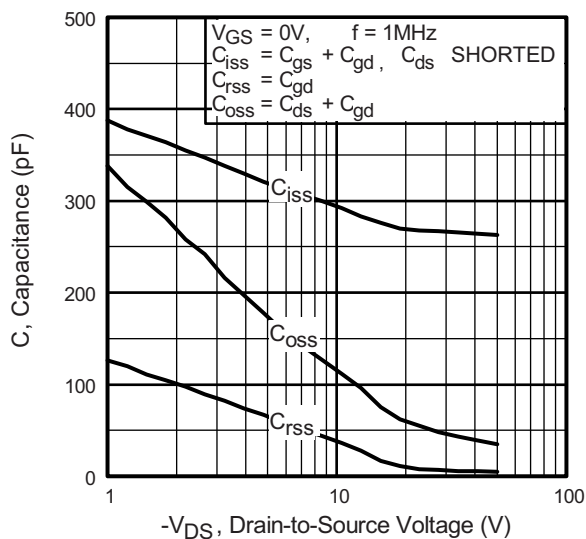
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA		- 500	-	-	V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	Reference to 25 °C, I _D = - 1 mA		-	- 0.41	-	V/°C
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA		- 2.0	-	- 4.0	V
Gate-Source Leakage	I _{GSS}	V _{GS} = ± 20 V		-	-	± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 500 V, V _{GS} = 0 V		-	-	- 100	μA
		V _{DS} = - 400 V, V _{GS} = 0 V, T _J = 125 °C		-	-	- 500	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = - 10 V	I _D = - 3.1 A ^b	-	4	-	Ω
Forward Transconductance	g _{fs}	V _{DS} = - 50 V, I _D = - 3.1 A		0.91	-	-	S
Dynamic							
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = - 25 V, f = 1.0 MHz, see fig. 5		-	940	-	pF
Output Capacitance	C _{oss}			-	50	-	
Reverse Transfer Capacitance	C _{rss}			-	8.0	-	
Total Gate Charge	Q _g	V _{GS} = - 10 V	I _D = - 1.1 A, V _{DS} = - 320 V, see fig. 6 and 13 ^b	-	-	13	nC
Gate-Source Charge	Q _{gs}			-	-	3.2	
Gate-Drain Charge	Q _{gd}			-	-	5.0	
Turn-On Delay Time	t _{d(on)}	V _{DD} = - 200 V, I _D = - 1.1 A, R _g = 21 Ω, R _D = 180 Ω, see fig. 10 ^b		-	11	-	ns
Rise Time	t _r			-	10	-	
Turn-Off Delay Time	t _{d(off)}			-	25	-	
Fall Time	t _f			-	24	-	
Internal Drain Inductance	L _D	Between lead, 6 mm (0.25") from package and center of die contact ^c 		-	4.5	-	nH
Internal Source Inductance	L _S			-	7.5	-	
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	I _S	MOSFET symbol showing the integral reverse p - n junction diode 		-	-	- 1.9	A
Pulsed Diode Forward Current ^a	I _{SM}			-	-	- 7.6	
Body Diode Voltage	V _{SD}	T _J = 25 °C, I _S = - 1.1 A, V _{GS} = 0 V ^b		-	-	- 4.0	V
Body Diode Reverse Recovery Time	t _{rr}	T _J = 25 °C, I _F = -1.1 A, dI/dt = 100 A/μs ^b		-	170	260	ns
Body Diode Reverse Recovery Charge	Q _{rr}			-	640	960	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by L _S and L _D)					

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
 b. Pulse width $\leq 300\text{ }\mu\text{s}$; duty cycle $\leq 2\%$.
 c. This is applied for IPAK, L_S of DPAK is measured between lead and center of die contact.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





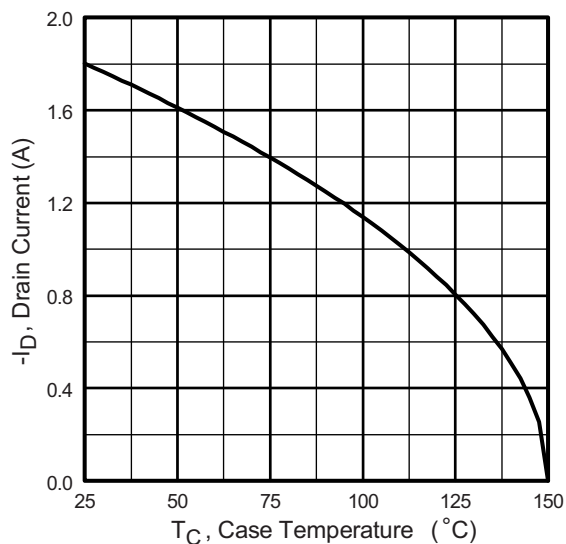


Fig. 9 - Maximum Drain Current vs. Case Temperature

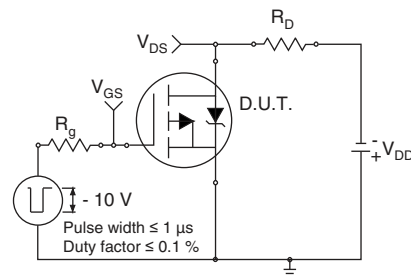


Fig. 10a - Switching Time Test Circuit

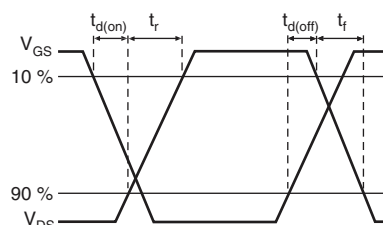


Fig. 10b - Switching Time Waveforms

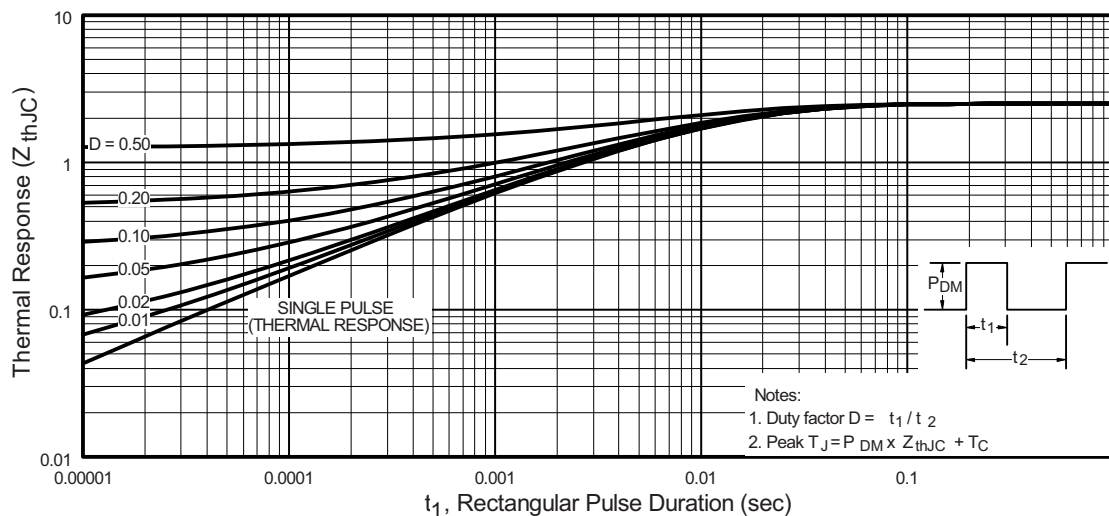


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

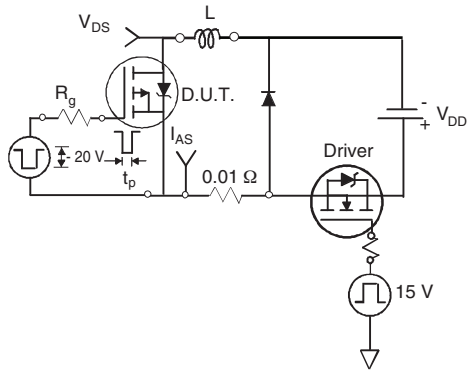


Fig. 12a - Unclamped Inductive Test Circuit

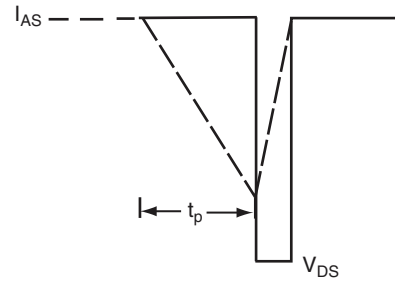


Fig. 12b - Unclamped Inductive Waveforms

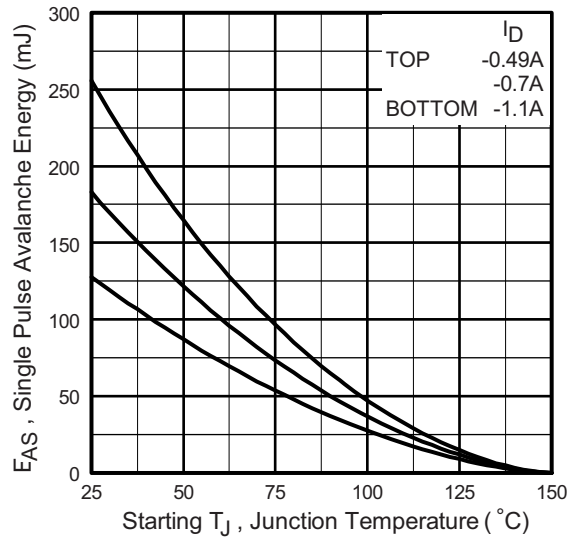


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

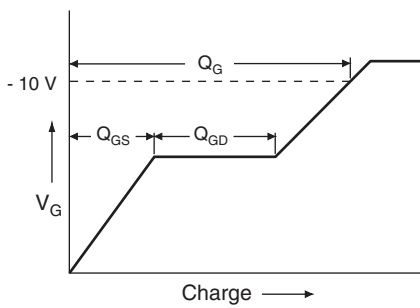


Fig. 13a - Basic Gate Charge Waveform

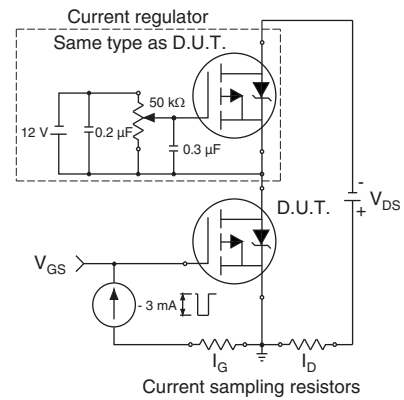
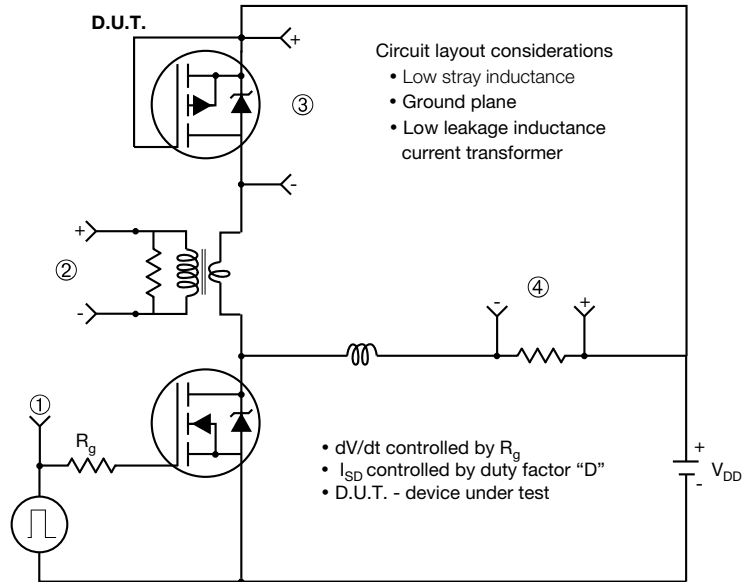


Fig. 13b - Gate Charge Test Circuit

Peak Diode Recovery dV/dt Test Circuit



Note
• Compliment N-Channel of D.U.T. for driver

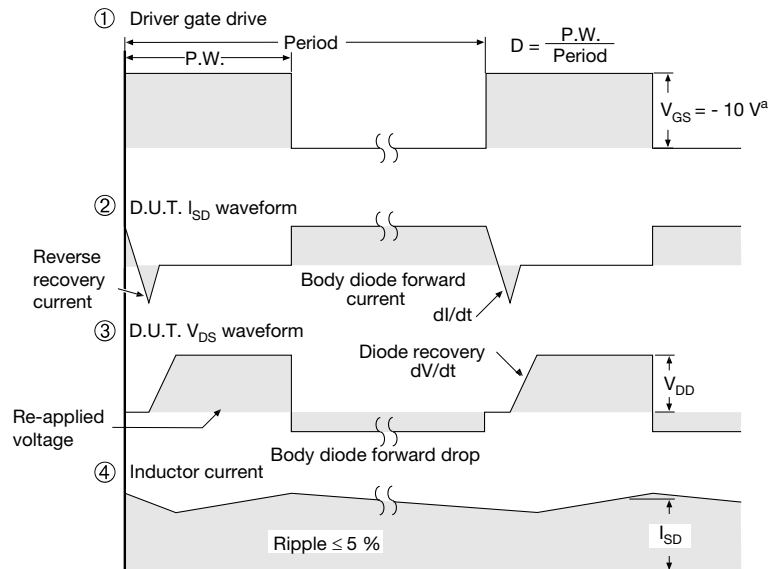
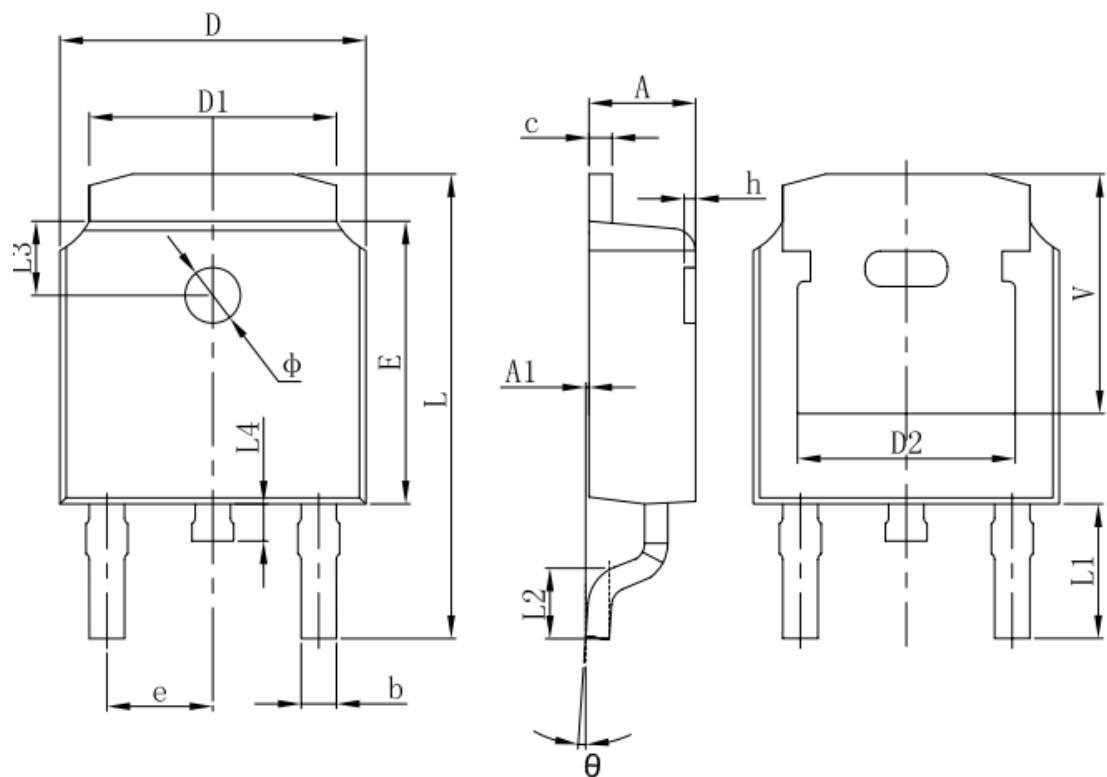


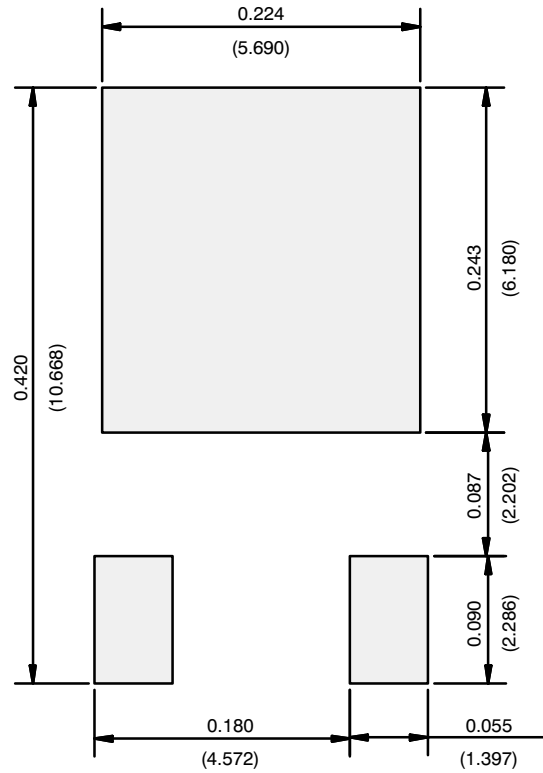
Fig. 14 - For P-Channel

TO252 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.635	0.770	0.025	0.030
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 REF.		0.190 REF.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.712	10.312	0.382	0.406
L1	2.900 REF.		0.114 REF.	
L2	1.400	1.700	0.055	0.067
L3	1.600 REF.		0.063 REF.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.250 REF.		0.207 REF.	

RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads
Dimensions in Inches/(mm)

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