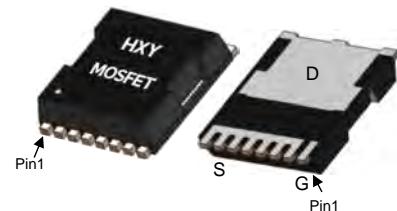




## General Description

The IPT015N10NF2SATMA1 use advanced SGT MOSFET technology to provide low RDS(ON), low gate charge, fast switching and excellent avalanche characteristics. This device is specially designed to get better ruggedness.



TOLL

## General Features

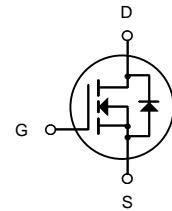
$V_{DS} = 100V$   $I_D = 350A$

$R_{DS(ON)} < 2m\Omega$  @  $V_{GS}=10V$

## Applications

Battery Protection

Power Distribution



N-Channel MOSFET

## Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
IPT015N10NF2SATMA1	TOLL	HXY MOSFET	2000

## Absolute Maximum Ratings at $T_j=25^\circ C$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	312	A
		200	
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	1248	A
Single Pulse Avalanche Energy <sup>2</sup>	EAS	1250	mJ
Total Power Dissipation	$P_D$	390.6	W
Operating Junction and Storage Temperature Range	$T_j, T_{STG}$	-55 to 150	°C
Thermal Resistance from Junction-to-Ambient <sup>3</sup>	$R_{\theta JA}$	39	°C/W
Thermal Resistance from Junction-to-Case	$R_{\theta JC}$	0.32	°C/W



**Electrical Characteristics (T<sub>J</sub> = 25°C, unless otherwise noted)**

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	100	-	-	V
Gate-body Leakage current	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T <sub>J</sub> =25°C	I <sub>DSS</sub>	V <sub>DS</sub> = 100V, V <sub>GS</sub> = 0V	-	-	1
	T <sub>J</sub> =100°C			-	-	100
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2	3	4	V
Drain-Source on-Resistance <sup>4</sup>	R <sub>DSS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A	-	1.4	2.0	mΩ
Forward Transconductance <sup>4</sup>	g <sub>f</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 20A	-	84	-	S
<b>Dynamic Characteristics<sup>5</sup></b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 50V, V <sub>GS</sub> = 0V, f = 1MHz	-	14300	-	pF
Output Capacitance	C <sub>oss</sub>		-	2120	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	50	-	
Gate Resistance	R <sub>g</sub>	f=1MHz	-	2.8	-	Ω
<b>Switching Characteristics<sup>5</sup></b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 50V, I <sub>D</sub> = 20A	-	250	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	53	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	77	-	
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 50V, R <sub>G</sub> = 3Ω, I <sub>D</sub> = 20A	-	41	-	ns
Rise Time	t <sub>r</sub>		-	88	-	
Turn-off Delay Time	t <sub>d(off)</sub>		-	163	-	
Fall Time	t <sub>f</sub>		-	98	-	
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20A, di/dt = 100A/μs	-	106	-	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	245	-	nC
<b>Drain-Source Body Diode Characteristics</b>						
Diode Forward Voltage <sup>4</sup>	V <sub>SD</sub>	I <sub>S</sub> = 20A, V <sub>GS</sub> = 0V	-	-	1.2	V
Continuous Source Current	T <sub>C</sub> =25°C	I <sub>S</sub>	-	-	312	A

**Note:**

- The maximum current rating is package limited.
- Repetitive rating; pulse width limited by max. junction temperature.
- V<sub>DD</sub>=32 V, R<sub>G</sub>=25 Ω, L=0.5mH, starting T<sub>j</sub>=25°C.
- P<sub>D</sub> is based on max. junction temperature, using junction-case thermal resistance.
- The value of R<sub>θJA</sub> is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with Ta=25°C .



## Typical Characteristics

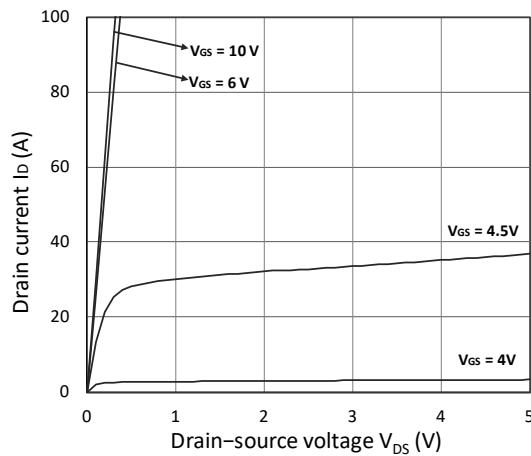


Figure 1. Output Characteristics

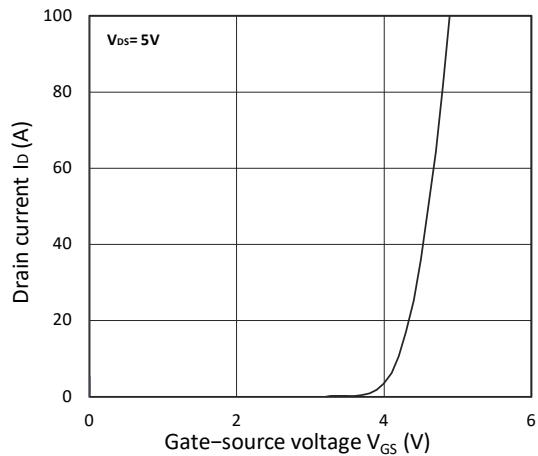


Figure 2. Transfer Characteristics

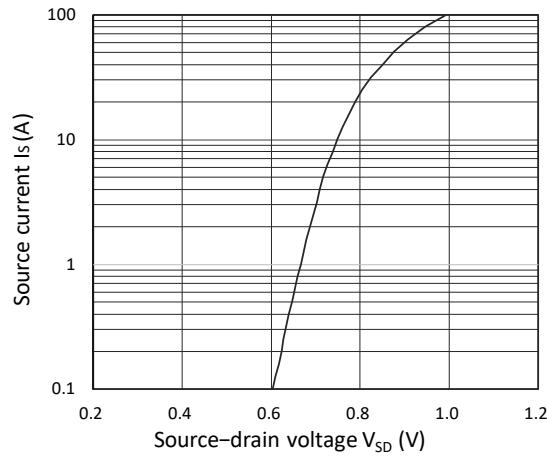


Figure 3. Forward Characteristics of Reverse

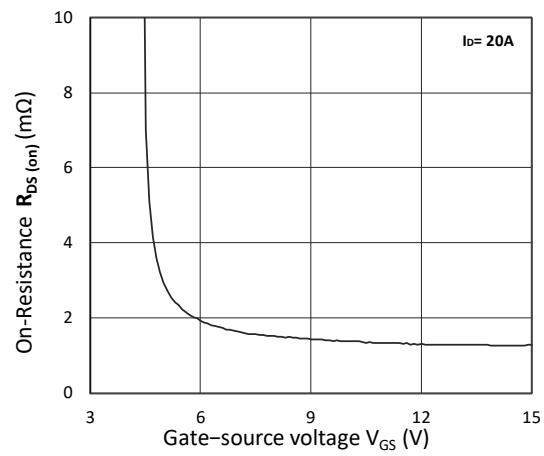


Figure 4.  $R_{DS(on)}$  vs.  $V_{GS}$

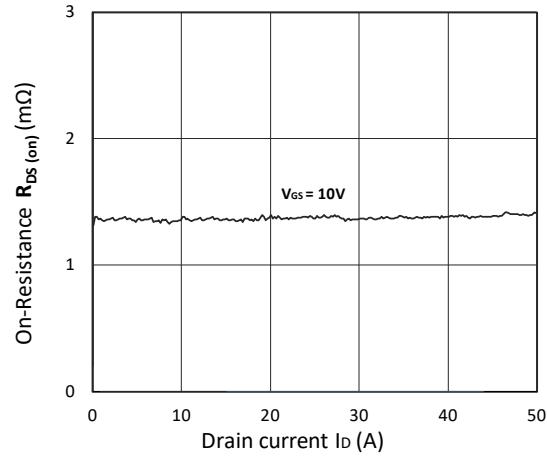


Figure 5.  $R_{DS(on)}$  vs.  $I_D$

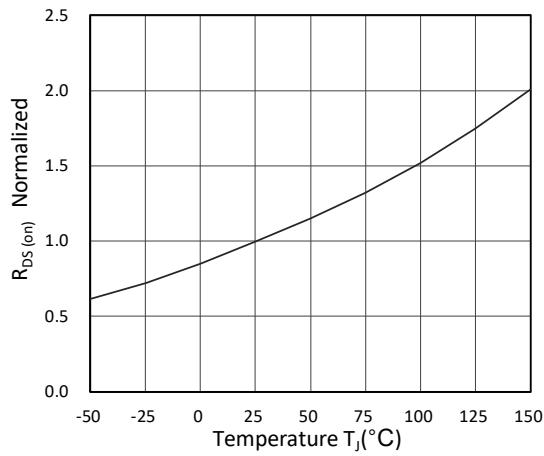


Figure 6. Normalized  $R_{DS(on)}$  vs. Temperature

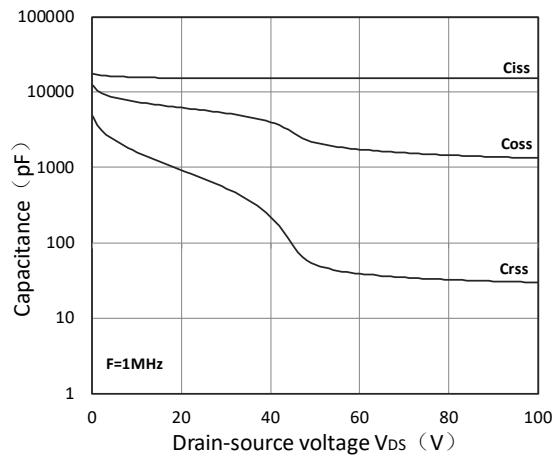


Figure 7. Capacitance Characteristics

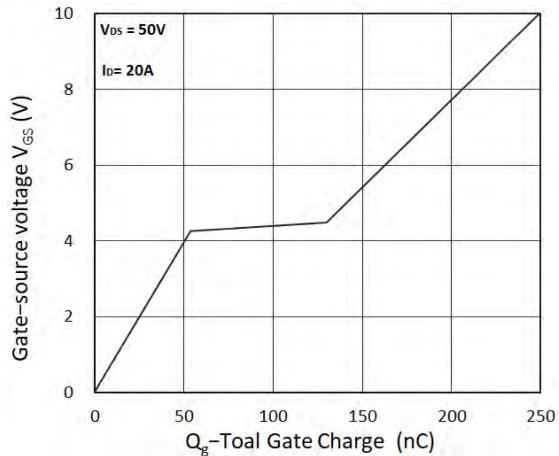


Figure 8. Gate Charge Characteristics

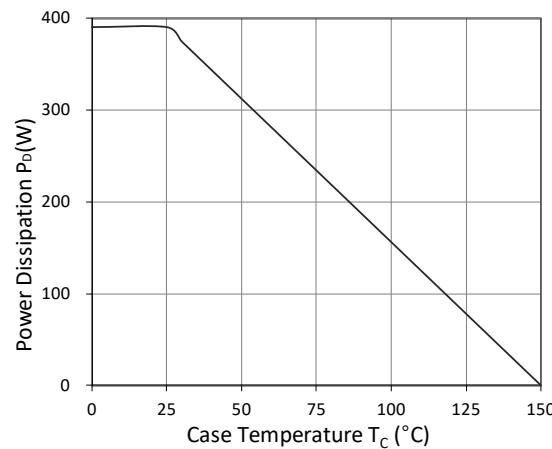


Figure 9. Power Dissipation

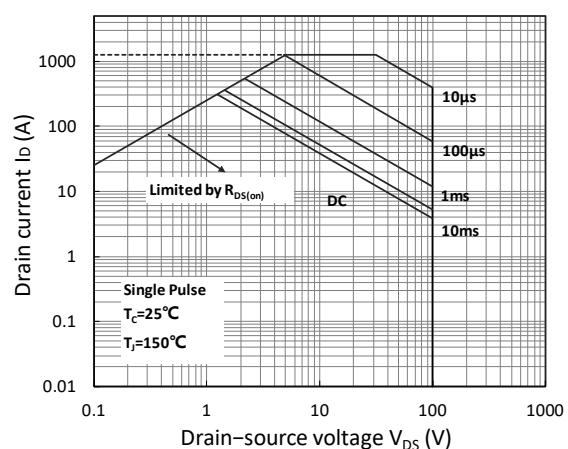


Figure 10. Safe Operating Area

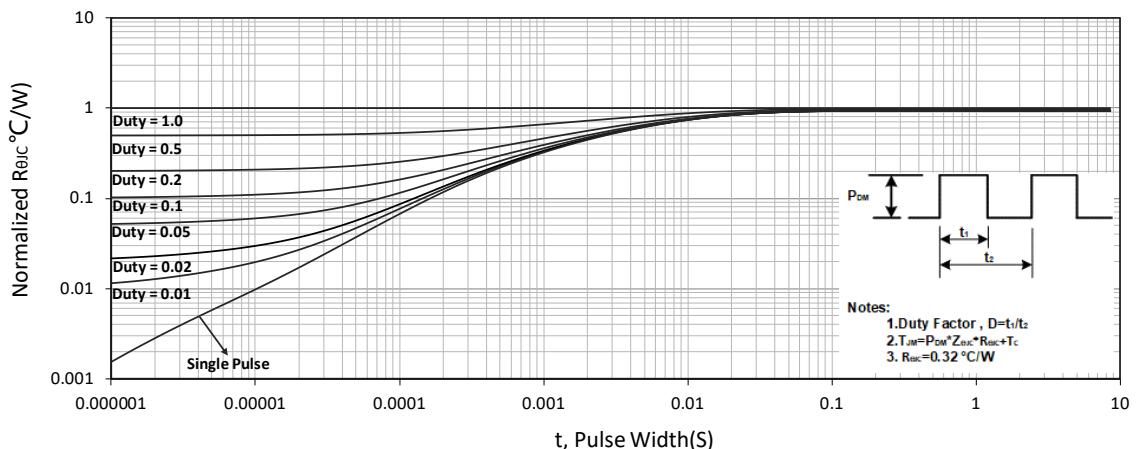


Figure 11. Normalized Maximum Transient Thermal Impedance



### Test Circuit

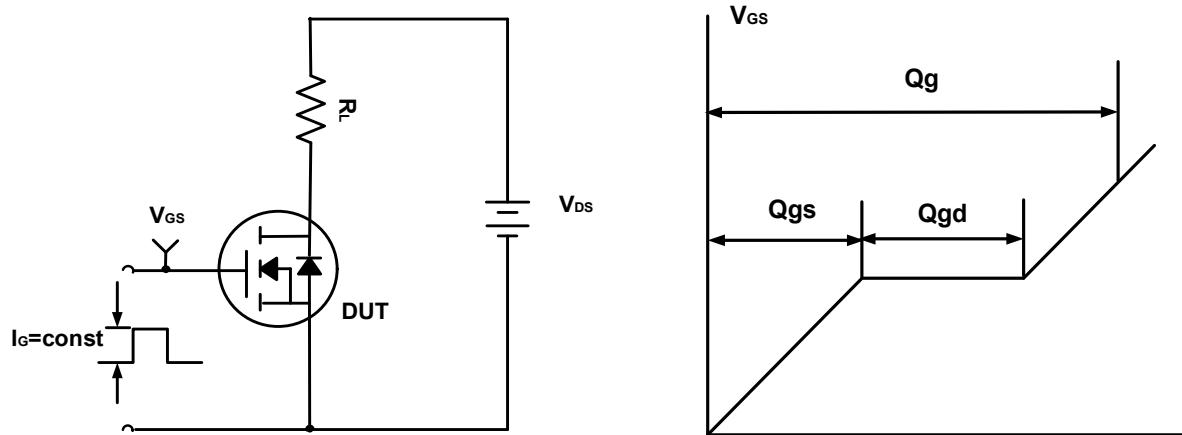


Figure A. Gate Charge Test Circuit & Waveforms

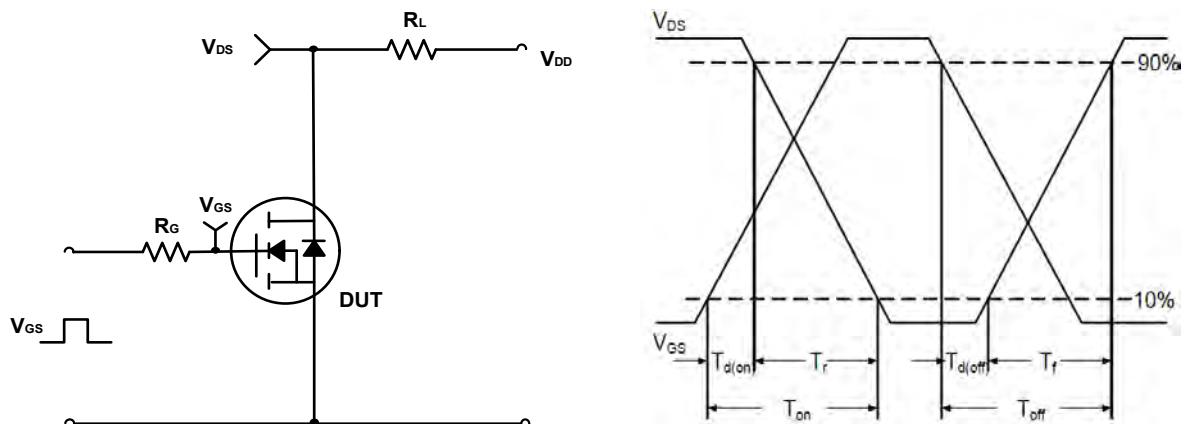


Figure B. Switching Test Circuit & Waveforms

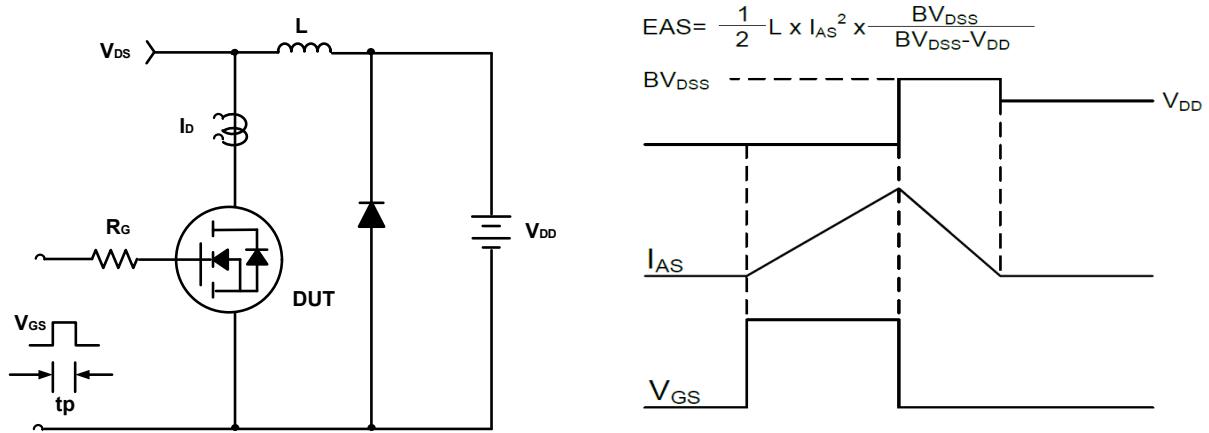
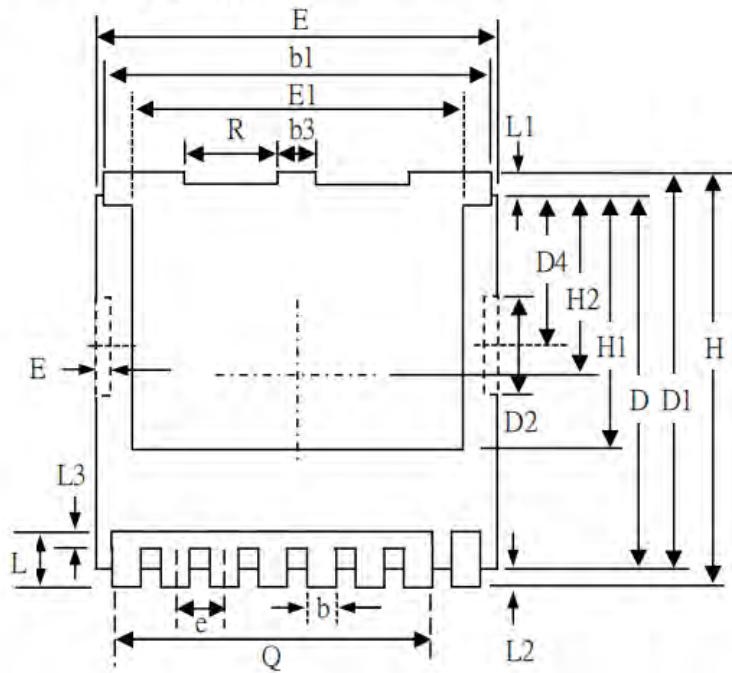


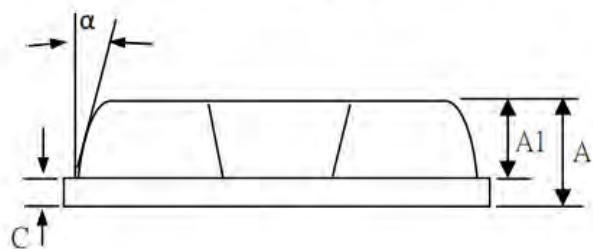
Figure C. Unclamped Inductive Switching Circuit & Waveforms



## TOLL Package Information



BACKSIDE VIEW



1. All Dimension Are In Millimeters.

2. Dimension Does Not Include Mold Protrusions.

SYMBOLS	MIN	NOM	MAX
A	2.20	2.30	2.40
A1	1.70	1.80	1.90
b	0.70	0.80	0.90
b1	9.70	9.80	9.90
b3	1.10	1.20	1.30
c	0.40	0.50	0.60
D	10.28	10.38	10.58
D1	9.80	11.08	11.80
D2	3.10	3.30	3.50
D4	4.37	4.55	4.77
E	9.70	9.90	10.10
E1	7.90	8.10	8.30
E2	0.50	0.70	0.90
e	1.20BCS		
H	11.48	11.68	11.88
H1	6.95BCS		
H2	5.89BCS		
L	1.40	1.90	2.10
L1	0.60	0.70	0.80
L2	0.50	0.60	0.70
L3	0.30	0.70	1.30
Q	8.00 REF.		
R	2.95	3.10	3.25
α	4°		10°



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