

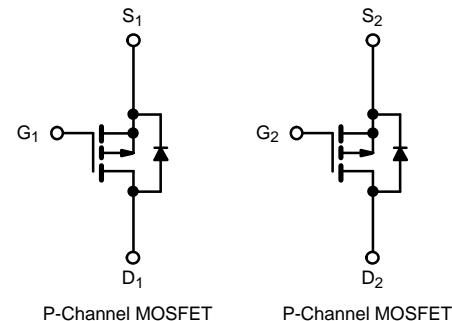
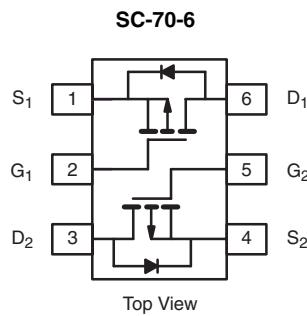
UM6J1NTN-VB Datasheet

Dual P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A) ^a	Q_g (Typ.)
- 20	0.155 at $V_{GS} = - 4.5V$	- 1.8	2.7 nC
	0.235 at $V_{GS} = - 2.5 V$	- 1.5	

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$, unless otherwise noted)				
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V_{DS}	- 20	V	
Gate-Source Voltage	V_{GS}	± 12		
Continuous Drain Current ($T_J = 150^\circ C$)	I_D	- 1.8 - 1.5 - 1.6 ^{b, c} - 1.1 ^{b, c}	A	
Pulsed Drain Current	I_{DM}	- 2.5		
Continuous Source-Drain Diode Current	I_S	- 1.17 - 0.95 ^{b, c}		
Maximum Power Dissipation	P_D	1.4 0.9 1.14 ^{b, c} 0.73 ^{b, c}	W	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	R_{thJA}	93	110	°C/W
Maximum Junction-to-Foot	Steady State	R_{thJF}	75	90

Notes:

- a. $T_C = 25^\circ C$.
- b. Surface mounted on 1" x 1" FR4 board.
- c. $t = 5$ s.
- d. Maximum under steady state conditions is 150 °C/W.

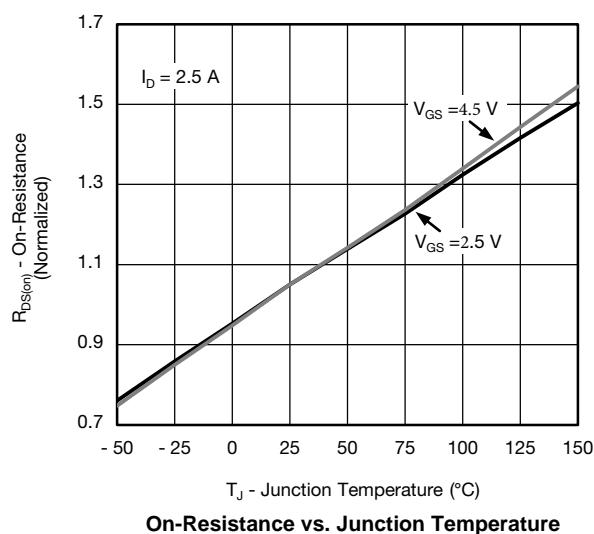
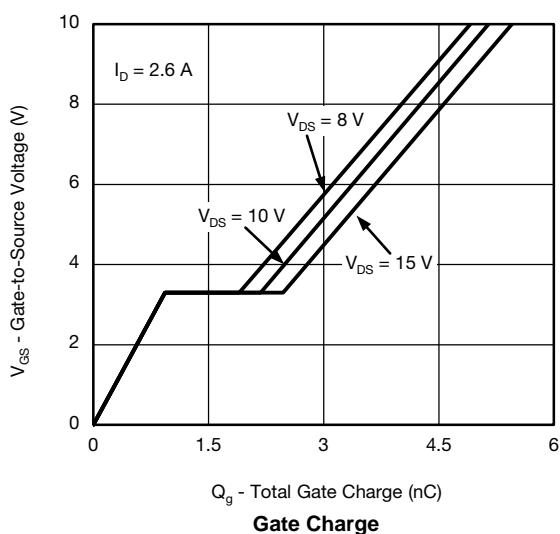
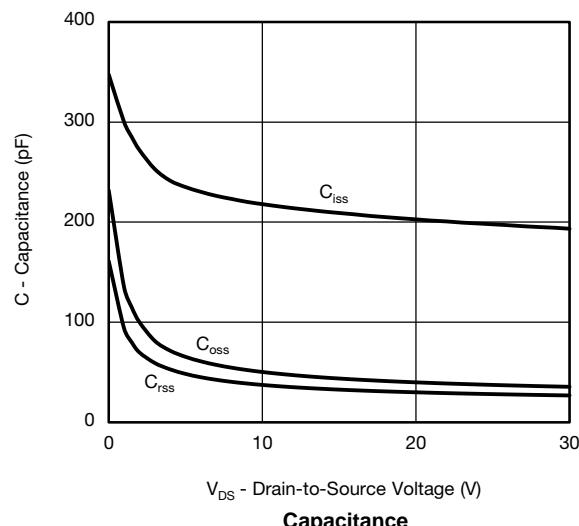
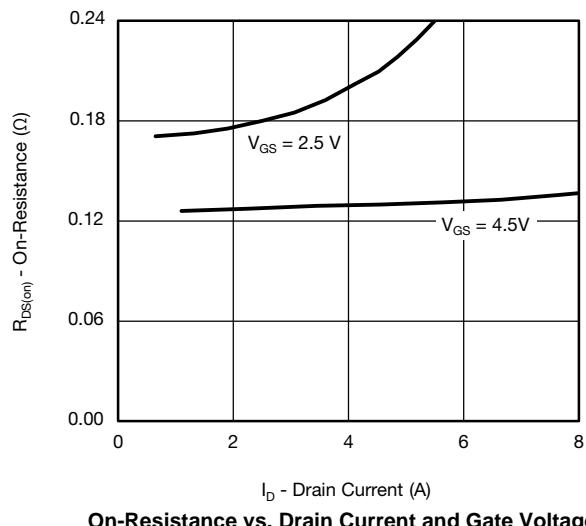
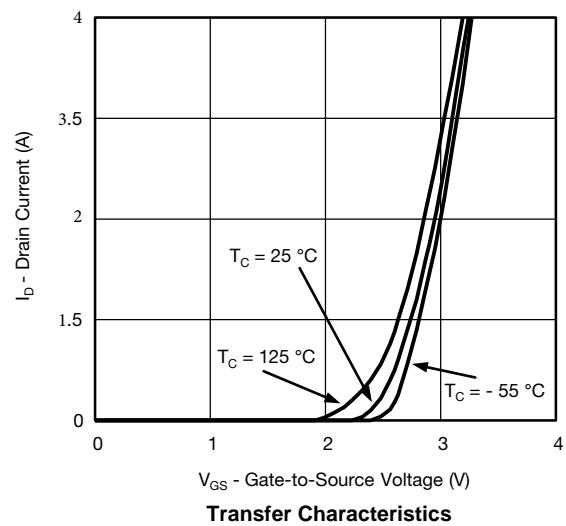
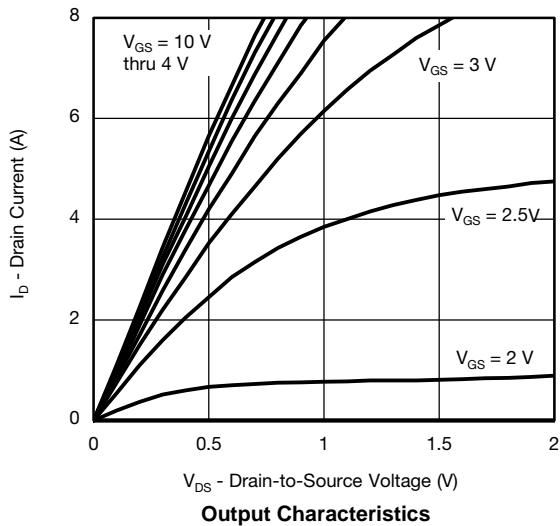
SPECIFICATIONS ($T_J = 25^\circ\text{C}$, unless otherwise noted)

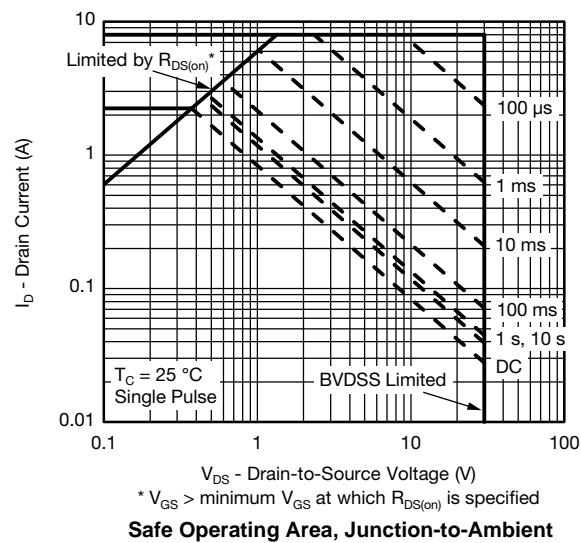
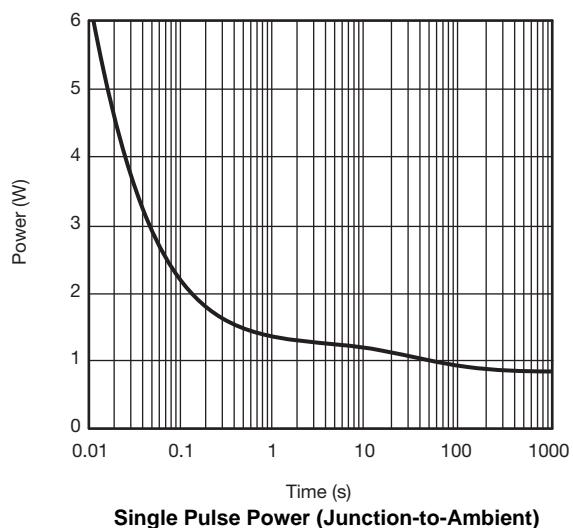
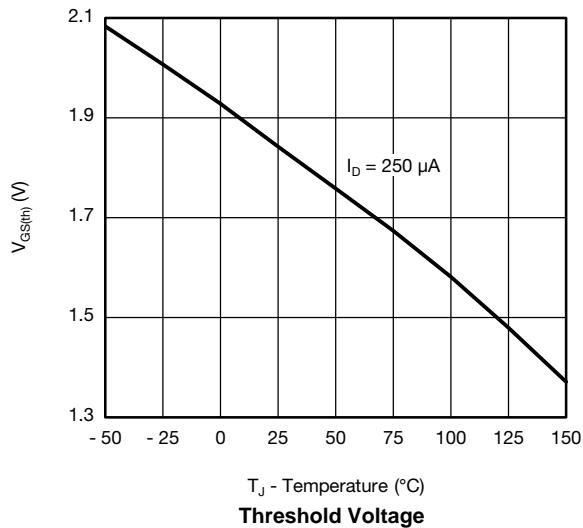
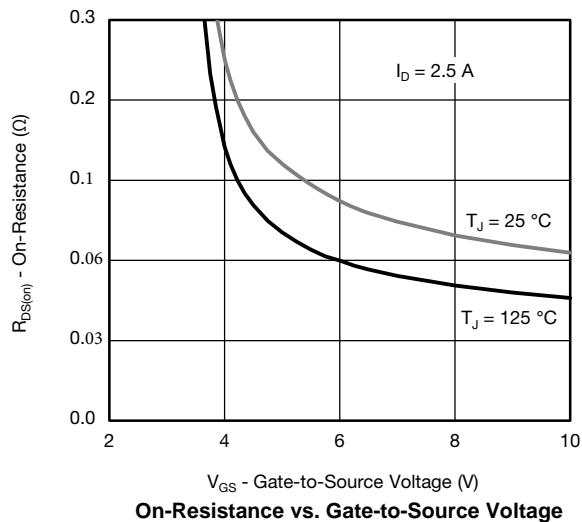
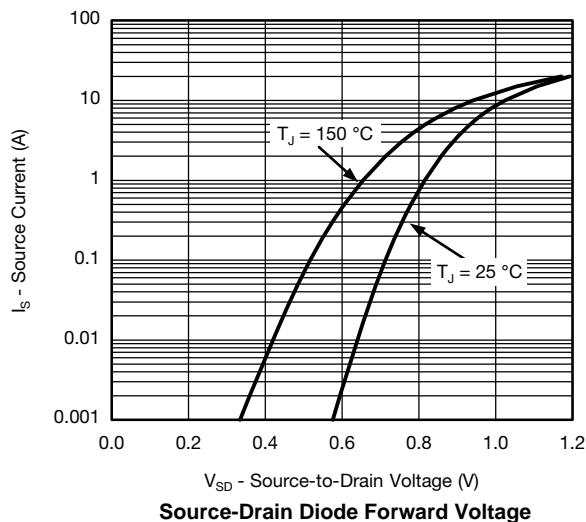
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}$, $I_D = -250 \mu\text{A}$	-20			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = -250 \mu\text{A}$		-17		$\text{mV}/^\circ\text{C}$
$V_{GS(\text{th})}$ Temperature Coefficient	$\Delta V_{GS(\text{th})}/T_J$			3.5		
Gate-Source Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$, $I_D = -250 \mu\text{A}$	-0.5		-1.5	V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}$, $V_{GS} = \pm 12 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -20 \text{ V}$, $V_{GS} = 0 \text{ V}$		1		μA
		$V_{DS} = -20 \text{ V}$, $V_{GS} = 0 \text{ V}$, $T_J = 55^\circ\text{C}$			10	
On-State Drain Current ^a	$I_{D(\text{on})}$	$V_{DS} \leq -5 \text{ V}$, $V_{GS} = -4.5 \text{ V}$	-8			A
Drain-Source On-State Resistance ^a	$R_{DS(\text{on})}$	$V_{GS} = -4.5 \text{ V}$, $I_D = -2.5 \text{ A}$		0.155		Ω
		$V_{GS} = -2.5 \text{ V}$, $I_D = -1 \text{ A}$		0.235		
Forward Transconductance ^a	g_{fs}	$V_{DS} = -15 \text{ V}$, $I_D = -2.6 \text{ A}$		5		S
Dynamic^b						
Input Capacitance	C_{iss}	$V_{DS} = -15 \text{ V}$, $V_{GS} = 0 \text{ V}$, $f = 1 \text{ MHz}$		210		pF
Output Capacitance	C_{oss}			45		
Reverse Transfer Capacitance	C_{rss}			33		
Total Gate Charge	Q_g	$V_{DS} = -15 \text{ V}$, $V_{GS} = -4.5 \text{ V}$, $I_D = -2.6 \text{ A}$		5.2	8	nC
				2.7	4	
Gate-Source Charge	Q_{gs}	$V_{DS} = -15 \text{ V}$, $V_{GS} = -4.5 \text{ V}$, $I_D = -2.6 \text{ A}$		0.94		
Gate-Drain Charge	Q_{gd}			1.3		
Gate Resistance	R_g	$f = 1 \text{ MHz}$	2	7	14	Ω
Turn-On Delay Time	$t_{d(\text{on})}$	$V_{DD} = -15 \text{ V}$, $R_L = 7.1 \Omega$ $I_D \equiv -2.1 \text{ A}$, $V_{GEN} = -4.5 \text{ V}$, $R_g = 1 \Omega$		39	59	ns
Rise Time	t_r			25	38	
Turn-Off Delay Time	$t_{d(\text{off})}$			13	20	
Fall Time	t_f			9	18	
Turn-On Delay Time	$t_{d(\text{on})}$			5	10	
Rise Time	t_r			10	20	
Turn-Off Delay Time	$t_{d(\text{off})}$			14	21	
Fall Time	t_f			7	14	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	$T_C = 25^\circ\text{C}$		1.17		A
Pulse Diode Forward Current	I_{SM}			8		
Body Diode Voltage	V_{SD}	$I_S = -2.1 \text{ A}$, $V_{GS} = 0 \text{ V}$		0.85	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = -2.1 \text{ A}$, $dl/dt = 100 \text{ A}/\mu\text{s}$, $T_J = 25^\circ\text{C}$		13	20	ns
Body Diode Reverse Recovery Charge	Q_{rr}			6	12	
Reverse Recovery Fall Time	t_a			9		ns
Reverse Recovery Rise Time	t_b			4		

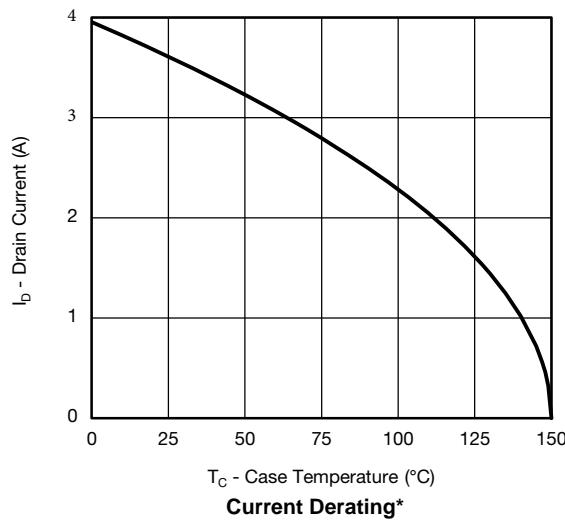
Notes:

a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$
 b. Guaranteed by design, not subject to production testing.

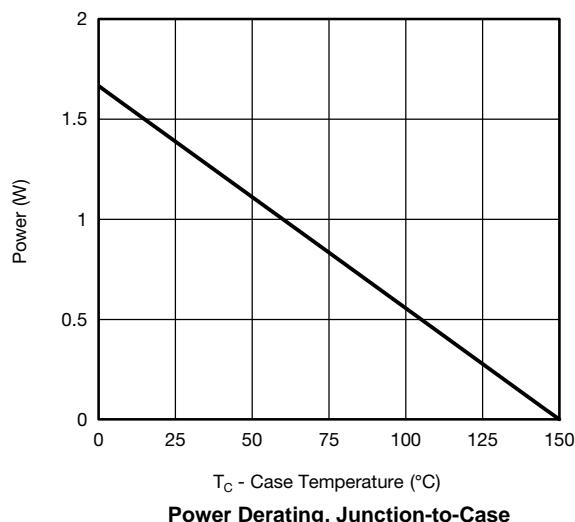
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)


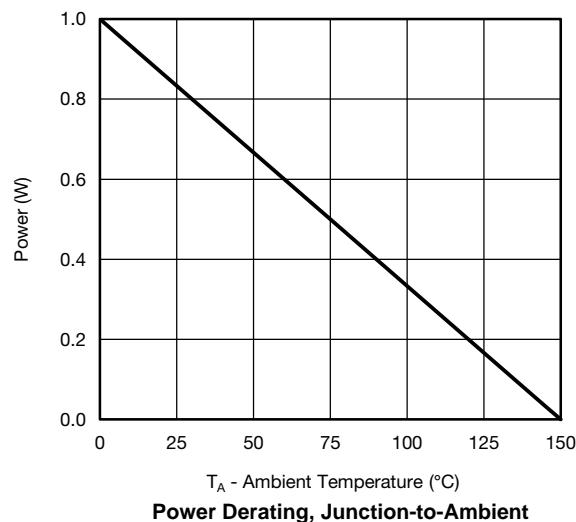
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)
 T_C - Case Temperature (°C)

Current Derating*

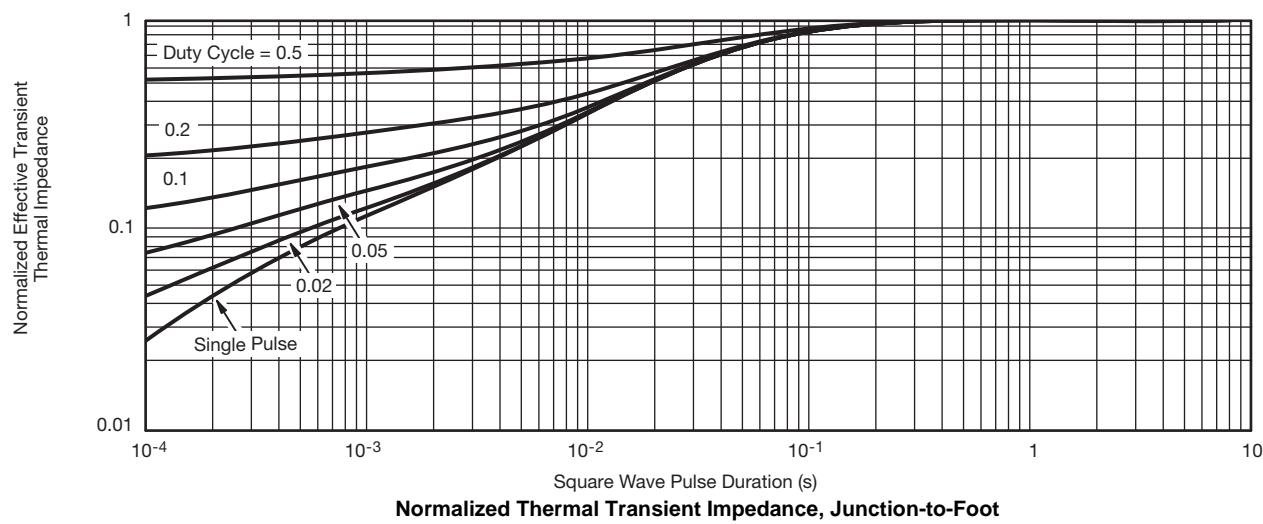
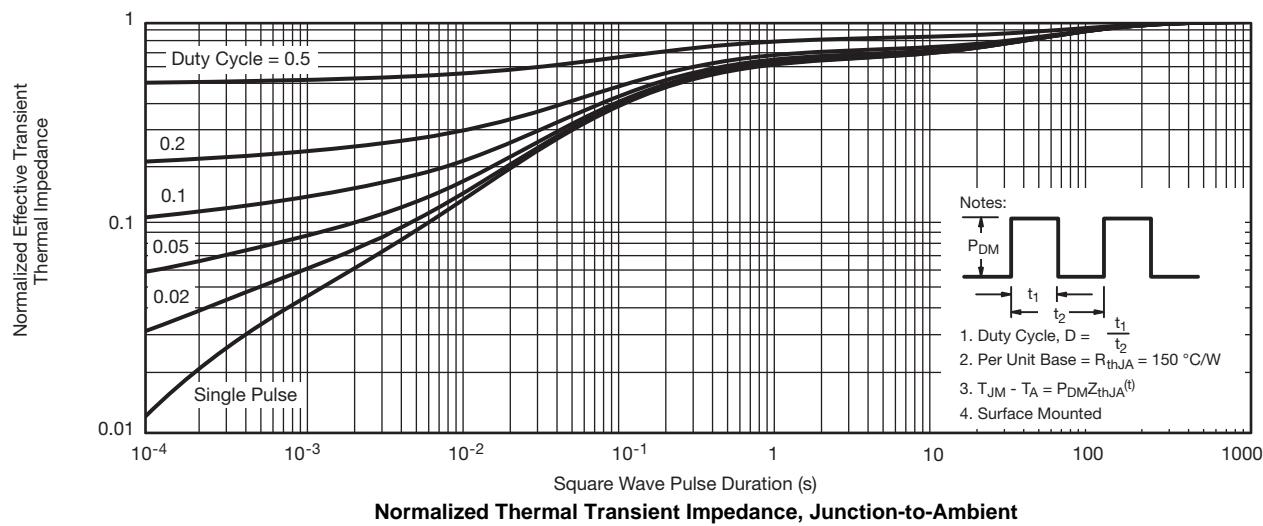
 T_C - Case Temperature (°C)

Power Derating, Junction-to-Case

 T_A - Ambient Temperature (°C)

Power Derating, Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(\text{max})} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)


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