



Low Pass Filter

Features

- high rejection
- sharp cut-off
- shielded package
- aqueous washable
- low cost

Applications

- defense communications
- receivers / transmitters
- harmonic rejection

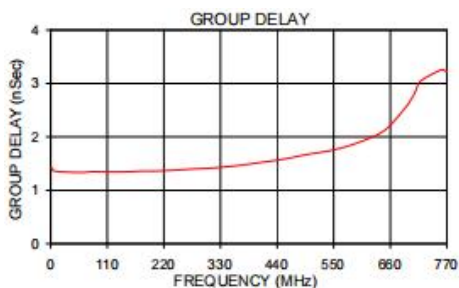
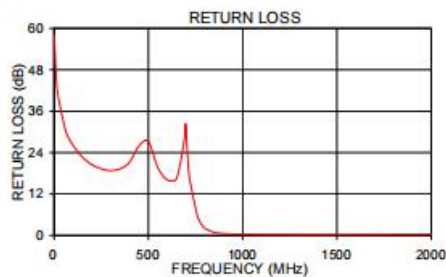
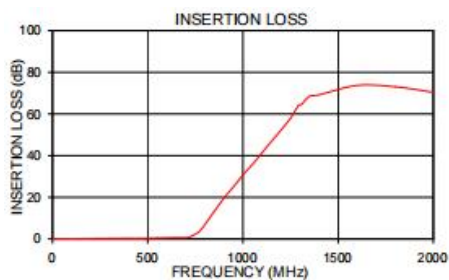
JY-SXLP-700+



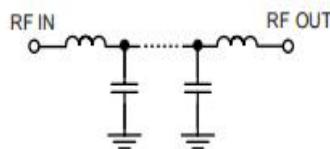
50Ω DC to 700 MHz

Low Pass Filter Electrical Specifications ($T_{AMB}=25^{\circ}\text{C}$)

PASSBAND (MHz)	FCO ₂ (MHz) Nom.	STOPBAND (MHz)		VSWR(:1)	
		(Loss > 20dB)	(Loss > 40dB)	Passband Typ.	Stopband Typ.
(Loss < 1dB)	(Loss 3dB)				
DC-700	770	1000-1300	1300-2000	1.7	18



Functional Schematic



Maximum Ratings

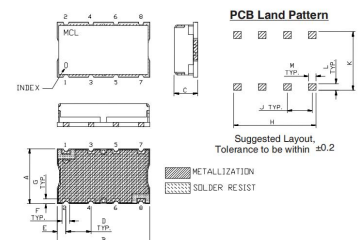
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C
RF Power Input*	0.5W max.

Permanent damage may occur if any of these limits are exceeded.

Pin Connections

INPUT	1
OUTPUT	8
GROUND	2,3,4,5,6,7

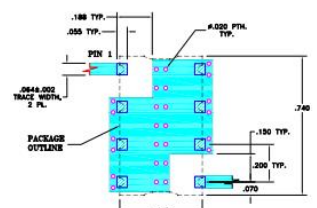
Outline Drawing



Outline Dimensions: Unit (mm)

A	11.18	G	1.02
B	18.80	H	16.76
C	6.86	J	5.08
D	5.08	K	11.94
E	1.78	L	1.40
F	1.52	M	1.52
WT	0.3g		

Suggested PCB Layout



NOTE:
1. TRACE WIDTH IS SHOWN FOR FR4 WITH DIELECTRIC THICKNESS: .005"±.005". COPPER: 1/2 OZ EACH SIDE.
FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.
3. DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).
4. DENOTES COPPER LAND PATTERN PRIOR OF SOLDER MASK