



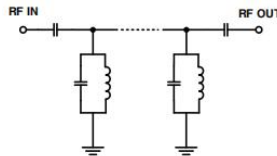
Features

- Low Insertion loss
- Minimal Insertion loss variation over operating temperature
- Low-profile shielded package

Applications

- Cordless telephony system
- Public cellular networks, GSM
- Wireless audio applications
- PCS broadband

Functional Schematic



JY-CBP-1880E+



Generic photo used for illustration purposes only
50Ω 1780 to 1980 MHz

Electrical Specifications at 25°C

Parameter		Frequency(MHz)	Min.	Typ.	Max.	Unit
Pass Band	Center Frequency	-	-	1880	-	MHz
	Insertion Loss	1780-1980	-	1.5	3	dB
	VSWR	1780-1980	-	1.5	2.3	:1
Stop Band, Lower	Insertion Loss	DC-1550	20	37	-	dB
	VSWR	DC-1550	-	25	-	:1
Stop Band, Upper	Insertion Loss	2150-3300	20	30	-	dB
	VSWR	2150-3300	-	14	-	:1

Maximum Ratings

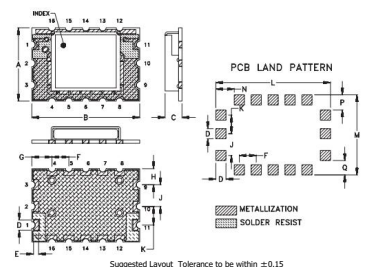
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C
RF Power Input*	6.3 W max @ 25°C

Permanent damage may occur if any of these limits are exceeded.

Pin Connections

INPUT	1
OUTPUT	11
GROUND	2,3,4,5,6,7,8,9,10,12,13,14,15,16

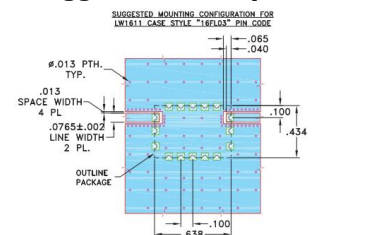
Outline Drawing



Outline Dimensions: Unit (mm)

A	11.02	B	16.21
C	3.05	D	1.52
E	0.76	F	2.54
G	3.02	H	2.41
J	3.28	K	2.79
L	17.22	M	12.04
N	2.77	P	2.29
Q	2.16	wt	0.8g

Suggested PCB Layout



NOTES:
1. TRACE WIDTH IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .0075.004". COPPER: 1/2 OZ. EACH SIDE.
FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.
DENOTES PCB COPPER LAYOUT WITH SMOBC (GOLDEN MASK OVER BASE COPPER)
DENOTES COPPER LAND PATTERN FREE OF SOLDERMASK

