

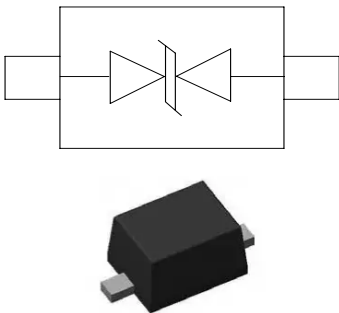
Features

- ◆ Ultra low leakage: nA level
- ◆ Low operating voltage: 5.0V
- ◆ Low clamping voltage
- ◆ 2-pin leadless package
- ◆ Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 30\text{kV}$
 - Contact discharge: $\pm 30\text{kV}$
 - IEC61000-4-5 (Lightning) 30A (8/20 μs)
- ◆ RoHS Compliant
- ◆ Package: SOD-523

Description

The ESDK5B0L0S5 is a bi-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power line. The ESDK5B0L0S5 complies with the IEC 61000-4-2 (ESD) standard with $\pm 30\text{ kV}$ air and $\pm 30\text{ kV}$ contact discharge. It is assembled into an ultra-small SOD-523 lead-free package. The small size and high ESD surge protection make ESDK5B0L0S5 an ideal choice to protect cell phone, digital cameras, audio players and many other portable applications.

Circuit Diagram



Applications

- ◆ Cellular Handsets and Accessories
- ◆ Personal Digital Assistants
- ◆ Notebooks and Handhelds
- ◆ Portable Instrumentation
- ◆ Digital Cameras
- ◆ Peripherals
- ◆ Audio Players
- ◆ Keypads, Side Keys, LCD Displays

Absolute Maximum Ratings : ($T_C=25^{\circ}\text{C}$ unless otherwise noted)

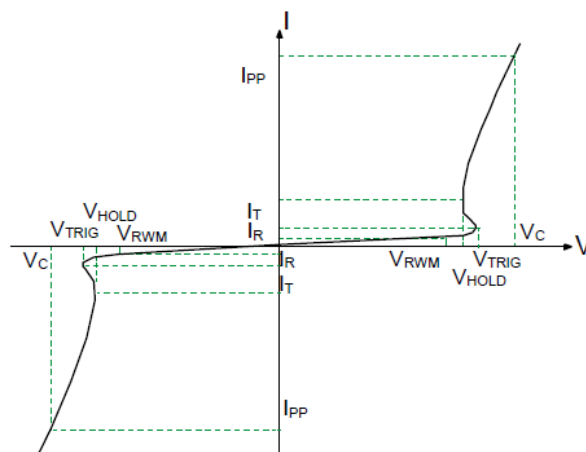
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	Ppk	360	W
Peak Pulse Current (8/20 μs)	IPP	30	A
ESD per IEC 61000-4-2 (Air)	VESD	± 30	kV
ESD per IEC 61000-4-2 (Contact)		± 30	
Operating Temperature Range	TJ	-55 to +125	$^{\circ}\text{C}$
Storage Temperature Range	Tstg	-55 to +150	$^{\circ}\text{C}$

Electrical Characteristics : ($T_C=25^{\circ}\text{C}$ unless otherwise noted)

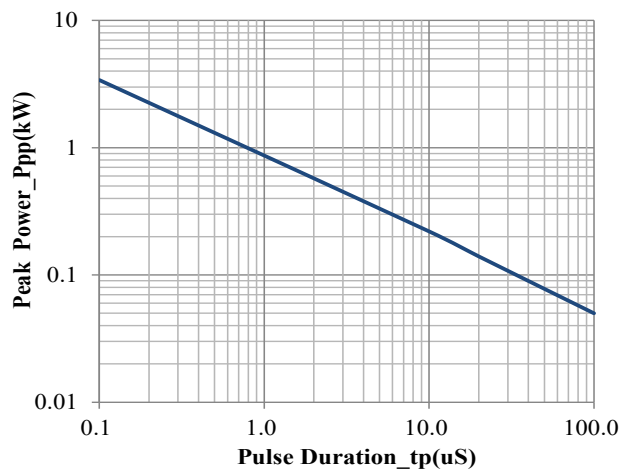
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}				5.0	V
Breakdown Voltage	V_{BR}	$I_T = 1\text{mA}$	5.5	7.2	8.5	V
Reverse Leakage Current	I_{R}	$V_{\text{RWM}} = 5.0\text{V}$			0.1	μA
Clamping Voltage	V_{C}	$I_{\text{PP}} = 1\text{A}$ (8 / 20 μs pulse)			9.0	V
Clamping Voltage	V_{C}	$I_{\text{PP}} = 30\text{A}$ (8 / 20 μs pulse)			12.0	V
Junction Capacitance	C_{J}	$V_{\text{R}} = 0\text{V}$, $f = 1\text{MHz}$		60	100	pF

Portion Electronics Parameter

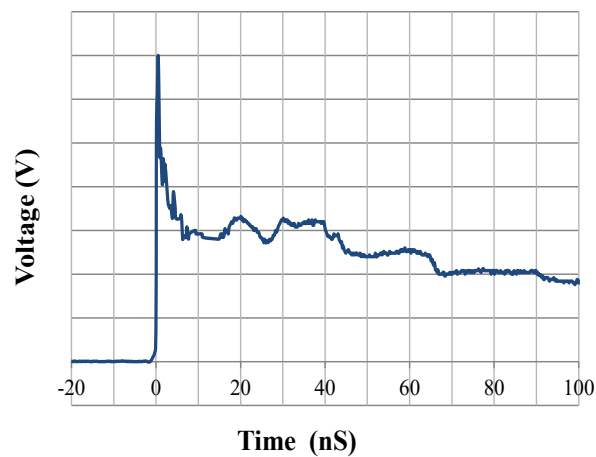
Symbol	Parameter
V_{RWM}	Peak Reverse Working Voltage
I_{R}	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_{T}
I_{T}	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_{C}	Clamping Voltage @ I_{PP}



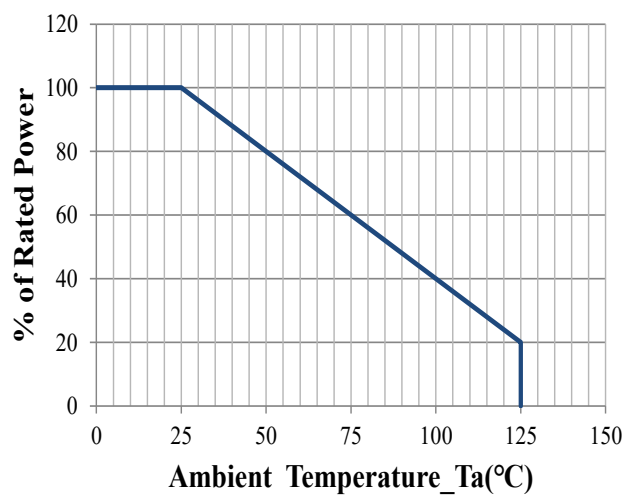
Typical Characteristics: ($T_c=25^{\circ}\text{C}$ unless otherwise noted)



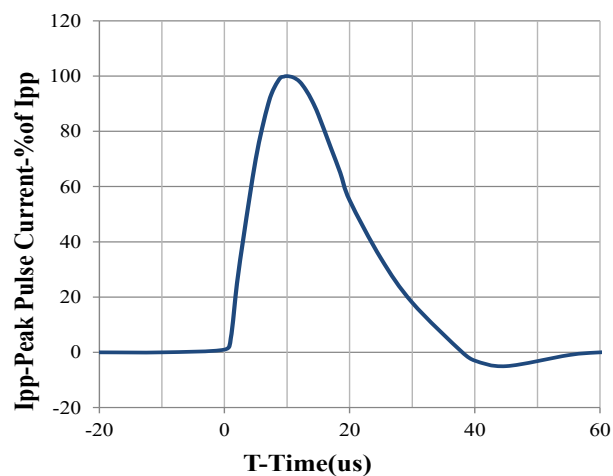
Peak Pulse Power vs. Pulse Time



IEC61000-4-2 Pulse Waveform

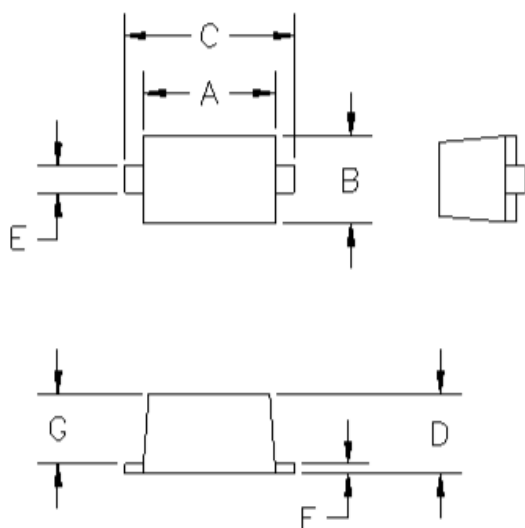


Power Derating Curve



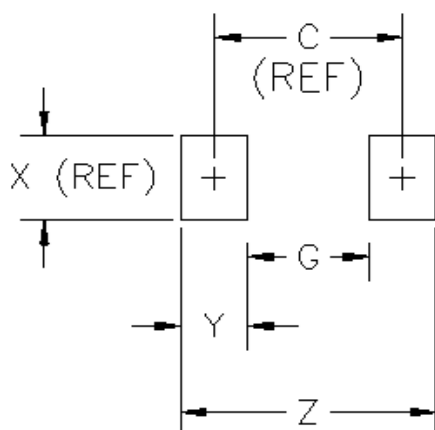
8 X 20us Pulse Waveform

SOD-523 Package Outline Drawing



DIMENSIONS					
DIM ^N	INCHES		MM		NOTE
	. MIN	MAX	MIN	MAX	
A	.043	.051	1.10	1.30	—
B	.028	.035	0.70	0.90	—
C	.059	.067	1.50	1.70	—
D	.020	.028	0.50	0.70	—
E	.010	.014	0.25	0.35	—
F	.004	.008	0.10	0.20	—
G	.020	.028	0.50	0.70	—

Suggested Land Pattern



DIMENSIONS					
DIM ^N	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
C	—	.067	—	1.70	REF
G	—	.043	—	1.10	—
X	—	.031	—	0.80	REF
Y	—	.024	—	0.60	—
Z	—	.091	—	2.30	—

