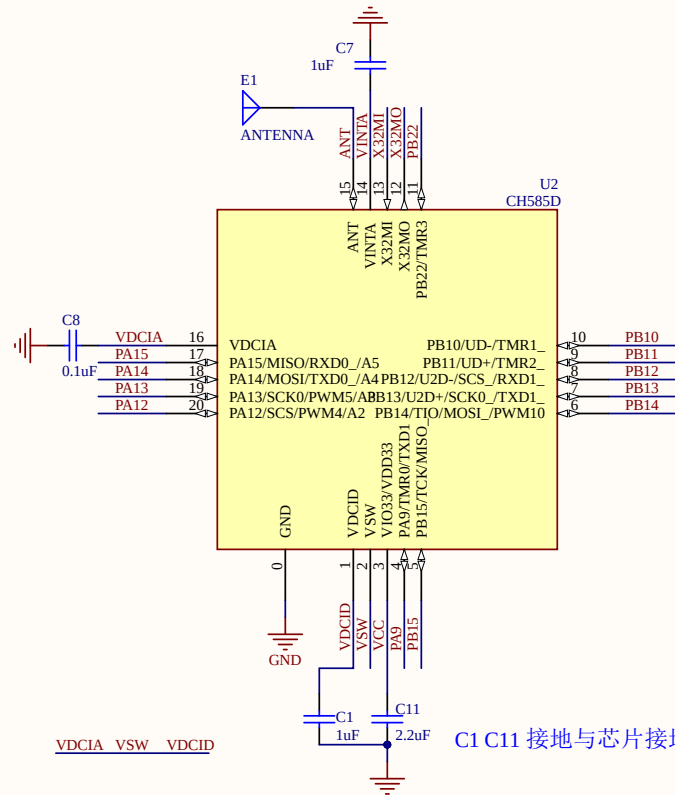
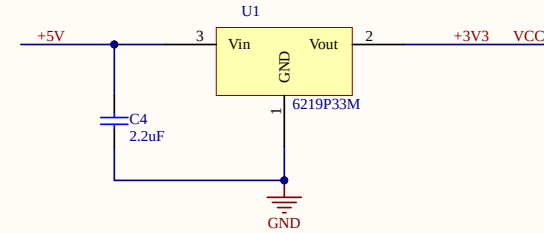
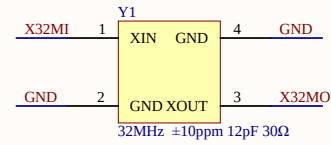




晶振选型需与芯片内置电容值匹配



C1 C11 接地与芯片接地回路优先级最高

