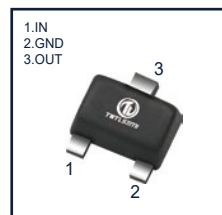
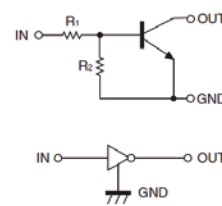


Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors(see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input.They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy



SOT-723



Equivalent Circuit

Absolute Maximum Ratings(T_A=25℃)

Symbol	Parameter	Limits	Uniat
V _{CC}	Supply Voltage	50	V
V _{IN}	Input Voltage	-10 ~ +40	V
I _O	Output Current	50	mA
I _{CM}	Peak Collector Current	100	mA
P _D	Power Dissipation	100	mW
T _J , T _{stg}	Operation Junction and Storage Temperature Range	-55 ~ +150	℃

Electrical Characteristics (T_A=25℃ unless otherwise specified)

Symbol	Parameter	Test conditions	Min	Typ	Max	Unit
V _{I(off)}	Input voltage	V _{CC} =5V, I _O =100μA	0.5			V
V _{I(on)}		V _O =0.3V, I _O =10mA			3	V
V _{O(on)}	Output voltage	I _O /I _{II} =10mA/0.5mA			0.3	V
I _I	Input current	V _I =5V			0.88	mA
I _{O(off)}	Output current	V _{CC} =50V, V _I =0			0.5	μA
G _I	DC current gain	V _O =5V, I _O =5mA	33			
R _I	Input resistance		7	10	13	kΩ
R ₂ /R ₁	Resistance ratio		0.8	1	1.2	
f _T	Transition frequency	V _O =10V, I _O =5mA, f=100MHz		250		MHz

Ordering information

Product ID	Marking	Naming rule	Pack	Qty(PCS)
DTC114EM	24	DTC114EM 产品名称 product name	SOT-723	8000

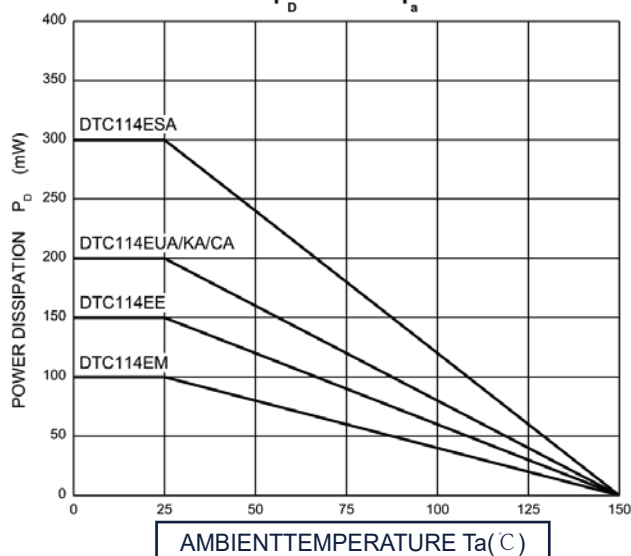
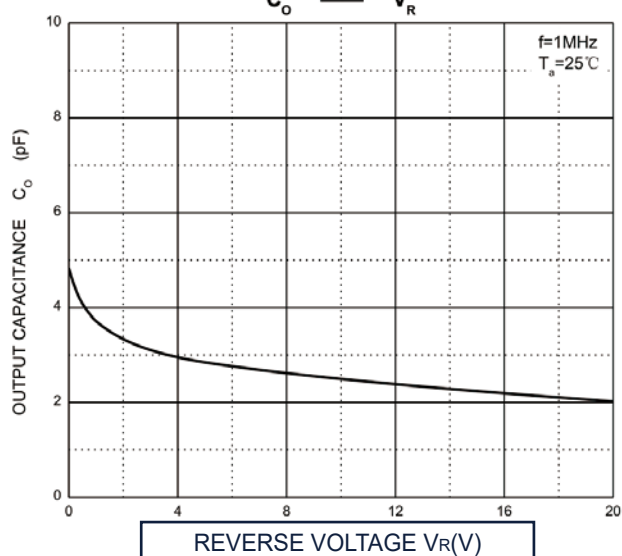
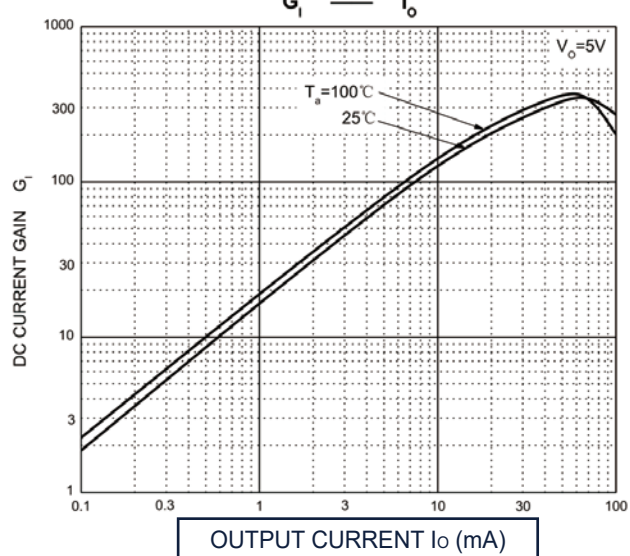
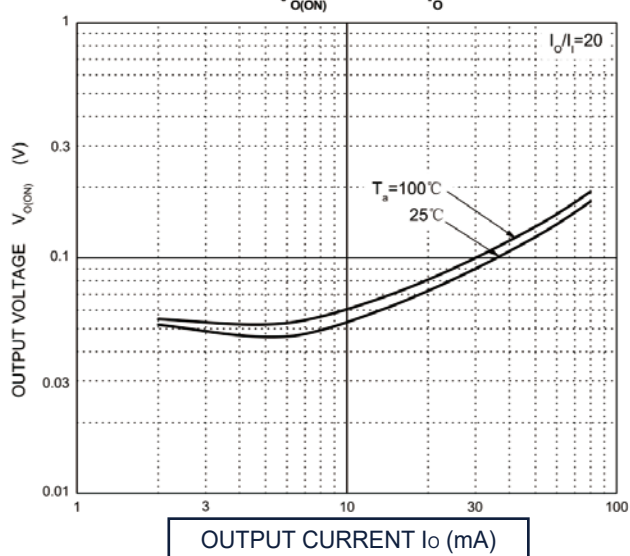
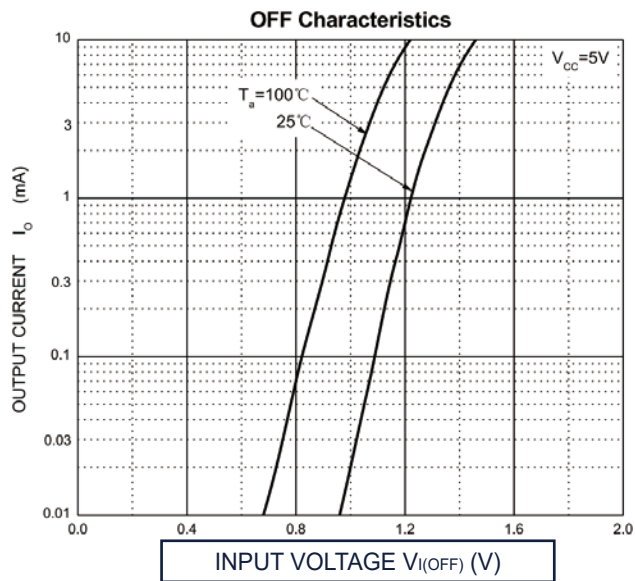
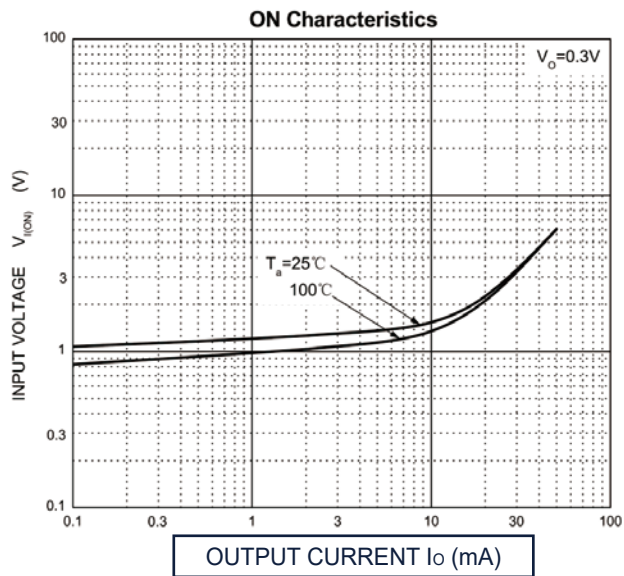


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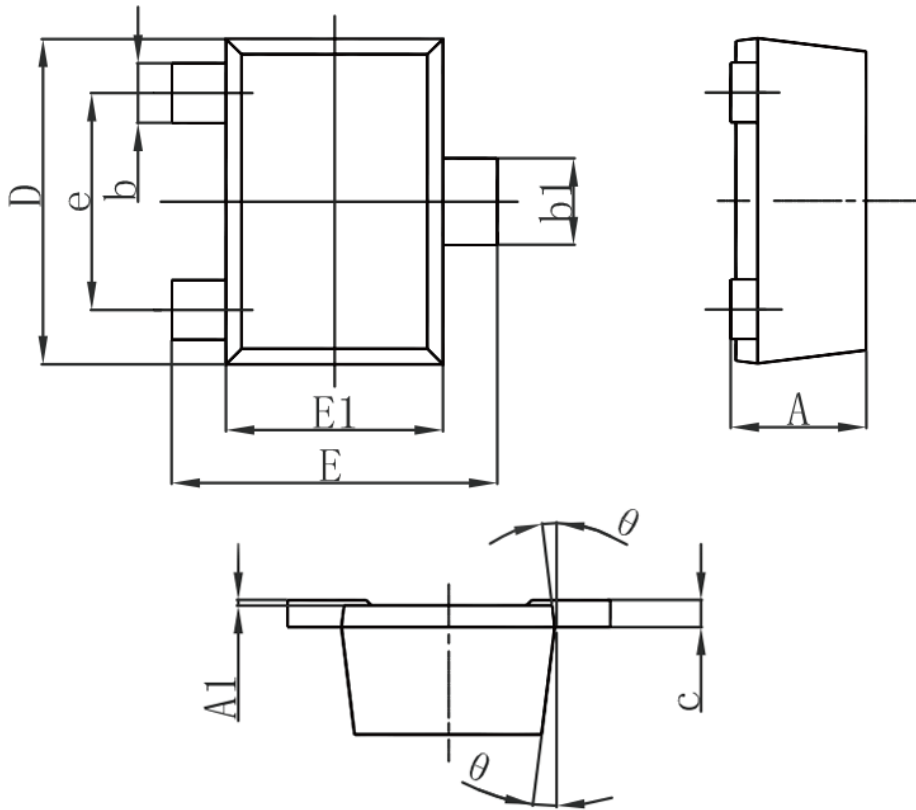
SOT-723 DIGITAL TRANSISTOR (NPN)

Typical Characteristics





SOT-723 Package Outline Dimensions



Symbol	Dimensions in Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A		0.500		0.020
A1	0.000	0.050	0.000	0.002
b	0.170	0.270	0.007	0.011
b1	0.270	0.370	0.011	0.015
c		0.150		0.006
D	1.150	1.250	0.045	0.045
E	1.150	1.250	0.045	0.049
E1	0.750	0.850	0.030	0.033
e	0.800TYP.		0.031TYP.	
θ	7° REF.		7° REF.	