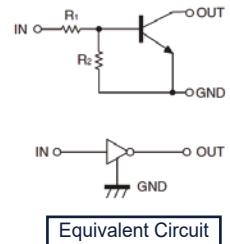


Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy



Absolute Maximum Ratings ($T_A=25^\circ\text{C}$)

Symbol	Parameter	Limits	Unit
V_{CC}	Supply Voltage	50	V
V_{IN}	Input Voltage	-10 ~ +40	V
I_O	Output Current	30	mA
I_{CM}	Power Dissipation	100	mA
P_D	Power Dissipation	100	mW
T_J, T_{STG}	Operation Junction and Storage Temperature Range	-55 ~ +150	°C

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test conditions	Min	Typ	Max	Unit
$V_{I(off)}$	Input voltage	$V_{CC}=5V, I_O=100\mu\text{A}$	0.5			V
$V_{I(on)}$		$V_O=0.3V, I_O=2\text{mA}$			3	V
$V_{O(on)}$	Output voltage	$I_O/I_I=10\text{mA}/0.5\text{mA}$			0.3	V
I_I	Input current	$V_I=5V$			0.18	mA
$I_O(off)$	Output current	$V_{CC}=50V, V_I=0$			0.5	μA
G_I	DC current gain	$V_O=5V, I_O=5\text{mA}$	68			
R_1	Input resistance		32.9	47	61.1	kΩ
R_2/R_1	Resistance ratio		0.8	1	1.2	
f_T	Transition frequency	$V_O=10V, I_O=5\text{mA}, f=100\text{MHz}$		250		MHz

Ordering information

Product ID	Marking	Naming rule	Pack	Qty(PCS)
DTC144EM	26	<div style="border: 1px solid black; padding: 2px; text-align: center;"> DTC144EM ↓ 产品名称 product name </div>	SOT-723	3000

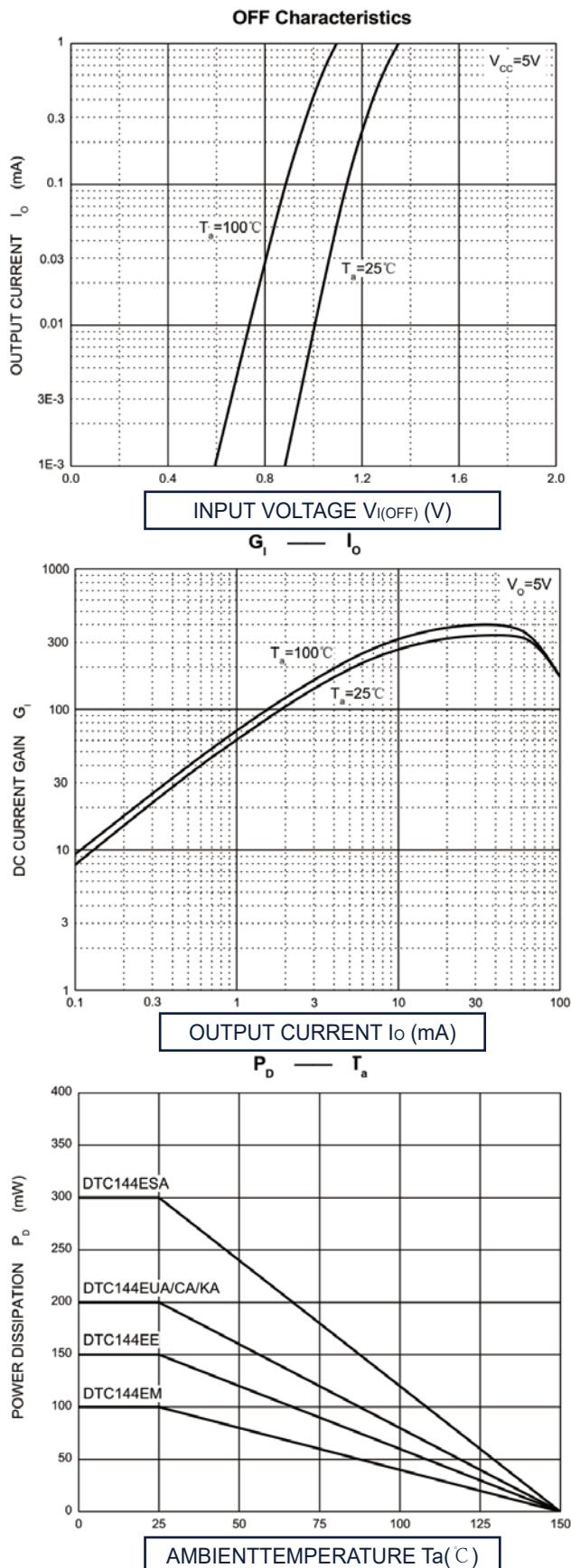
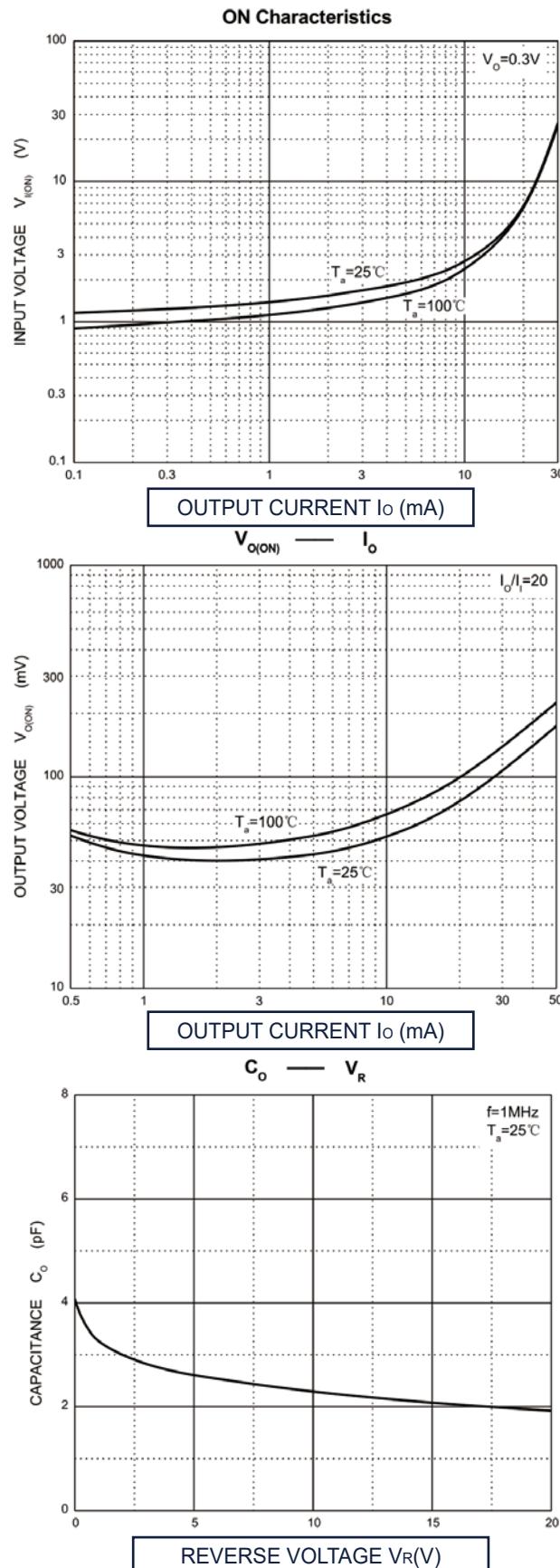


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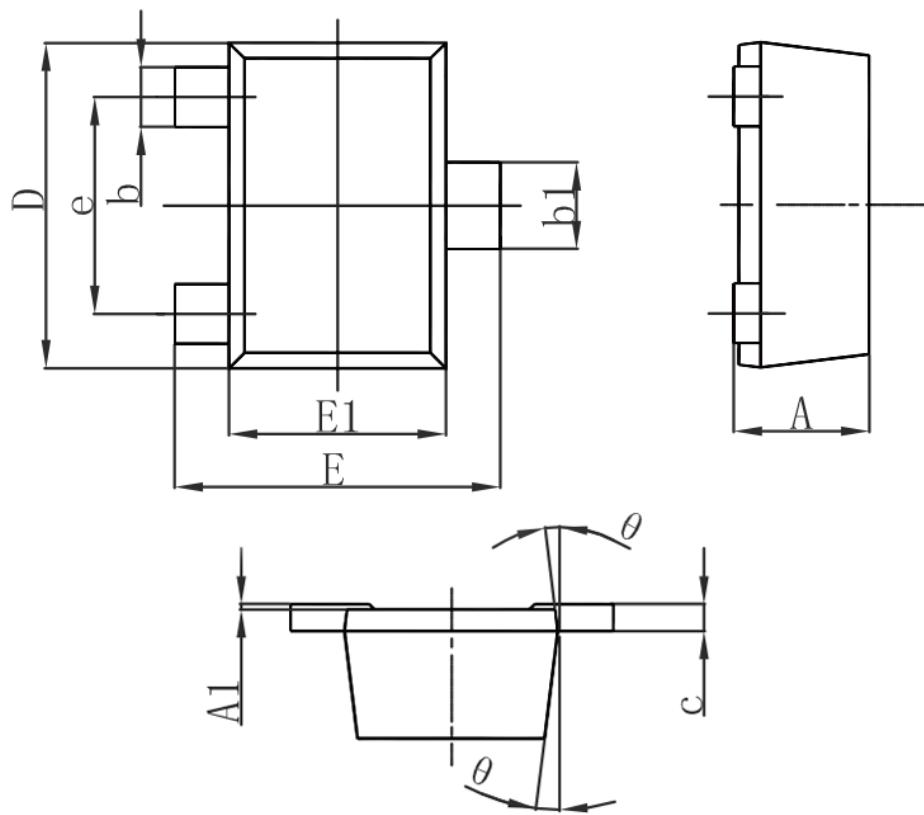
TL-DTC144EM

SOT-723 DIGITAL TRANSISTOR (NPN)

Typical Characteristics



SOT-723 Package Outline Dimensions



Symbol	Dimensions in Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A		0.500		0.020
A1	0.000	0.050	0.000	0.002
b	0.170	0.270	0.007	0.011
b1	0.270	0.370	0.011	0.015
c		0.150		0.006
D	1.150	1.250	0.045	0.045
E	1.150	1.250	0.045	0.049
E1	0.750	0.850	0.030	0.033
e	0.800TYP.		0.031TYP.	
θ	7° REF.		7° REF.	