

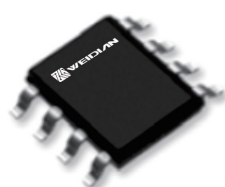
N and P-channel Enhancement Mode Power MOSFET

Features

- Advanced
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Pb-free Lead Plating
- Halogen-free and RoHS-compliant
- 100% UIS TESTED!, 100% Rg TESTED!

Applications

- Load Switch
- PWM Application
- Power Management



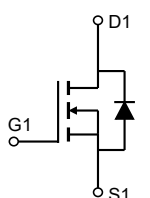
SOP8 Top View

N-Channel

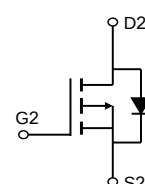
Parameters	Value	Unit
V_{DSS}	30	V
$V_{GS(th)_{Typ}}$	1.5	V
$I_D(@V_{GS}=10V)$	5.6	A
$R_{DS(ON)_{Typ}}(@V_{GS}=10V)$	18	$m\Omega$
$R_{DS(ON)_{Typ}}(@V_{GS}=4.5V)$	25	$m\Omega$

P-Channel

Parameters	Value	Unit
V_{DSS}	-30	V
$V_{GS(th)_{Typ}}$	-1.5	V
$I_D(@V_{GS}=-10V)$	-10	A
$R_{DS(ON)_{Typ}}(@V_{GS}=-10V)$	24.5	$m\Omega$
$R_{DS(ON)_{Typ}}(@V_{GS}=-4.5V)$	36.5	$m\Omega$



N-channel



P-channel

Ordering Information

Device	Marking	Package	Packing	Reel Size	Tape Width	Reel (pcs)
ZK3010DS	3010D	SOP8	Reel	N/A	N/A	4000

MAXIMUM RATINGS ($T_A=25^{\circ}C$ unless otherwise noted)

Parameter		Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage		V_{DS}	30	-30	V
Gate-Source Voltage		V_{GS}	± 20	± 20	V
Continuous Drain Current	$T_C = 25^{\circ}C$	I_D	5.6	-10	A
	$T_C = 70^{\circ}C$		3	-5	
Pulsed Drain Current		I_{DM}	60	-60	A
Maximum Power Dissipation	$T_A=25^{\circ}C$	P_D	6.0	5.5	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 To 150	-55 To 150	$^{\circ}C$

N-CH ELECTRICAL CHARACTERISTICS

$T_a = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Off characteristics						
Drain-source breakdown voltage	V _{(BR) DSS}	V _{GS} = 0V, I _D = 250uA	30			V
Zero gate voltage drain current	I _{DSS}	V _{DS} = 30V, V _{GS} =0V			100	nA
Gate-body leakage current	I _{GSS}	V _{DS} = 0V, V _{GS} =±20V			±100	nA
On characteristics						
Gate-threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	1.0	1.5	2.2	V
Static drain-source on-sate resistance	R _{DS(on)}	V _{GS} =10V, I _D =5.6A		18	25	mΩ
		V _{GS} =4.5V, I _D =5A		25	35	mΩ
Dynamic characteristics						
Input capacitance	C _{iss}	V _{DS} =20V,V _{GS} =0V f =1MHz		1700		pF
Output capacitance	C _{oss}			755		
Reverse transfer capacitance	C _{rss}			254		
Gate Resistance	R _g	f =1MHz		2.6		Ω
Switching characteristics						
Total gate charge	Q _g	V _{GS} =0 to 10V V _{DS} =20V, I _D =10A		20.0		nC
Gate-source charge	Q _{gs}			2.3		
Gate-drain charge	Q _{gd}			1.7		
Turn-on delay time	t _{d(on)}	V _{DD} =20V,I _D =5.4A V _{GS} =10V,R _{GEN} =1Ω		5		ns
Turn-on rise time	t _r			10		
Turn-off delay time	t _{d(off)}			16		
Turn-off fall time	t _f			7		
Drain-Source Diode Characteristics						
Drain-source diode forward voltage	V _{SD} ④	V _{GS} =0V, I _S =5.4A			1.2	V
Continuous drain-source diode forward current	I _S ①	T _C =25℃			5.6	A

Notes:

1. Guaranteed by design, not subject to production testing.

2. Pulse test; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

3. Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

P-CH ELECTRICAL CHARACTERISTICS

$T_a = 25\text{ }^{\circ}\text{C}$ unless otherwise specified

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Off characteristics						
Drain-source breakdown voltage	V _{(BR) DSS}	V _{GS} = 0V, I _D = -250uA	-30			V
Zero gate voltage drain current	I _{DSS}	V _{DS} = -30V, V _{GS} =0V			-1	μA
Gate-body leakage current	I _{GSS}	V _{DS} = 0V, V _{GS} =±20V			±100	nA
On characteristics						
Gate-threshold voltage	V _{GS(th)}	V _{DS} =-10V, I _D =-250uA	-1.0	-1.5	-2.2	V
Static drain-source on-sate resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-10A		24.5	32	mΩ
		V _{GS} =-4.5V, I _D =-5A		36.5	50	mΩ
Dynamic characteristics						
Input capacitance	C _{iss}	V _{DS} =-20V,V _{GS} =0V f =1MHz		1515		pF
Output capacitance	C _{oss}			800		
Reverse transfer capacitance	C _{rss}			25		
Gate Resistance	R _g	f =1MHz		12.8		Ω
Switching characteristics						
Total gate charge	Q _g	V _{GS} =0 to -10V V _{DS} =-20V, I _D =-10A		20		nC
Gate-source charge	Q _{gs}			33		
Gate-drain charge	Q _{gd}			5.6		
Turn-on delay time	t _{d(on)}	V _{DD} =-20V,R _L =2Ω V _{GS} =-10V,R _{GEN} =1Ω		10		ns
Turn-on rise time	t _r			9		
Turn-off delay time	t _{d(off)}			50		
Turn-off fall time	t _f			13		
Drain-Source Diode Characteristics						
Drain-source diode forward voltage	V _{SD} ④	V _{GS} =0V, I _S =-2A			-1.2	V
Continuous drain-source diode forward current	I _S ①	T _C =25℃			-5.6	A

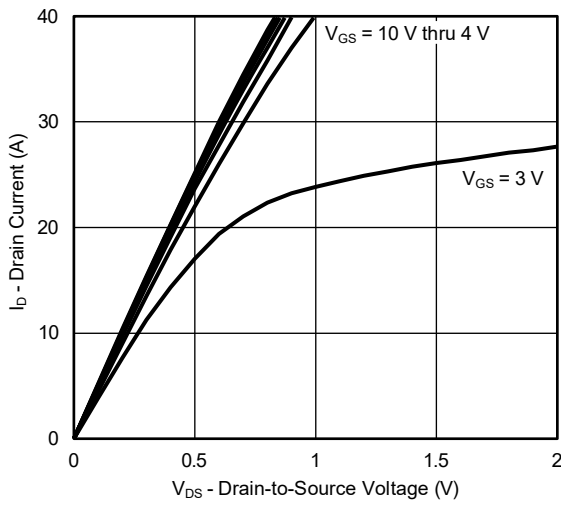
Notes:

1. Guaranteed by design, not subject to production testing.

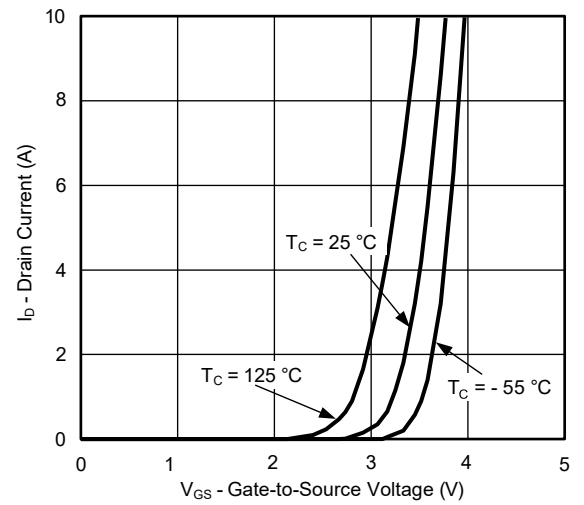
2. Pulse test; pulse width $\leq 300\text{ }\mu s$, duty cycle $\leq 2\%$.

3. Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

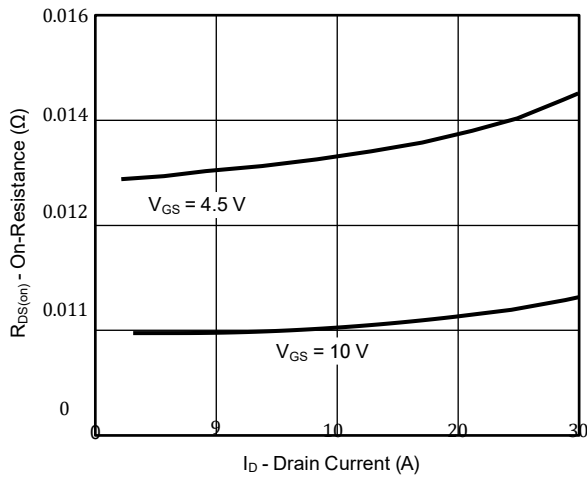
N-CH Typical Electrical & Thermal Characteristics



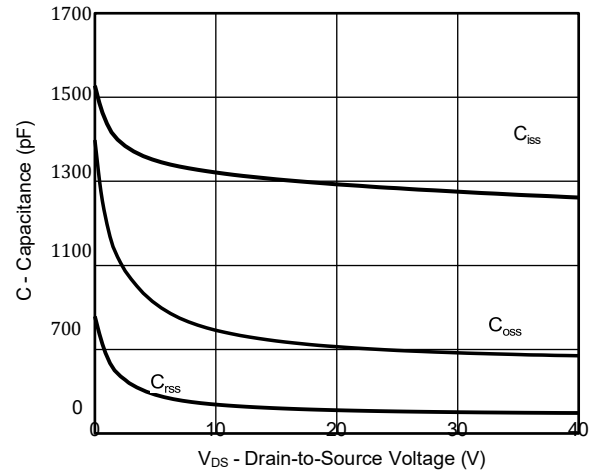
Output Characteristics



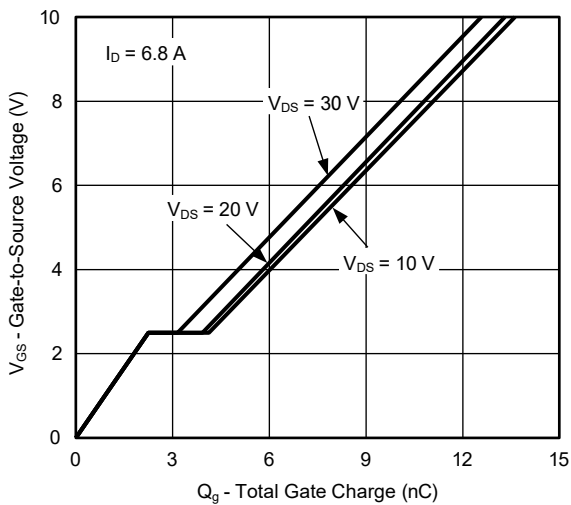
Transfer Characteristics



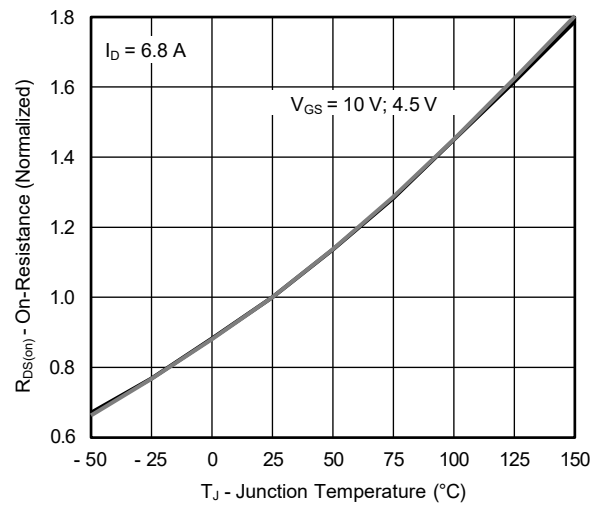
On-Resistance vs. Drain Current and Gate Voltage



Capacitance

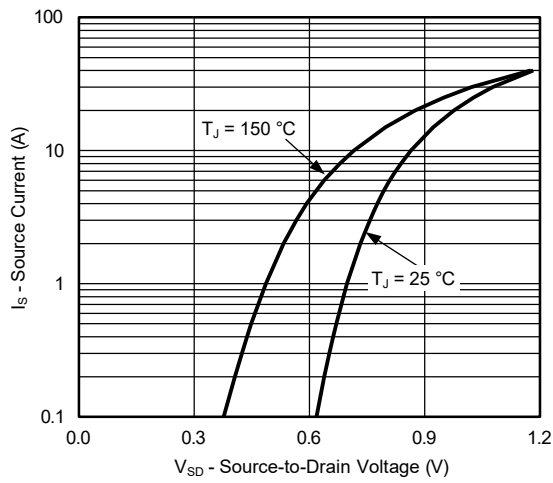


Gate Charge

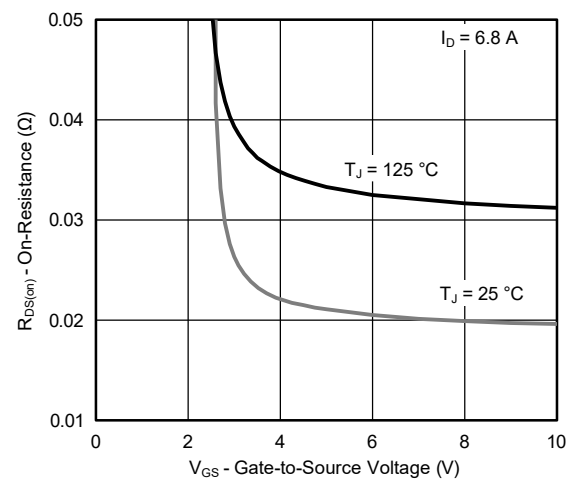


On-Resistance vs. Junction Temperature

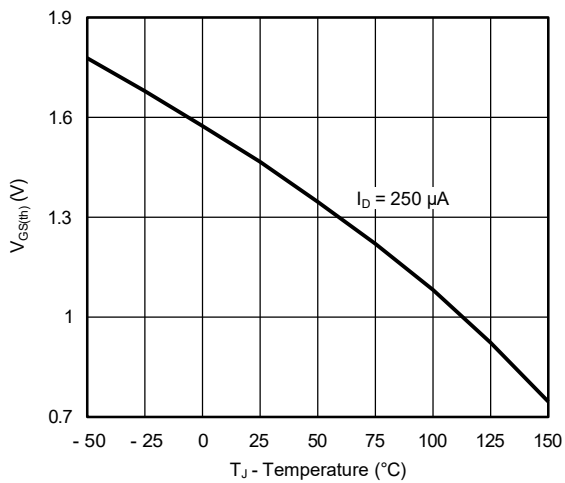
N-CH Typical Electrical & Thermal Characteristics



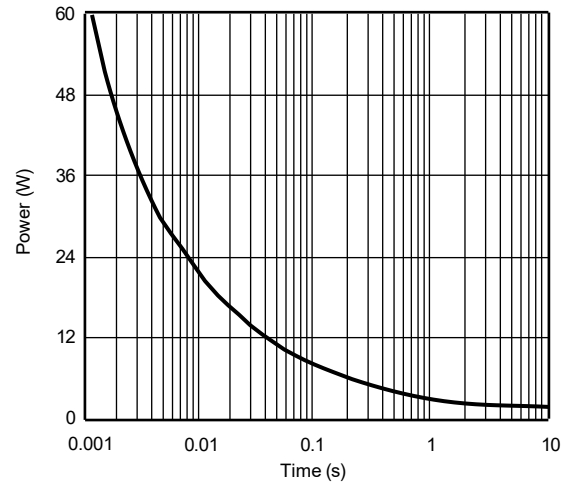
Source-Drain Diode Forward Voltage



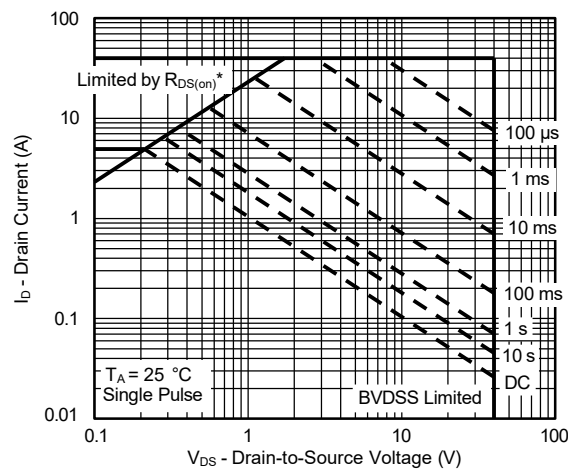
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

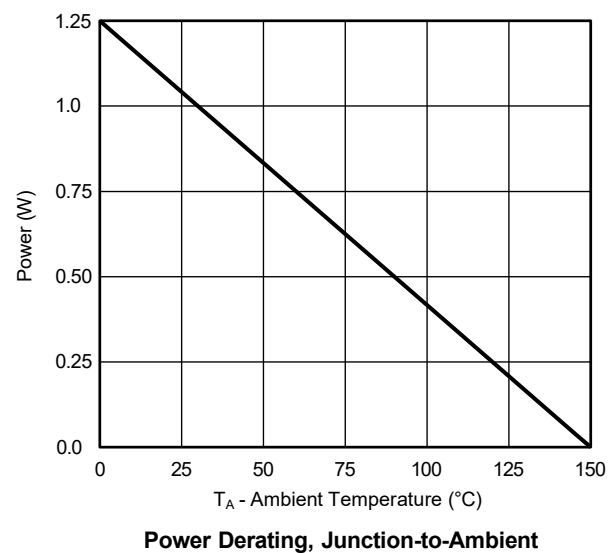
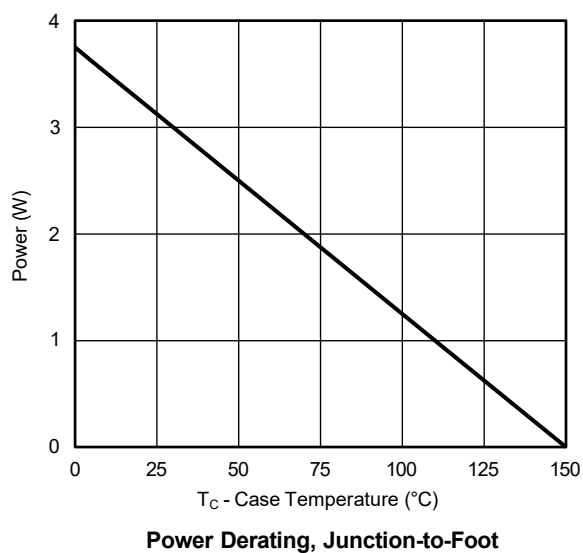
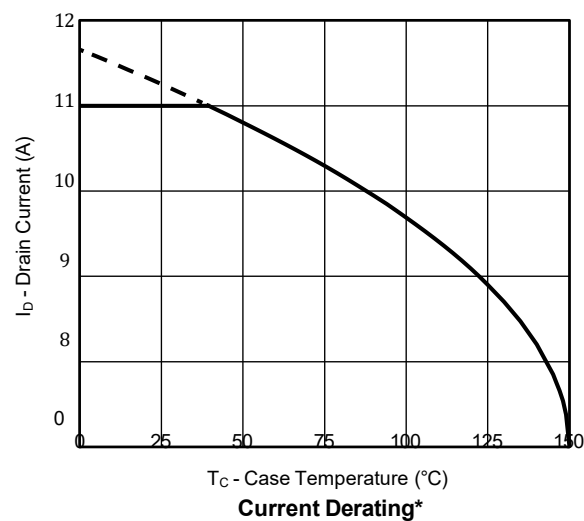


Single Pulse Power, Junction-to-Ambient

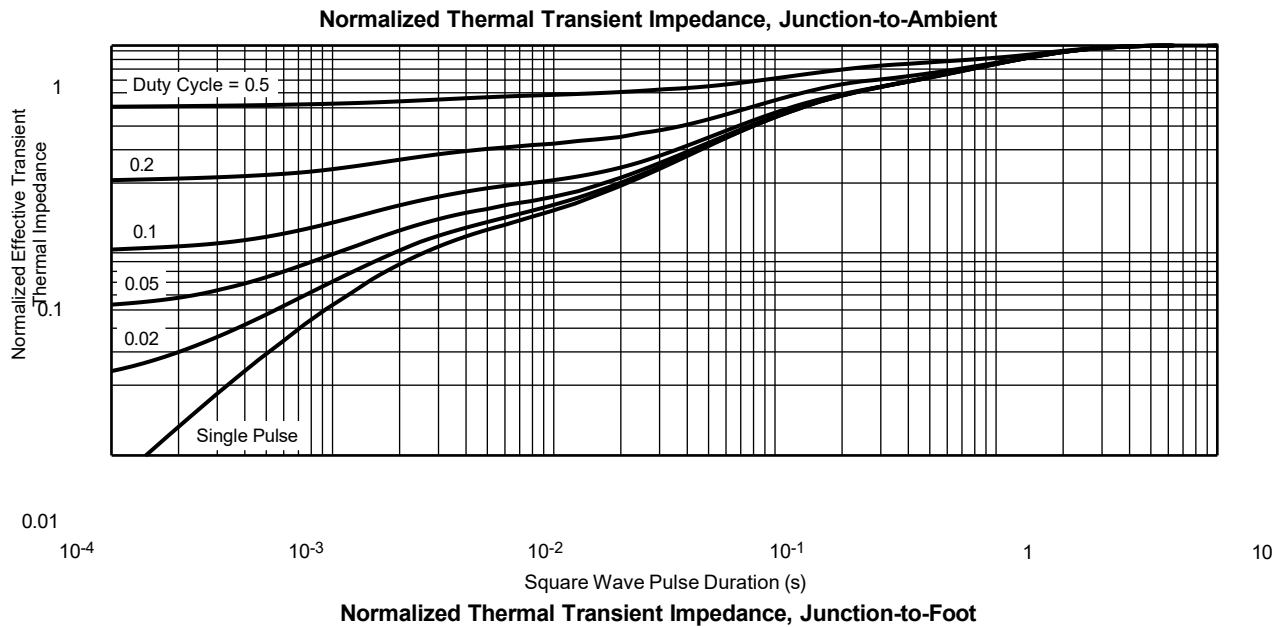
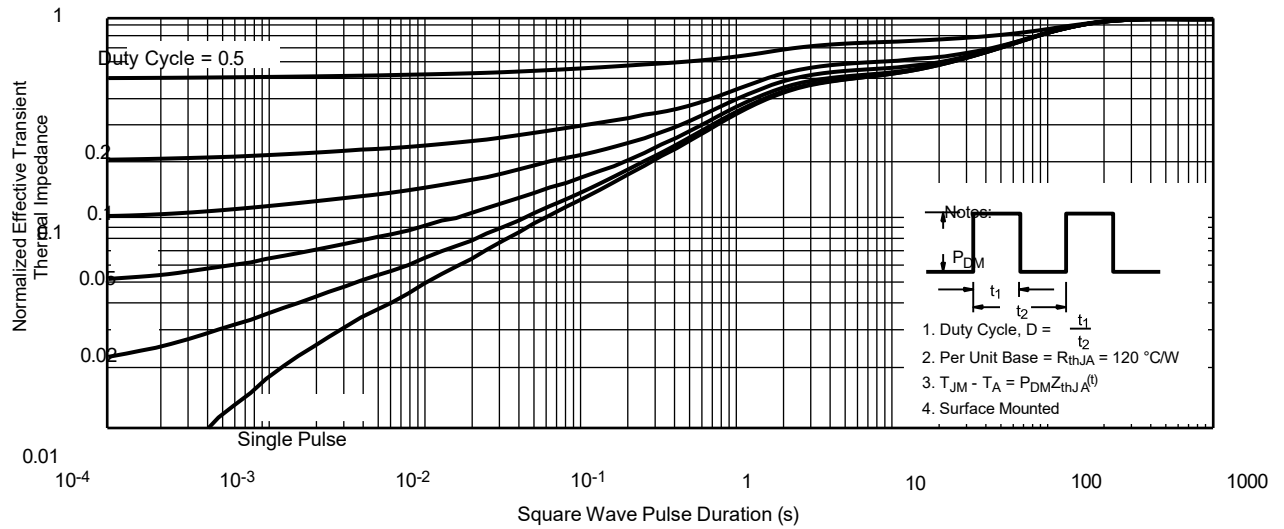


* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

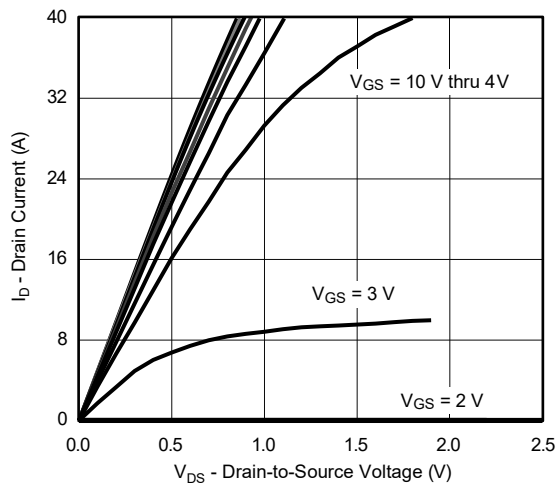
Safe Operating Area, Junction-to-Ambient



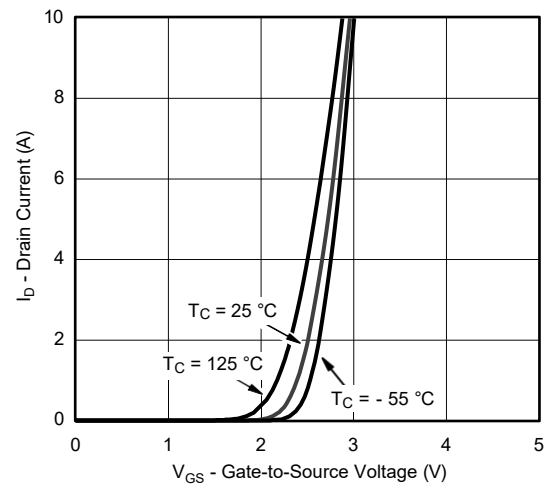
N-CH Typical Electrical & Thermal Characteristics



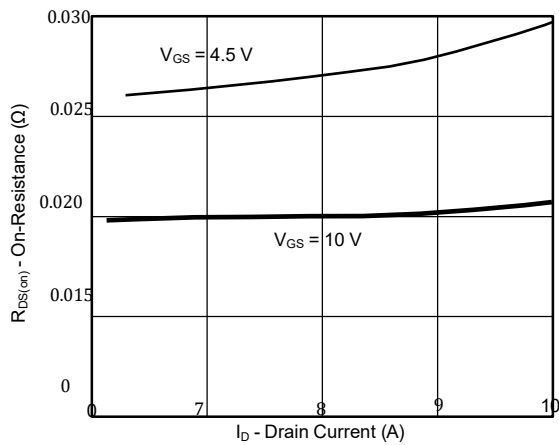
P-CH Typical Electrical & Thermal Characteristics



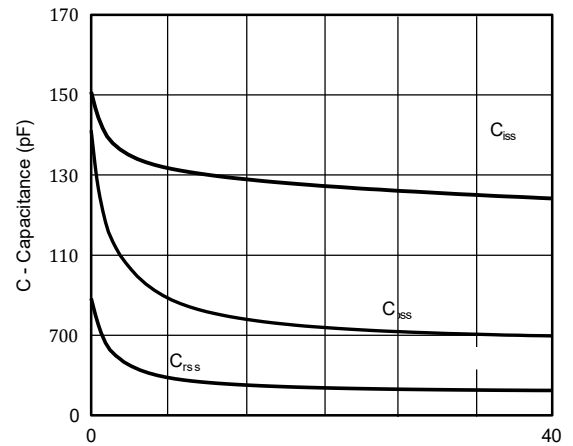
Output Characteristics



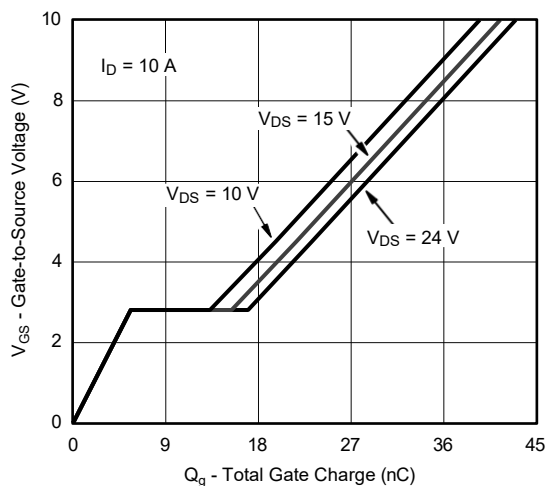
Transfer Characteristics



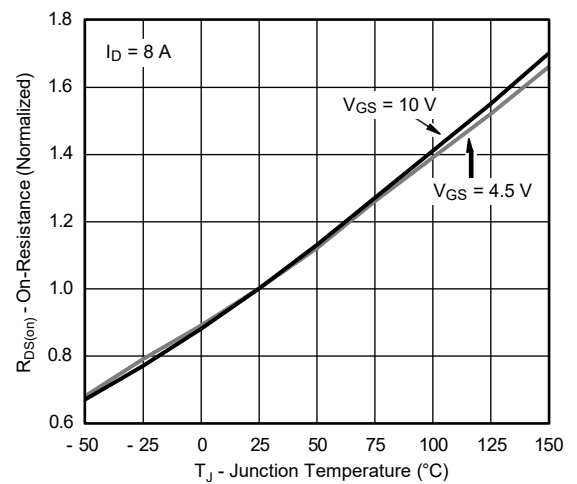
On-Resistance vs. Drain Current and Gate Voltage



Capacitance

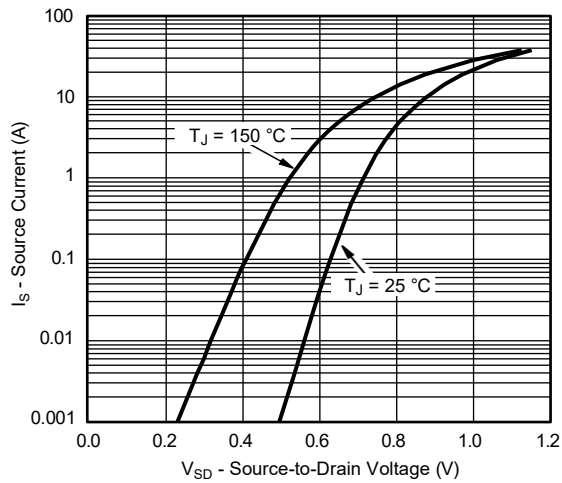


Gate Charge

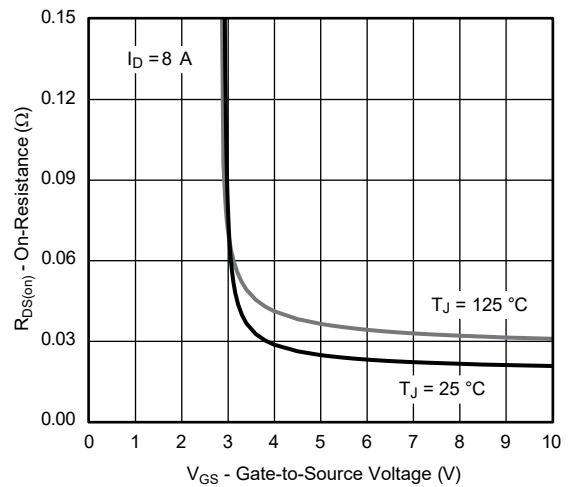


On-Resistance vs. Junction Temperature

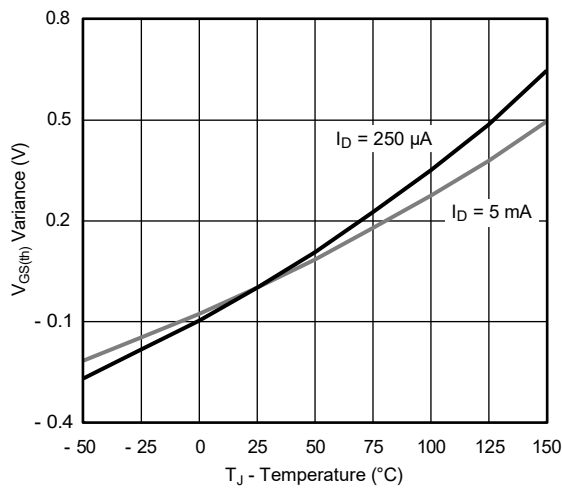
P-CH Typical Electrical & Thermal Characteristics



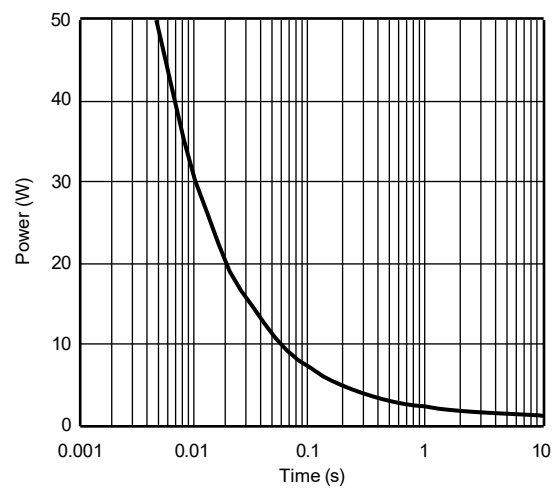
Source-Drain Diode Forward Voltage



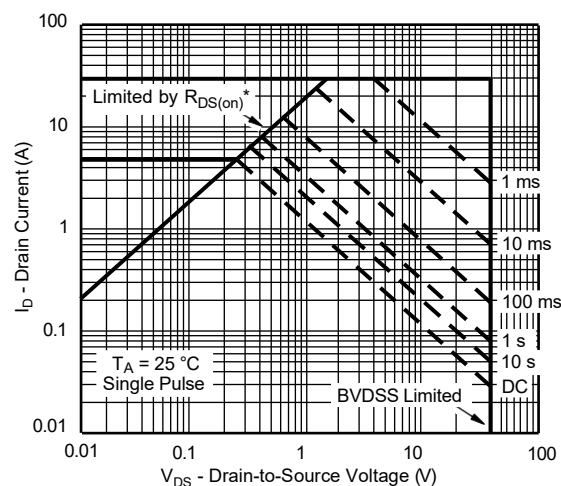
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



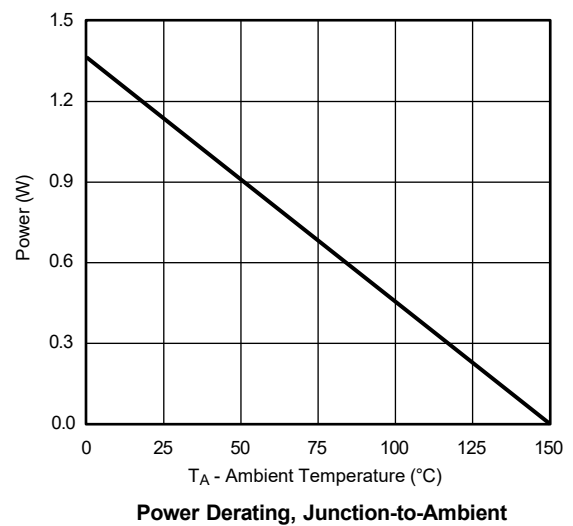
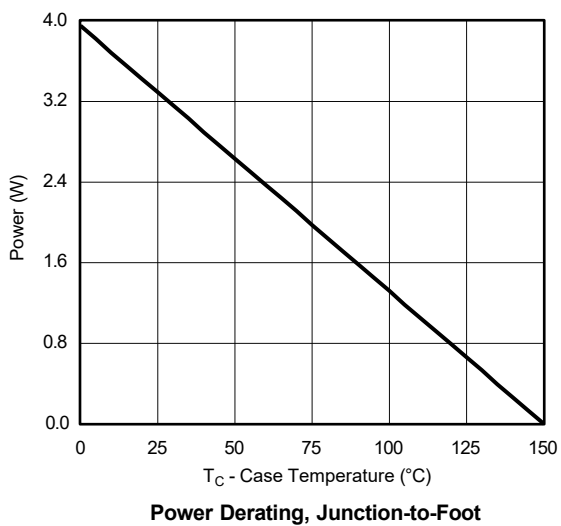
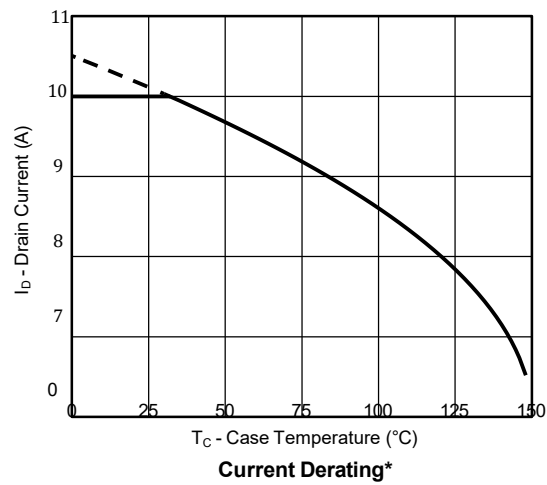
Single Pulse Power, Junction-to-Ambient



* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

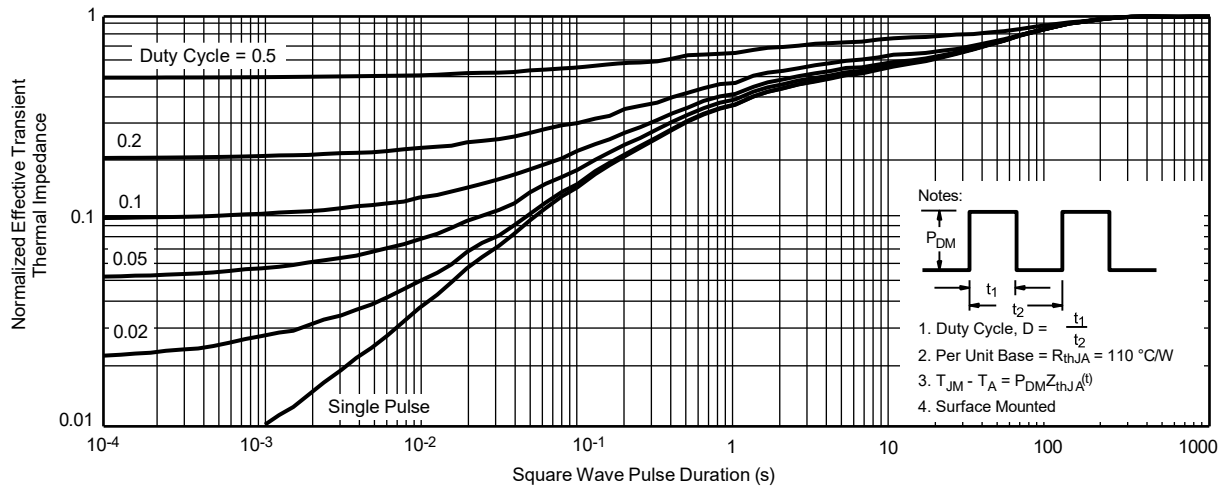
Safe Operating Area, Junction-to-Ambient

P-CH Typical Electrical & Thermal Characteristics

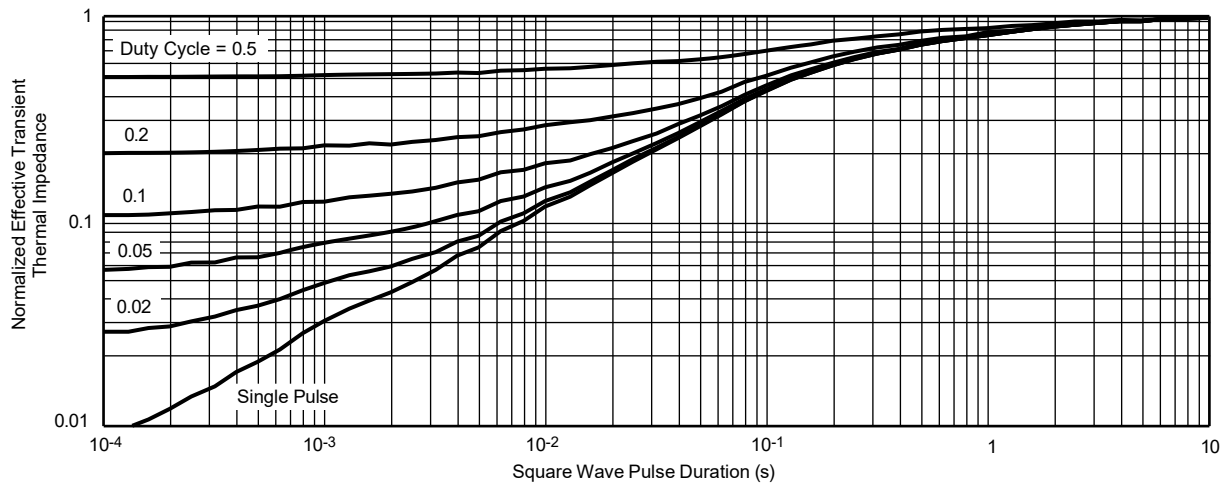


* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

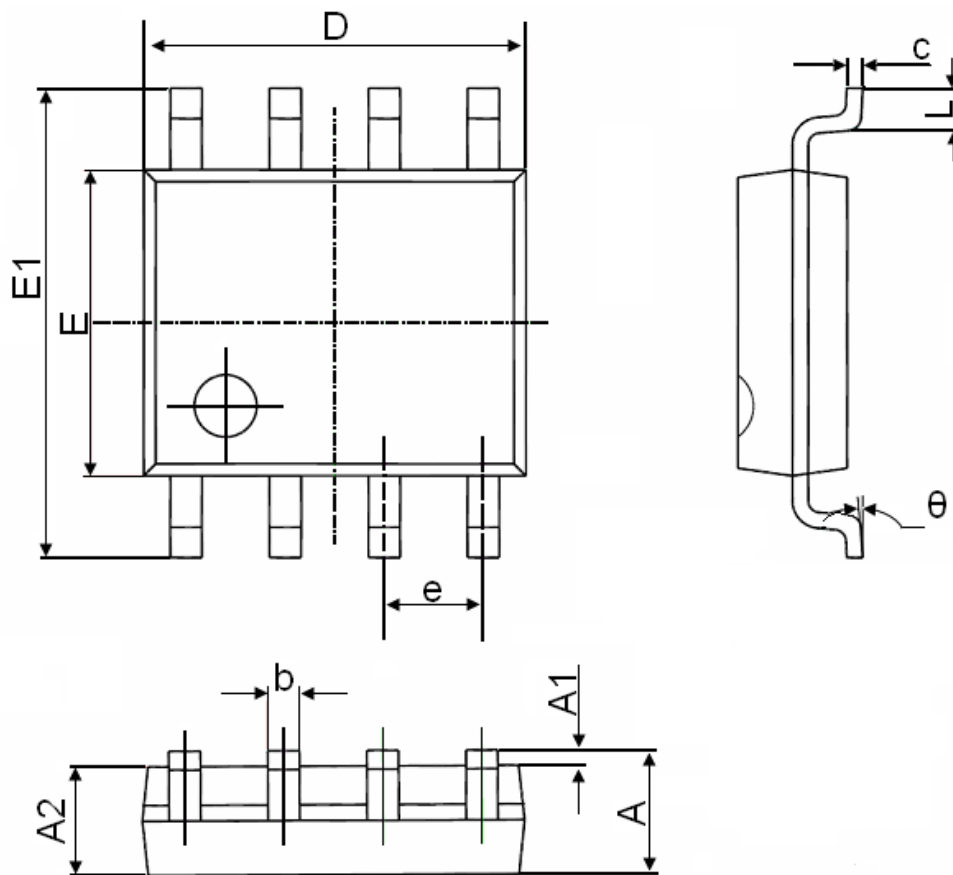
P-CH Typical Electrical & Thermal Characteristics



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°