

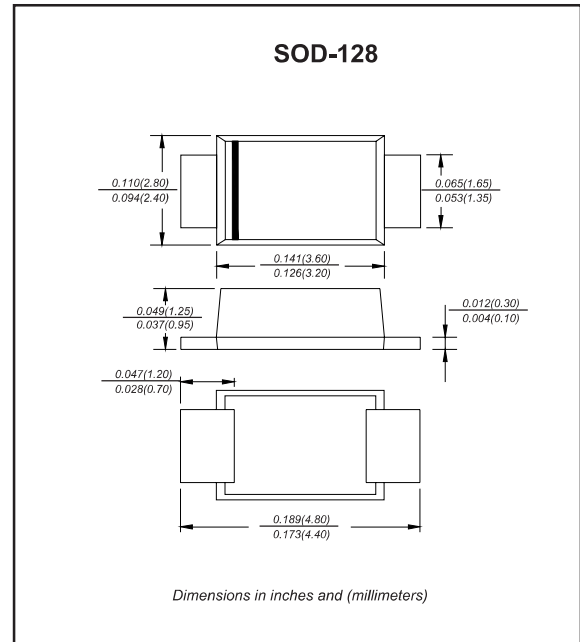
Features

- ◆ The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- ◆ For surface mounted applications
- ◆ Metal silicon junction,majority carrier conduction
- ◆ Low power loss,high efficiency
- ◆ Built-in strain relief,ideal for automated placement
- ◆ High forward surge current capability
- ◆ High temperature soldering guaranteed:
260°C/10 seconds at terminals
- ◆ Compliant to RoHS Directive 2011/65/EU
- ◆ Compliant to Halogen-free

Mechanical data

- ◆ **Case:** JEDEC SOD-128 molded plastic body
- ◆ **Terminals:** Solder plated, solderable per MIL-STD-750, Method 2026
- ◆ **Polarity:** Color band denotes cathode end
- ◆ **Mounting Position:** Any

Package outline



Maximum ratings and Electrical Characteristics (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current	See Fig.2	I_O			5.0	A
Forward surge current	8.3ms single half sine-wave (JEDEC methode)	I_{FSM}			120	A
Reverse current	$V_R = V_{RRM} \quad T_A = 25^{\circ}\text{C}$	I_R			0.3	mA
	$V_R = V_{RRM} \quad T_A = 100^{\circ}\text{C}$				50	
Thermal resistance	Junction to ambient NOTE 1	$R_{\theta JA}$		45		$^{\circ}\text{C/W}$
Diode junction capacitance	f=1MHz and applied 4V DC reverse voltage	C_J		800		pF
Storage temperature		T_{STG}	-55		+150	$^{\circ}\text{C}$

Note: 1.P.C.B. mounted with 2.0x2.0" (5.0x5.0cm) copper pad areas

SYMBOLS	V_{RRM}^{*1} (V)	V_{RMS}^{*2} (V)	V_R^{*3} (V)	V_F^{*4} (V)	Operating temperature T_J , ($^{\circ}\text{C}$)
PMEG4050EP	40	28	40	0.45	-55 to +150

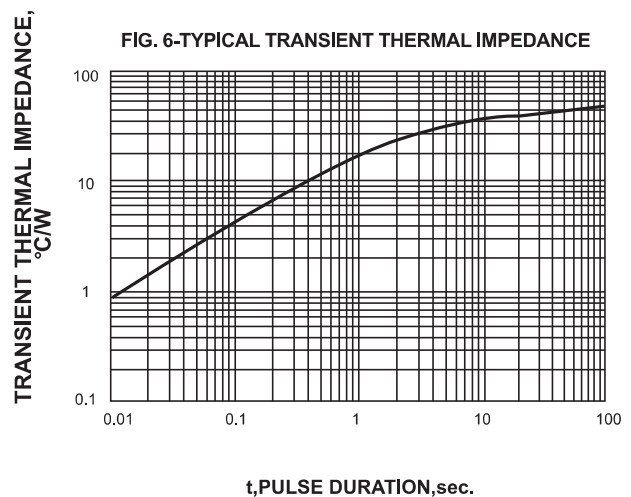
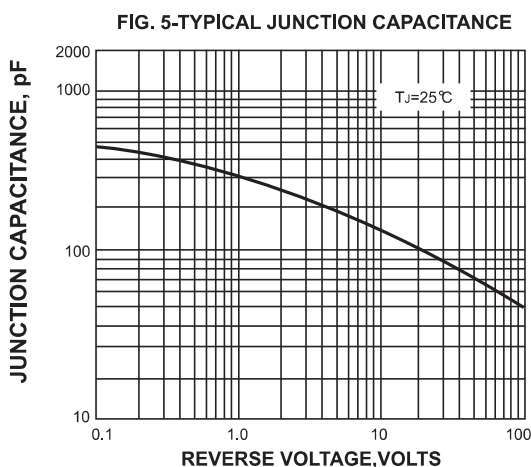
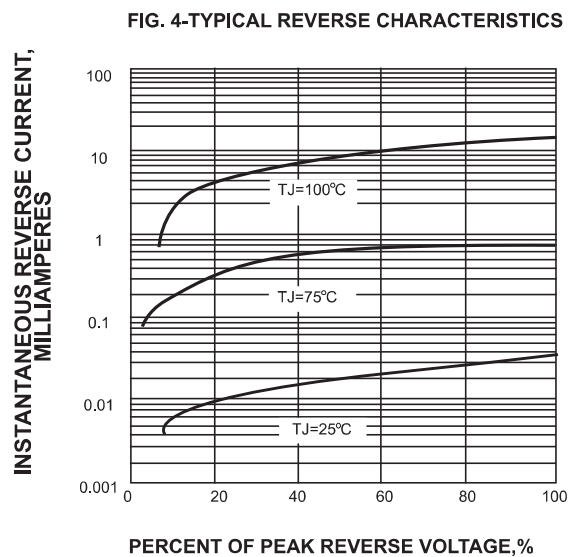
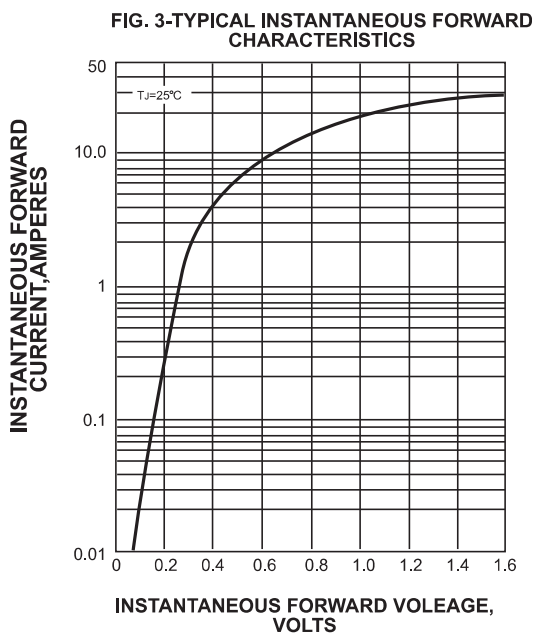
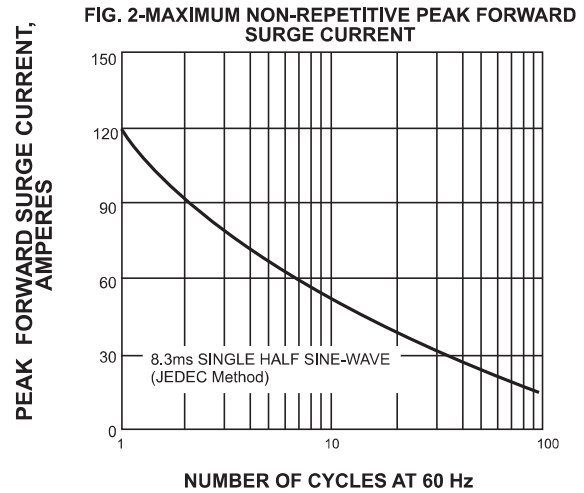
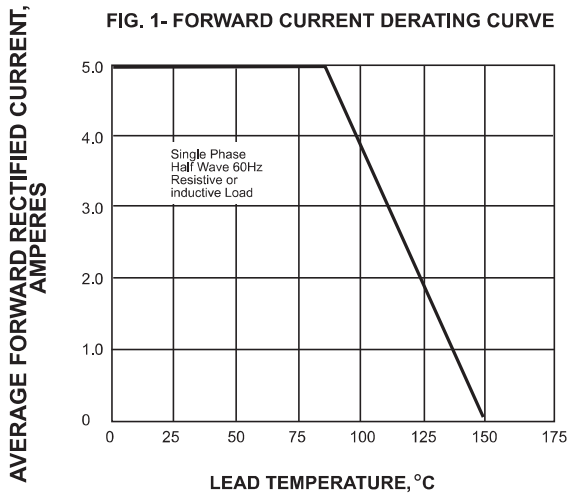
*1 Repetitive peak reverse voltage

*2 RMS voltage

*3 Continuous reverse voltage

*4 Maximum forward voltage@ $I_F=5.0\text{A}$

Rating and characteristic curves



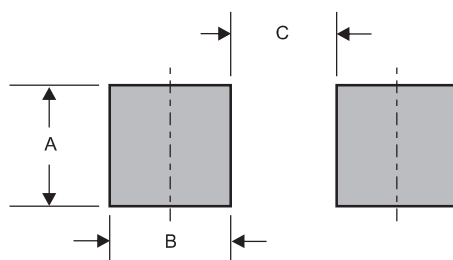
Pinning information

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode		

Marking

Type number	Marking code
PMEG4050EP	AF

Suggested solder pad layout

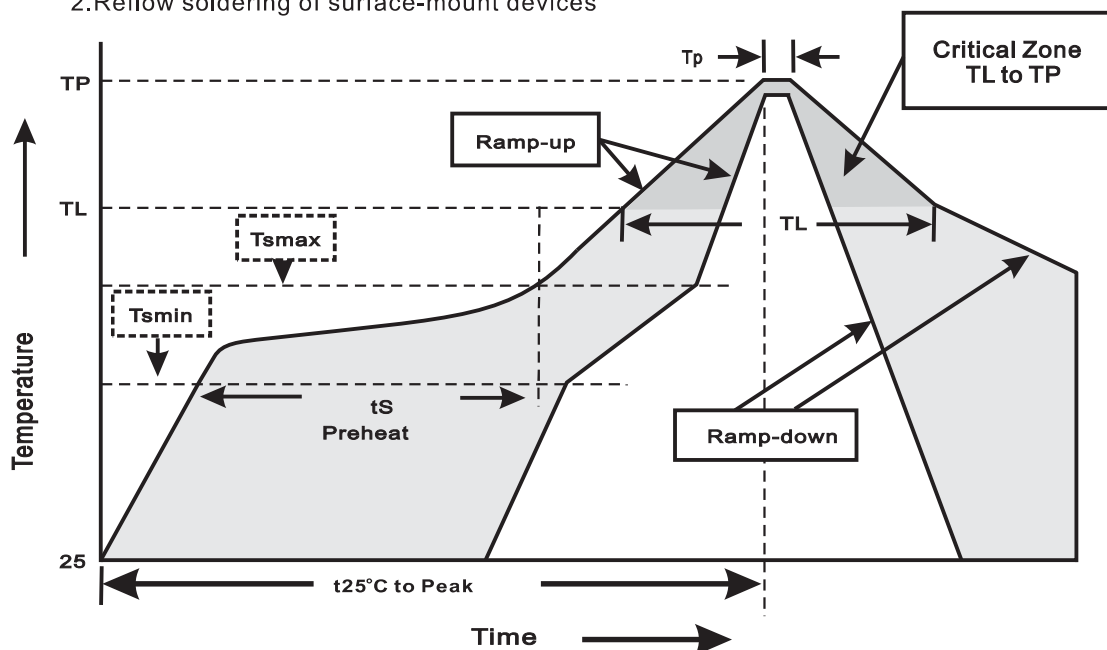


Dimensions in inches and (millimeters)

PACKAGE	A	B	C
SOD-128	0.110 (2.80)	0.063 (1.60)	0.087 (2.20)

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(T_L to T_P)	$<3^{\circ}\text{C}/\text{sec}$
Preheat -Temperature Min(T_{min}) -Temperature Max(T_{max}) -Time(min to max)(t_s)	150°C 200°C $60\sim 120\text{sec}$
T_{max} to T_L -Ramp-upRate	$<3^{\circ}\text{C}/\text{sec}$
Time maintained above: -Temperature(T_L) -Time(t_L)	217°C $60\sim 260\text{sec}$
Peak Temperature(T_P)	$255^{\circ}\text{C}-0/+5^{\circ}\text{C}$
Time within 5°C of actual Peak Temperature(t_P)	$10\sim 30\text{sec}$
Ramp-down Rate	$<6^{\circ}\text{C}/\text{sec}$
Time 25°C to Peak Temperature	$<6\text{minutes}$