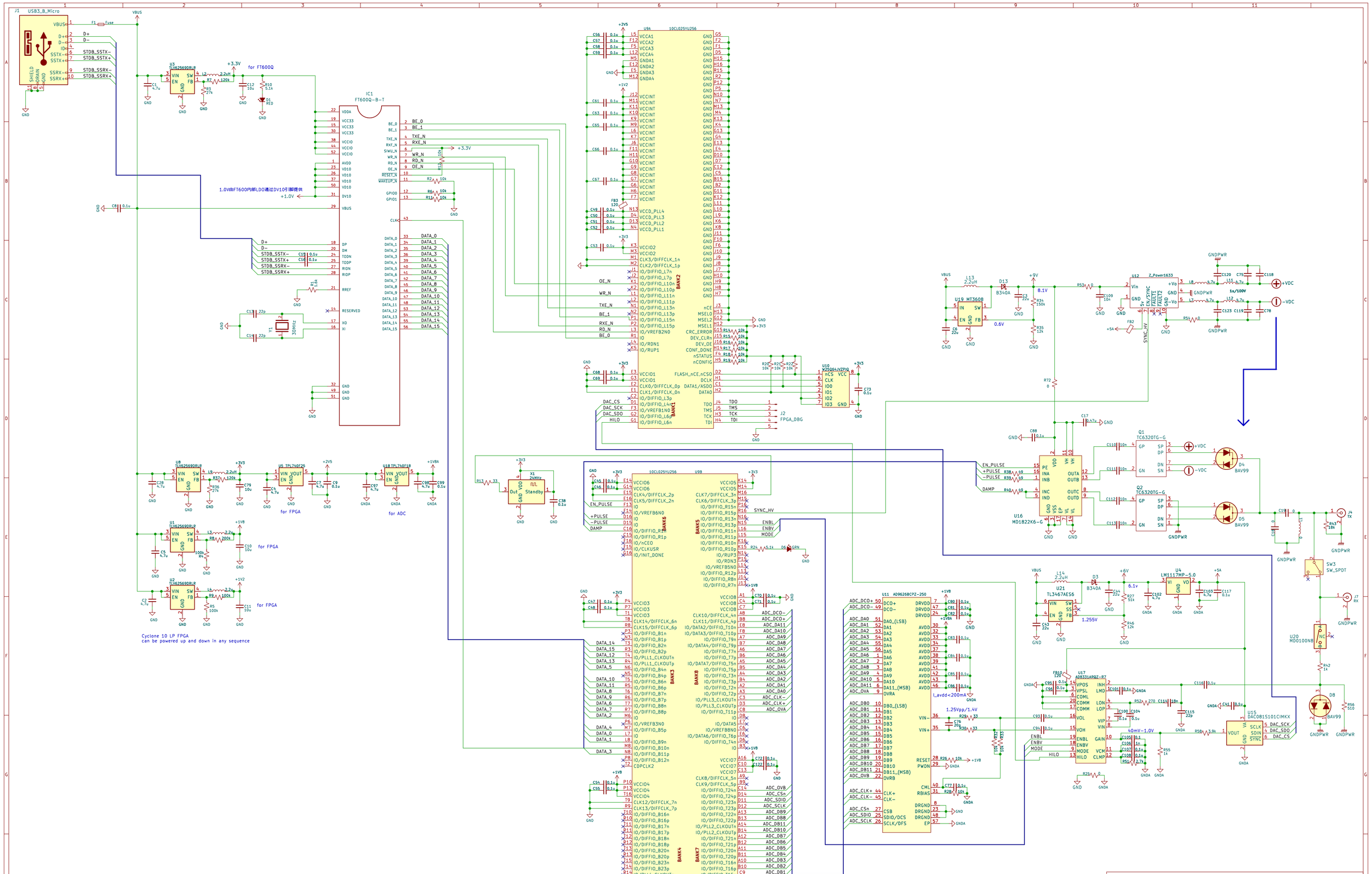
J1 USB3_B_Micro
IC1 FT600Q-B-T
U9A 10CL025YU256
U9B 10CL025YU256
U11 AD9626BCPZ-250
U16 MD1B22K6-G
U19 MT3608
U21 TL3467AES6
U17 AD8331ARQZ-R7
U15 DAC081S101CIMKX
U20 MD0100NB
Q1 TC6320TG-G
Q2 TC6320TG-G
D4 BAV99
D5 BAV99
D8 BAV99
SW3 SW_SPDT
J4 TX
J7 RX
J2 FPGA_DBG
X1 24MHz
Y1 30MHz
+VDC
-VDC
GNDPWR
GND
Cyclone 10 LP FPGA
can be powered up and down in any sequence
1.0V由FT600内部LDO通过DV10引脚提供
for FT600Q
for FPGA
for ADC
EETree Info & Tech Limited
Sheet: /
File: FTDI_FPGA.kicad_sch
Title: FTDI+FPGA Based Ultrasonic Detector
Size: A2
Date: 2024-12-27
Rev: 1.0
KiCad E.D.A. 8.0.2
Id: 1/1