

AP9971GJ-VB Datasheet

N-Channel 60 V (D-S) MOSFET

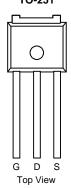
PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ.)		
60	0.032 at V _{GS} = 10 V	35 ^d	21.7		
00	0.037 at $V_{GS} = 4.5 \text{ V}$	30 ^d	21.7		

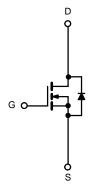
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_g and UIS Tested
 Compliant to RoHS Directive 2002/95/EC









N-Channel MOSFET

APPLICATIONS

- Power Supply
 - Secondary Synchronous Rectification
- DC/DC Converter

ABSOLUTE MAXIMUM RATINGS	$T_C = 25$ °C, unless oth	erwise noted			
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	60	V		
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Drain Current (T _{.I} = 150 °C)	T _C = 25 °C	I _D	35 ^d		
Continuous Diam Current (1) = 130 C)	T _C = 70 °C	O O	30 ^d	A	
Pulsed Drain Current		I _{DM}	100] ^	
Avalanche Current		I _{AS}	40]	
Single Avalanche Energy ^a	L = 0.1 mH	E _{AS}	80	mJ	
Mariana Barras Disain ation 8	T _C = 25 °C	P _D	59.5 ^b	W	
Maximum Power Dissipation ^a	T _A = 25 °C ^c		2.7] vv	
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Limit	Unit
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	46	°C/W
Junction-to-Case (Drain)	R _{thJC}	2.1	C/VV

Notes:

- a. Duty cycle \leq 1 %.
- b. See SOA curve for voltage derating.c. When mounted on 1" square PCB (FR-4 material).
- d. Package limited.



SPECIFICATIONS $T_J = 25^{\circ}$	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static	Symbol	rest conditions	IVIIII.	тур.	IVIAA.	Offic
	1	V 0.V 1 050 A		l		
Drain-Source Breakdown Voltage	V _{DS}	$V_{DS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	60			V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	2.0		3.5	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 250	nA
		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$			1	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 \text{ °C}$			50	μΑ
		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 150 ^{\circ}\text{C}$			250	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α
Drain-Source On-State Resistance ^a	D	$V_{GS} = 10 \text{ V}, I_{D} = 12 \text{ A}$		0.032		Ω
Dialii-Source On-State Resistance	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$		0.037		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 10 A		110		S
Dynamic ^b						
Input Capacitance	C _{iss}			1100		
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 30 \text{ V}, f = 1 \text{ MHz}$		281		pF
Reverse Transfer Capacitance	C _{rss}			130		
Total Cata Channel	Qg	V _{DS} = 30 V, V _{GS} = 10 V, I _D = 10 A		46		nC
Total Gate Charge ^c				28		
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 30 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 10 \text{ A}$		7		
Gate-Drain Charge ^c	Q _{gd}			6.7		
Gate Resistance	R _g	f = 1 MHz	0.4	2	4	Ω
Turn-On Delay Time ^c	t _{d(on)}			8	16	
Rise Time ^c	t _r	$V_{DD} = 30 \text{ V, R}_{1} = 1.5 \Omega$		9	18	ns
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 10 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		35	53	
Fall Time ^c	t _f	, and the second		9	18	
Drain-Source Body Diode Ratings an	nd Characteris	stics T _C = 25 °C ^b		<u> </u>		
Continuous Current	Is				50	
Pulsed Current	I _{SM}				100	Α
Forward Voltage ^a	V _{SD}	I _F = 10 A, V _{GS} = 0 V		0.75	1.5	V
Reverse Recovery Time	t _{rr}	1 / 60		34	51	ns
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = 10 A, dl/dt = 100 A/μs		2	3	A
Reverse Recovery Charge	Q _{rr}	,,		34	51	nC

Notes:

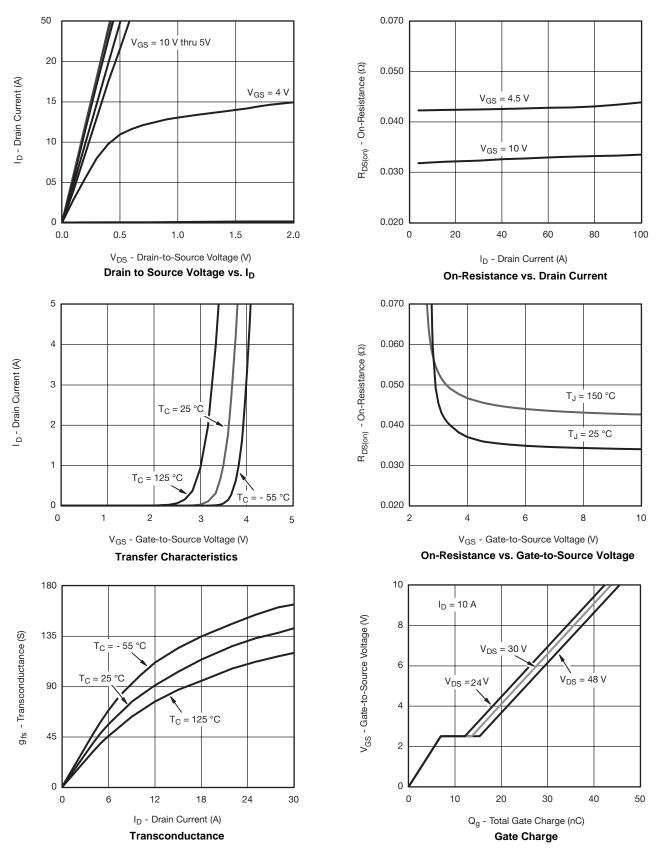
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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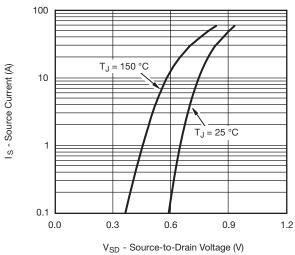


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

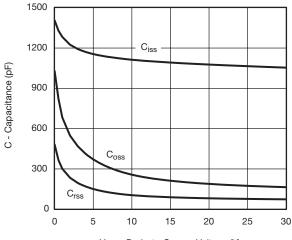




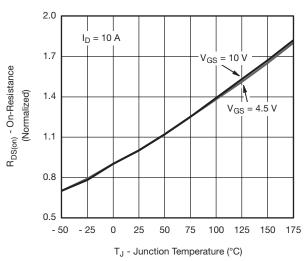
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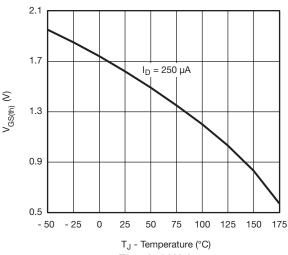
Source-Drain Diode Forward Voltage



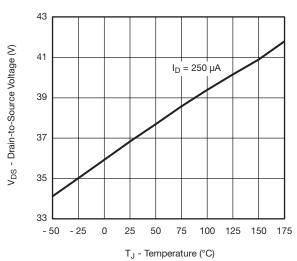
 V_{DS} - Drain-to-Source Voltage (V) $\label{eq:capacitance}$



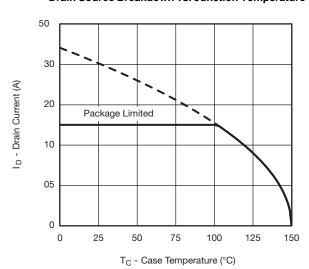
On-Resistance vs. Junction Temperature



Threshold Voltage



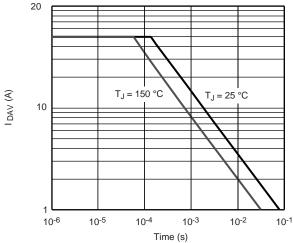
Drain Source Breakdown vs. Junction Temperature

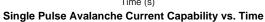


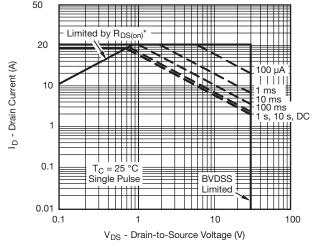
Current Derating



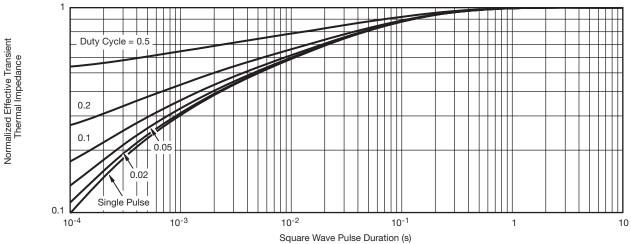
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







 * V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified $\mbox{\bf Safe Operating Area}$

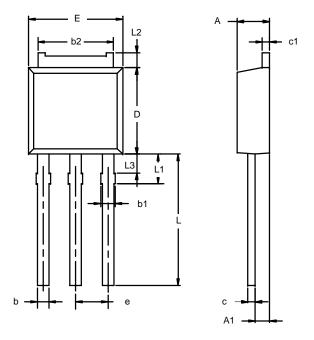


Normalized Thermal Transient Impedance, Junction-to-Case

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TO-251AA (DPAK)



Note: Dimension L3 is for reference only.

Min 2.21 0.89 0.71 0.76 5.23 0.46 0.46	Max 2.38 1.14 0.89 1.14 5.43 0.58	Min 0.087 0.035 0.028 0.030 0.206 0.018	0.094 0.045 0.035 0.045 0.214	
0.89 0.71 0.76 5.23 0.46	1.14 0.89 1.14 5.43 0.58	0.035 0.028 0.030 0.206	0.045 0.035 0.045 0.214	
0.71 0.76 5.23 0.46	0.89 1.14 5.43 0.58	0.028 0.030 0.206	0.035 0.045 0.214	
0.76 5.23 0.46	1.14 5.43 0.58	0.030	0.045 0.214	
5.23 0.46	5.43 0.58	0.206	0.214	
0.46	0.58			
		0.018	0.023	
0.46				
	0.58	0.018	0.023	
5.97	6.22	0.235	0.245	
6.48	6.73	0.255	0.265	
2.28 BSC		0.090 BSC		
8.89	9.53	0.350	0.375	
1.91	2.28	0.075	0.090	
0.89	1.27	0.035	0.050	
1.15	1.52	0.045	0.060	
1	6.48 2.28 8.89 1.91 0.89 1.15	6.48 6.73 2.28 BSC 8.89 9.53 1.91 2.28 0.89 1.27	6.48 6.73 0.255 2.28 BSC 0.090 8.89 9.53 0.350 1.91 2.28 0.075 0.89 1.27 0.035 1.15 1.52 0.045	

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