

AP9971GD-VB Datasheet

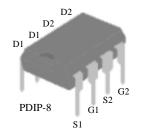
Dual N-Channel 60 V (D-S) MOSFET

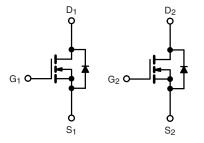
PRODUCT SUMMARY				
V _{DS} (V)	60			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 V$	0.033			
$R_{DS(on)} (\Omega)$ at $V_{GS} = 4.5 V$	0.045			
I _D (A) per leg	7			
Configuration	Dual			

FEATURES

- TrenchFET® power MOSFET
- 100 % $\rm R_g$ and UIS tested







N-Channel MOSFET N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V _{DS}	60	V	
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Drain Current	T _C = 25 °C	I	7		
	T _C = 125 °C	I _D	4		
Continuous Source Current (Diode Conduction) a		I _S	3.6	А	
Pulsed Drain Current ^b		I _{DM}	28		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	18		
Single Pulse Avalanche Energy	L = 0.1 IIIH	E _{AS}	16.2	mJ	
Maximum Power Dissipation ^b	T _C = 25 °C	PD	4	W	
	T _C = 125 °C	гD	1.3	٧V	
Operating Junction and Storage Temperature F	Range	T _J , T _{stg}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient	PCB Mount ^c	R _{thJA}	110	°C/W	
Junction-to-Foot (Drain)		R _{thJF}	34	0/10	

Notes

- a. Package limited.
- b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.
- c. When mounted on 1" square PCB (FR4 material).

PARAMETER	SYMBOL	vise noted) TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static	OTMBOL			WIIN.		INAA.	
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 µA		60	-	-	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$		1.5	2.0	2.5	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$		-	-	± 100	nA
Zero Gate Voltage Drain Current		$V_{GS} = 0 V$	$V_{DS} = 60 \text{ V}$			1	
	I _{DSS}	V _{GS} = 0 V	V _{DS} = 60 V, T _J = 125 °C	-	-	50	μA
		$V_{GS} = 0 V$	$V_{DS} = 60 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$	-	-	150	
On-State Drain Current ^a	I _{D(on)}	$V_{GS} = 10 V$	$V_{DS} \ge 5 V$	20	-	-	А
		$V_{GS} = 10 V$	I _D = 4.5 A	-	0.033	-	Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 10 V$	$I_D = 4.5 \text{ A}, \text{ T}_J = 125 \text{ °C}$	-	0.066	-	
	US(on)	$V_{GS} = 10 V$	I _D = 4.5 A, T _J = 175 °C	-	0.081	-	
		$V_{GS} = 4.5 V$	$I_D = 4 A$	-	0.045	-	
Forward Transconductance ^f	9fs	V _{DS}	= 15 V, I _D = 4.5 A	-	15	-	S
Dynamic ^b							
Input Capacitance	C _{iss}			-	600	750	
Output Capacitance	C _{oss}	$V_{GS} = 0 \ V \qquad V_{DS} = 25 \ V, \ f = 1 \ MHz$	-	110	140	pF	
Reverse Transfer Capacitance	C _{rss}			-	50	62	1
Total Gate Charge ^c	Qg			-	11.7	18	
Gate-Source Charge ^c	Q _{gs}	$V_{GS} = 10 V$	$V_{DS} = 30 \text{ V}, \text{ I}_{D} = 5.3 \text{ A}$	-	1.8	2.7	nC
Gate-Drain Charge ^c	Q _{gd}			-	2.8	4.2	
Gate Resistance	Rg	f = 1 MHz		1.3	-	6	Ω
Turn-On Delay Time ^c	t _{d(on)}				7	11	
Rise Time ^c	t _r	$\label{eq:VDD} \begin{array}{l} V_{DD}=30 \text{ V}, \text{ R}_L=6.8 \ \Omega \\ I_D\cong 4.4 \text{ A}, \text{ V}_{GEN}=10 \text{ V}, \text{ R}_g=1 \ \Omega \end{array}$		-	3.3	5	- ns
Turn-Off Delay Time ^c	t _{d(off)}			-	22.4	33.5	
Fall Time ^c	t _f			-	2.1	3.2	
Source-Drain Diode Ratings and Chara	acteristics ^b				1	I	
Pulsed Current ^a	I _{SM}			-	-	28	Α
Forward Voltage	V _{SD}	$I_{\rm F} = 2 {\rm A}, {\rm V}_{\rm GS} = 0 {\rm V}$		-	0.75	1.1	v

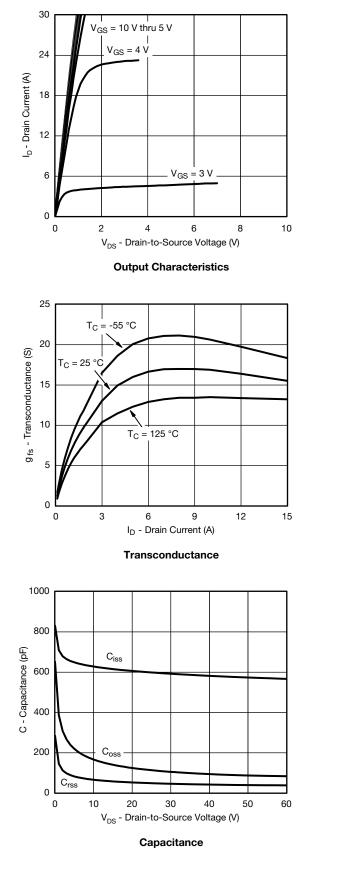
Notes

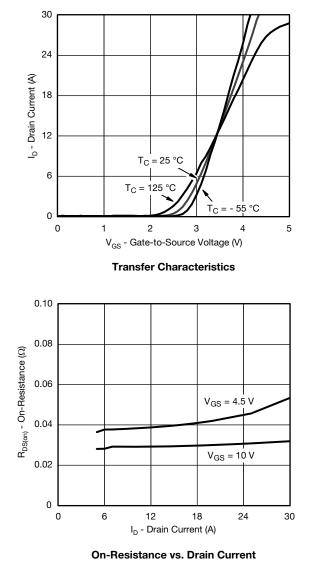
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

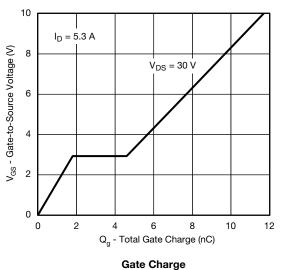
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TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)

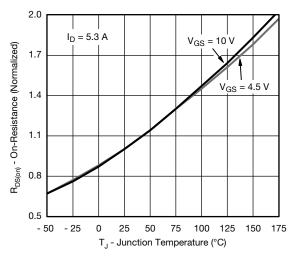




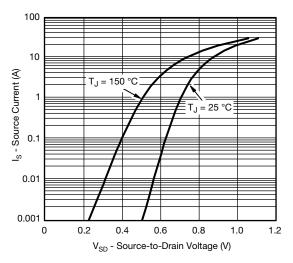




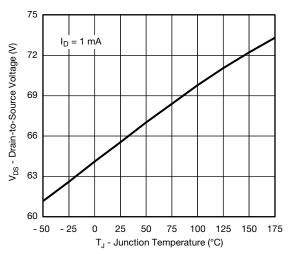
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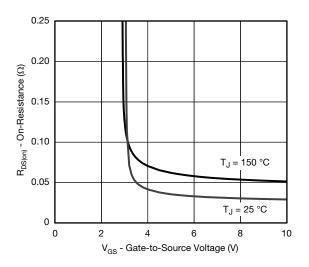
On-Resistance vs. Junction Temperature



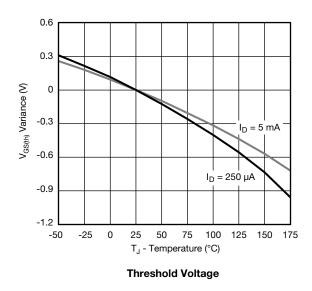
Source Drain Diode Forward Voltage



Drain Source Breakdown vs. Junction Temperature

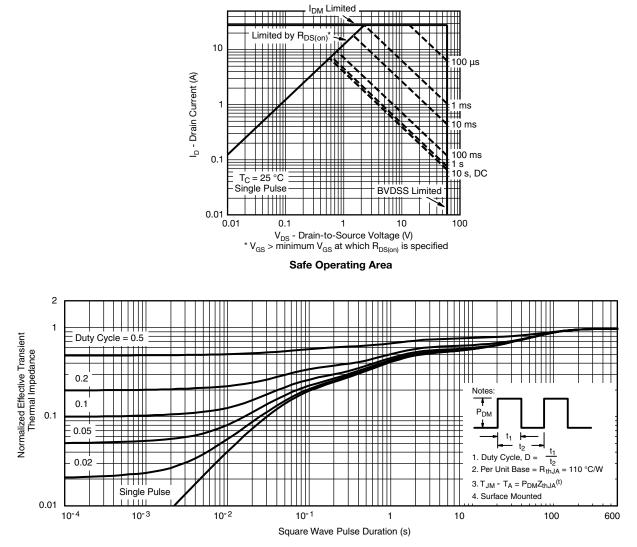


On-Resistance vs. Gate-to-Source Voltage





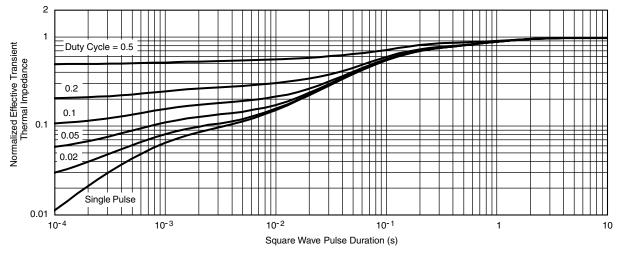
THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



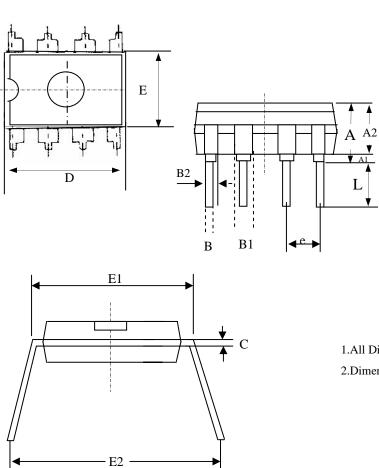
THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot



Package Outline : PDIP-8



SYMBOLS	Millimeters			
	MIN	NOM	MAX	
Α	3.60	4.50	5.40	
A1	0.38			
A2	2.90	3.95	5.00	
В	0.36	0.46	0.56	
B 1	1.10	1.45	1.80	
B2	0.76	0.98	1.20	
С	0.20	0.28	0.36	
D	9.00	9.60	10.20	
Е	6.10	6.65	7.20	
E 1	7.62	7.94	8.26	
E2	8.30	9.65	11.00	
e	2.540 BSC			
L	3.18			

All Dimensions Are in Millimeters.
 Dimension Does Not Include Mold Protrusions.



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