

AP98T03GP-VB Datasheet

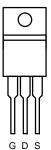
N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	30				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 V$	0.001				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 V$	0.0016				
I _D (A)	260				
Configuration	Single				

FEATURES

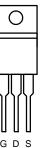
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- Package with Low Thermal Resistance
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC





Top View

TO-220AB



D GC S

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	(T _C = 25 °C, unles	ss otherwise noted))		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V _{DS}	30	N	
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Drain Current	T _C = 25 °C		260		
Continuous Drain Current	T _C = 125 °C	ID	120 ^a		
Continuous Source Current (Diode Conduction) ^a		I _S	120	А	
Pulsed Drain Current ^b		I _{DM}	680		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	82		
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	336	mJ	
Maximum Bower Dissipation	T _C = 25 °C	PD	375	W	
Maximum Power Dissipation ^b	T _C = 125 °C		125	vv	
Operating Junction and Storage Temperature	Range	T _J , T _{stg}	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount ^c	R _{thJA}	40	°C/W
Junction-to-Case (Drain)		R _{thJC}	0.4	0/10

Notes

a. Package limited.

b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

c. When mounted on 1" square PCB (FR-4 material).

d. Parametric verification ongoing.



PARAMETER	SYMBOL	TES	T CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = 250 \mu A$		30	-	-	v
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	= V _{GS} , I _D = 250 μΑ	1.5	2.0	2.5	v
Gate-Source Leakage	I _{GSS}	V _{DS} =	0 V, $V_{GS} = \pm 20 V$	-	-	± 100	nA
		$V_{GS} = 0 V$	V _{DS} = 30 V	-	-	1	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = 30 V, T _J = 125 °C	-	-	50	μA
		$V_{GS} = 0 V$	V _{DS} = 30 V, T _J = 175 °C	-	-	250	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	120	-	-	Α
		$V_{GS} = 10 V$	I _D = 30 A	-	0.001	-	Ω
Drain Course On State Desistence?	P	V _{GS} = 10 V	I _D = 30 A, T _J = 125 °C	-	0.0023	-	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V	I _D = 30 A, T _J = 175 °C	-	0.0028	-	
		$V_{GS} = 4.5 V$	I _D = 20 A	-	0.0016	-	
Forward Transconductance ^b	9 _{fs}	V _{DS} = 15 V, I _D = 30 A		-	190	-	S
Dynamic ^b	·	·					•
Input Capacitance	C _{iss}			-	12484	15605	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$	V _{DS} = 15 V, f = 1 MHz	-	2204	2755	pF
Reverse Transfer Capacitance	C _{rss}	1		-	860	1075	
Total Gate Charge ^c	Qg			-	179	270	nC
Gate-Source Charge ^c	Q _{gs}	V _{GS} = 10 V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 120 \text{ A}$	-	34	-	
Gate-Drain Charge ^c	Q _{gd}	1		-	21	-	
Gate Resistance	R _g	f = 1 MHz		0.59	1.19	1.79	Ω
Turn-On Delay Time ^c	t _{d(on)}			-	18	27	-
Rise Time ^c	t _r	V _{DD} =	$V_{DD} = 15 \text{ V}, \text{ R}_{\text{I}} = 0.3 \Omega$		11	17	
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 50 \text{ A},$	$V_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$	-	64	96	ns
Fall Time ^c	t _f	1		-	11	17	1
Source-Drain Diode Ratings and Char	acteristics ^b	<u> </u>					·
Pulsed Current ^a	I _{SM}			-	-	480	А
Forward Voltage	V _{SD}	IF =	60 A, V _{GS} = 0 V	-	0.81	1.5	V

Notes

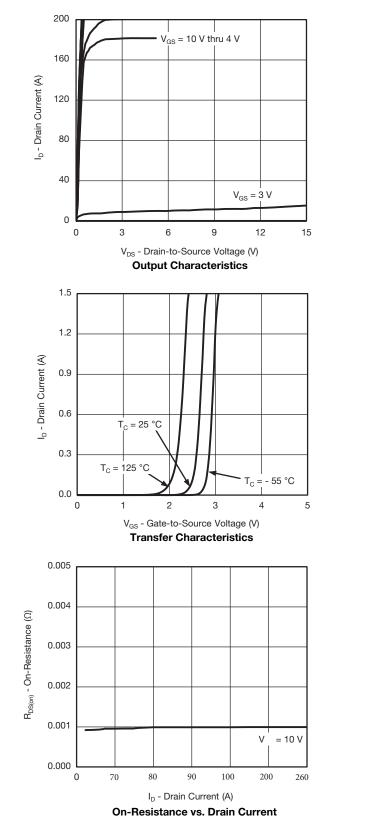
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

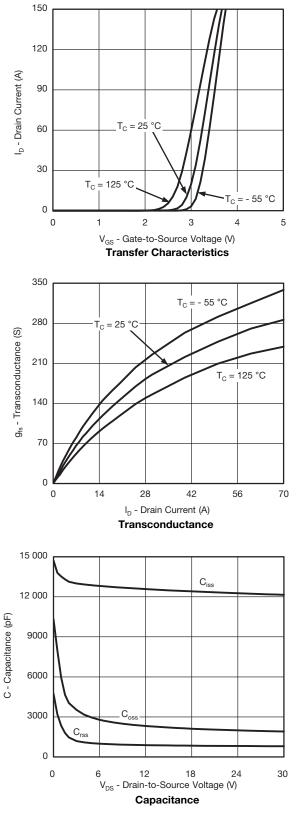
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

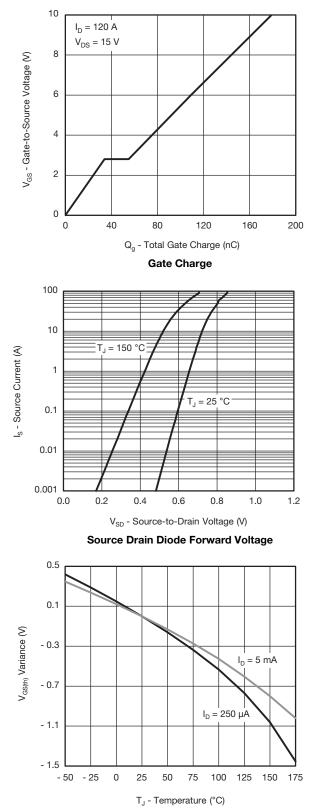




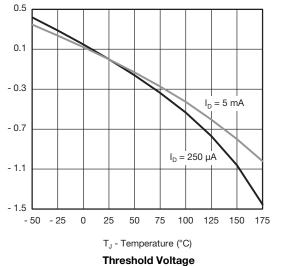
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)

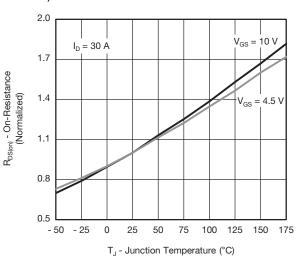




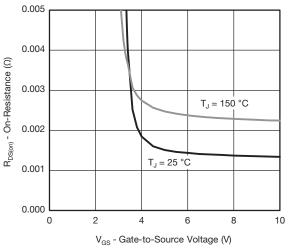


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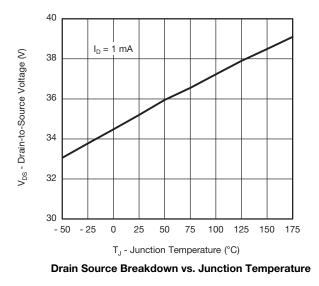




On-Resistance vs. Junction Temperature

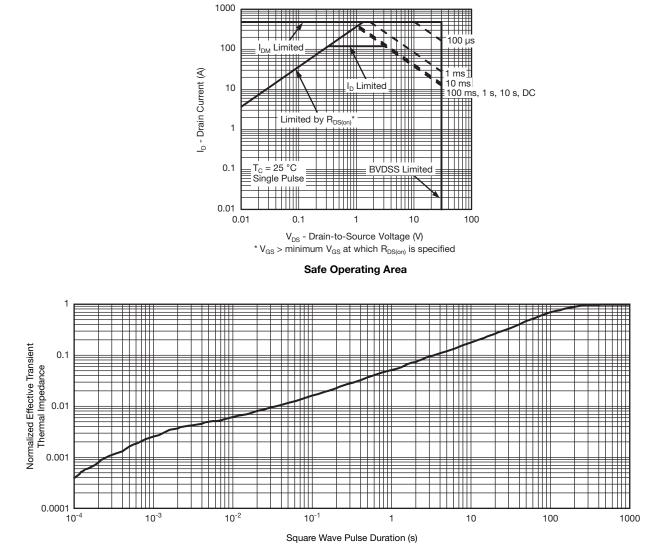


On-Resistance vs. Gate-to-Source Voltage





THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)

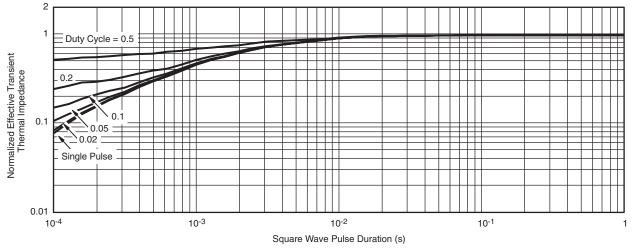


Normalized Thermal Transient Impedance, Junction-to-Ambient

AP98T03GP-VB



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

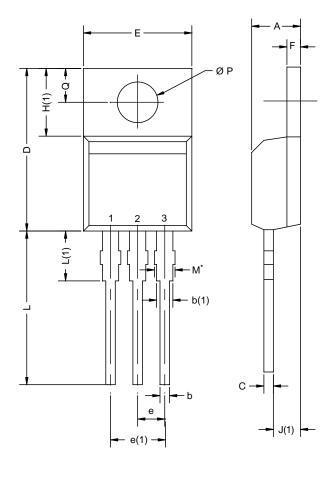
The characteristics shown in the two graphs •

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Case (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



TO-220AB



	MILLIN	IETERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
А	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
Е	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØΡ	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	
ECN: X12- DWG: 547	0208-Rev. N, 1	08-Oct-12			

Notes

* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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