

AP9562GP-HF-VB Datasheet

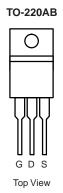
P-Channel 40 V (D-S) MOSFET

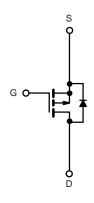
PRODUC	T SUMMARY	
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a
- 40	0.012 at V _{GS} = - 10 V	± 65
- 40	0.014 at V _{GS} = - 4.5 V	± 60

FEATURES

Compliant to RoHS Directive 2002/95/EC







P-Channel MOSFET

ABSOLUTE MAXIMUM RAT	INGS (T _C = 25 °C, unless other	rwise noted)		
Parameter		Symbol	Limit	Unit
Gate-Source Voltage		V _{GS}	± 40	V
Continuous Drain Current (T ₁ = 175 °C)	T _C = 25 °C	1	- 65 ^a	
Continuous Diain Current (19 - 175 O)	T _C = 125 °C	Ι _D	- 62	A
Pulsed Drain Current		I _{DM}	- 60	A
Avalanche Current		I _{AR}	- 60	
Repetitive Avalanche Energy ^b	L = 0.1 mH	E _{AR}	180	mJ
Power Dissipation	T _C = 25 °C (TO-220AB and TO-263)	Р	187 ^d	W
Power Dissipation	T _A = 25 °C (TO-263) ^c	P _D	3.75	vv
Operating Junction and Storage Tempera	ture Range	T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE	RATINGS			
Parameter		Symbol	Limit	Unit
Junction-to-Ambient	PCB Mount (TO-263) ^c	Р	40	
Sunction-to-Ambient	Free Air (TO-220AB)	– R _{thJA}	62.5	°C/W
Junction-to-Case		R _{thJC}	0.8	

Notes:

a. Package limited.

b. Duty cycle \leq 1 %.

c. When mounted on 1" square PCB (FR-4 material).

d. See SOA curve for voltage derating.

* Pb containing terminations are not RoHS compliant, exemptions may apply.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = -250 \mu A$	- 40			V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1.5		- 1.7	v
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
		$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = - 30 V, V_{GS} = 0 V, T_{J} = 125 °C			- 50	μA
		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 \text{ °C}$			- 250	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -10 V$	- 120			А
		V _{GS} = - 10 V, I _D = - 30 A		0.012		
Drain Source On State Peristance ^a	P	V_{GS} = - 10 V, I _D = - 30 A, T _J = 125 °C		0.010		0
ain-Source On-State Resistance ^a	R _{DS(on)}	V_{GS} = - 10 V, I _D = - 30 A, T _J = 175 °C		0.013		Ω
		V _{GS} = - 4.5 V, I _D = - 20 A		0.014		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 75 A	20			S
Dynamic ^b						
Input Capacitance	C _{iss}			9000		
Output Capacitance	C _{oss}	$V_{GS} = 0 V$, $V_{DS} = -25 V$, f = 1 MHz		1565		pF
Reversen Transfer Capacitance	C _{rss}			715		
Total Gate Charge ^c	Qg			160	240	
Gate-Source Charge ^c	Q _{gs}	V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 75 A		32		nC
Gate-Drain Charge ^c	Q _{gd}			30		
Turn-On Delay Time ^c	t _{d(on)}			25	40	
Rise Time ^c	t _r	$V_{DD} = -15 \text{ V}, \text{ R}_{1} = 0.2 \Omega$		225	360	
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong$ - 75 Å, V_{GEN} = - 10 V, R_g = 2.5 Ω		150	240	- ns
Fall Time ^c	t _f	1		210	340	
Source-Drain Diode Ratings and Cha	racteristics ^b	(T _C = 25 °C)		•		
Continuous Current	۱ _S				- 80	٨
Pulsed Current	I _{SM}				- 240	A
Forward Voltage ^a	V _{SD}	I _F = - 75 A, V _{GS} = 0 V		- 1.2	- 1.5	V
Reverse Recovery Time	t _{rr}			55	100	ns
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = - 75 A, dl/dt = 100 A/μs		2.5	5	А
Reverse Recovery Charge	Q _{rr}	1		0.07	0.25	μC

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

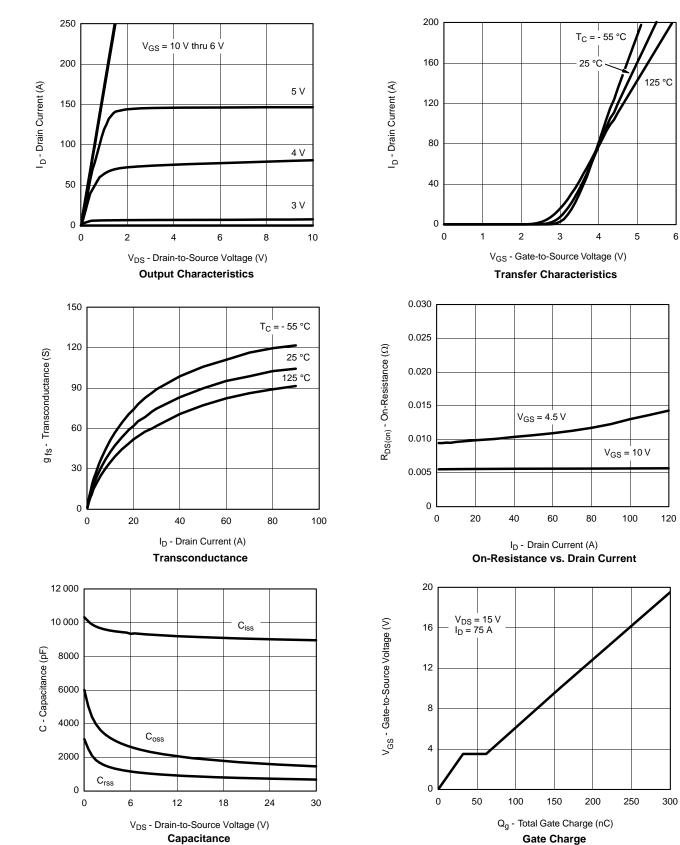
b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



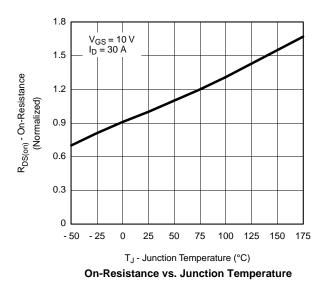
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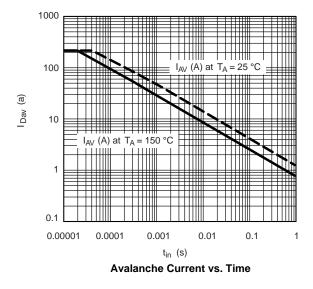


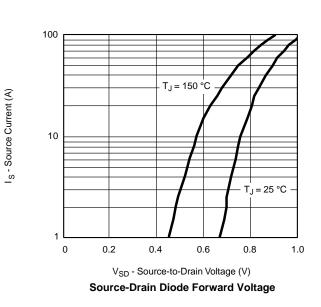
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

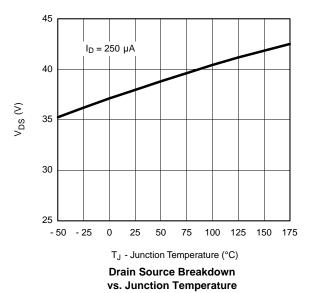


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





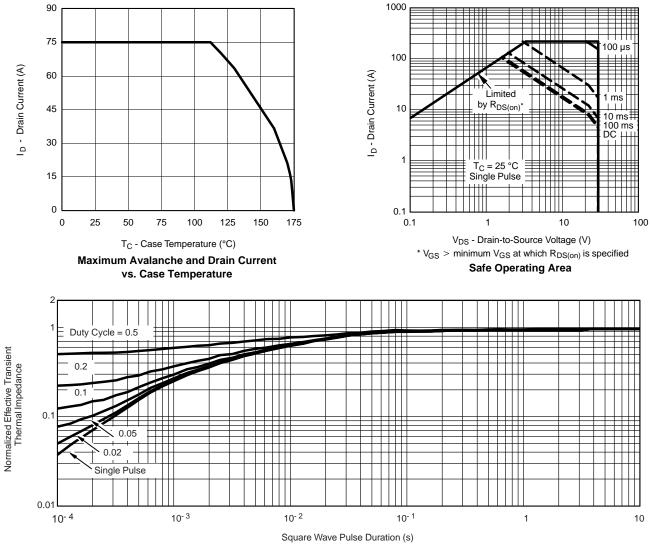




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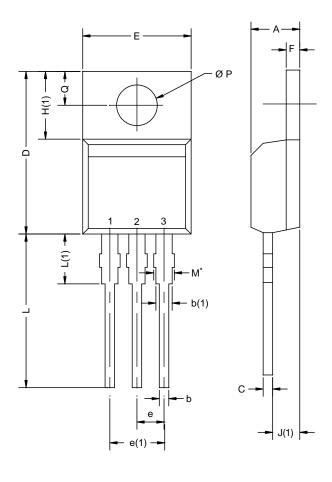
THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case



TO-220AB



MIN. 4.25 0.69 1.20 0.36 14.85 10.04 2.41 4.88	MAX. 4.65 1.01 1.73 0.61 15.49 10.51 2.67	MIN. 0.167 0.027 0.047 0.014 0.585 0.395 0.095	MAX. 0.183 0.040 0.068 0.024 0.610 0.414 0.105
0.69 1.20 0.36 14.85 10.04 2.41	1.01 1.73 0.61 15.49 10.51	0.027 0.047 0.014 0.585 0.395	0.040 0.068 0.024 0.610 0.414
1.20 0.36 14.85 10.04 2.41	1.73 0.61 15.49 10.51	0.047 0.014 0.585 0.395	0.068 0.024 0.610 0.414
0.36 14.85 10.04 2.41	0.61 15.49 10.51	0.014 0.585 0.395	0.024 0.610 0.414
14.85 10.04 2.41	15.49 10.51	0.585 0.395	0.610 0.414
10.04 2.41	10.51	0.395	0.414
2.41			-
	2.67	0.095	0.105
1 00			
4.00	5.28	0.192	0.208
1.14	1.40	0.045	0.055
6.09	6.48	0.240	0.255
2.41	2.92	0.095	0.115
13.35	14.02	0.526	0.552
3.32	3.82	0.131	0.150
3.54	3.94	0.139	0.155
2.60	3.00	0.102	0.118
	6.09 2.41 13.35 3.32 3.54 2.60	6.09 6.48 2.41 2.92 13.35 14.02 3.32 3.82 3.54 3.94	6.096.480.2402.412.920.09513.3514.020.5263.323.820.1313.543.940.1392.603.000.102

Notes

* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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