

AP3405GH-HF-VB Datasheet P-Channel 30-V (D-S) MOSFET

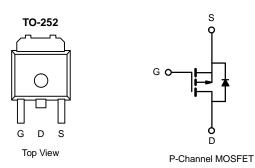
PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^d	Q _g (Typ.)			
- 30	0.033 at V _{GS} = - 10 V	- 38	19 nC			
- 30	0.046 at V_{GS} = - 4.5 V	- 25	13110			

FEATURES

- Halogen-free
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested
- 100 % UIS Tested

APPLICATIONS

- Load Switch
- Notebook Adaptor Switch



Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	- 30	V		
Gate-Source Voltage	V _{GS}	± 20			
	T _C = 25 °C		- 38		
Continuous Drain Current ($T_1 = 150 \ ^{\circ}C$)	T _C = 70 °C	I _D	- 25		
Continuous Drain Current (1) = 150°C)	T _A = 25 °C	'D	- 14.9 ^{a, b}		
	T _A = 70 °C] [- 13.6 ^{a, b}	Α	
Pulsed Drain Current	I _{DM}	- 112	A		
Continuous Source-Drain Diode Current	T _C = 25 °C		- 4.1		
Continuous Source-Drain Diode Current	T _A = 25 °C	Is Is	- 2.2 ^{a, b}		
Avalanche Current	L = 0.1 mH	I _{AS}	- 20		
Single-Pulse Avalanche Energy		E _{AS}	20		
	T _C = 25 °C		25		
Maximum Power Dissipation	T _C = 70 °C	- P _D	20	w	
	T _A = 25 °C		2.7 ^{a, b}	~~~~	
	T _A = 70 °C		1.7 ^{a, b}		
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{a, c}	t ≤ 10 s	R _{thJA}	38	46	°C/W		
Maximum Junction-to-Foot	Steady State	R _{thJF}	20	25	0/10		

Notes:

b. t = 10 s.

c. Maximum under Steady State conditions is 85 $^{\circ}\text{C/W}.$

d. Based on T_C = 25 °C.

a. Surface mounted on 1" x 1" FR4 board.

SPECIFICATIONS $T_J = 25 \text{ °C}$, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			•			•	
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = - 250 μA		- 34		mV/	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	i _D = - 250 μA		5.3		°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1.0		- 2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 25 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55 \text{ °C}$ $V_{DS} \ge -10 \text{ V}, V_{GS} = -10 \text{ V}$	- 30		- 5	A	
	D(OII)	$V_{GS} = -10 V, I_D = -10 A$	00	0.033			
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -8 \text{ A}$		0.035		Ω	
Forward Transconductance ^a	g _{fs}	V _{DS} = - 10 V, I _D = - 10 A		28		S	
Dynamic ^b							
Input Capacitance	C _{iss}			1350		pF	
Output Capacitance	C _{oss}	V _{DS} = - 15 V, V _{GS} = 0 V, f = 1 MHz		255			
Reverse Transfer Capacitance	C _{rss}			190			
Total Gate Charge	Qg	$V_{DS} = -15$ V, $V_{GS} = -10$ V, $I_{D} = -10$ A		27	43	nC	
				19	25		
Gate-Source Charge	Q _{gs}	V_{DS} = - 15 V, V_{GS} = - 4.5 V, I_{D} = - 10 A		6			
Gate-Drain Charge	Q _{gd}			12			
Gate Resistance	R _g	f = 1 MHz	0.5	2.2	4.4	Ω	
Turn-On Delay Time	t _{d(on)}			13	25		
Rise Time	t _r	V_{DD} = - 15 V, R_L = 1.5 Ω		12	24		
Turn-Off DelayTime	t _{d(off)}	${\rm I}_{\rm D}\cong$ - 10 A, ${\rm V}_{\rm GEN}$ = - 10 V, ${\rm R}_{\rm g}$ = 1 Ω		40	70		
Fall Time	t _f			9	18		
Turn-On Delay Time	t _{d(on)}			48	80	ns	
Rise Time	t _r	V_{DD} = - 15 V, R_L = 1.5 Ω		92	160	-	
Turn-Off DelayTime	t _{d(off)}	$I_{D}\cong$ - 10 A, V_{GEN} = - 4.5 V, R_{g} = 1 Ω		34	60		
Fall Time	t _f			19	35		
Drain-Source Body Diode Characteris	stics						
Continous Source-Drain Diode Current	۱ _S	T _C = 25 °C			- 4.1	^	
Pulse Diode Forward Current	I _{SM}				- 40	- A	
Body Diode Voltage	V _{SD}	I _S = - 3 A, V _{GS} = 0 V		- 0.75	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			27	45	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	L = 10 A dl/dt = 100 A/up T 25 °C		16	27	nC	
Reverse Recovery Fall Time	t _a	I _F = - 10 A, dl/dt = 100 A/μs, T _J = 25 °C		12		ns	
Reverse Recovery Rise Time	t _b			15			

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

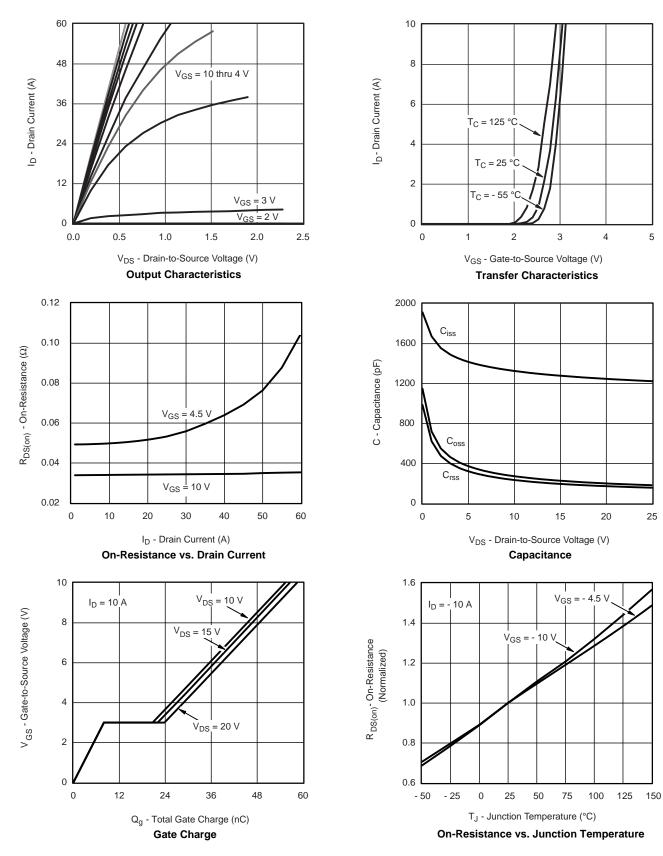
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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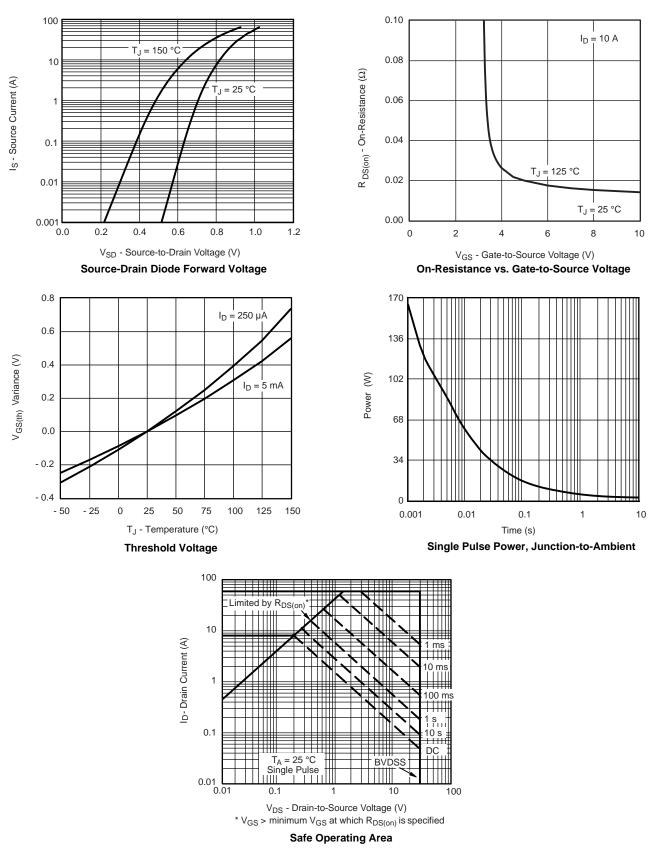


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



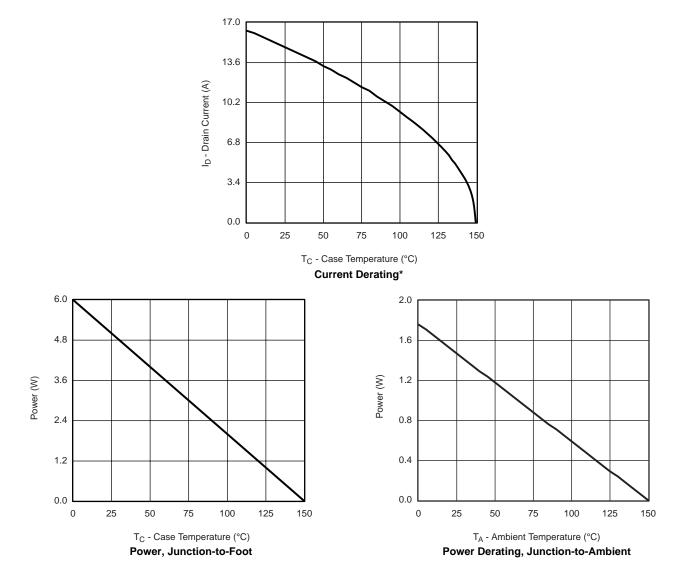


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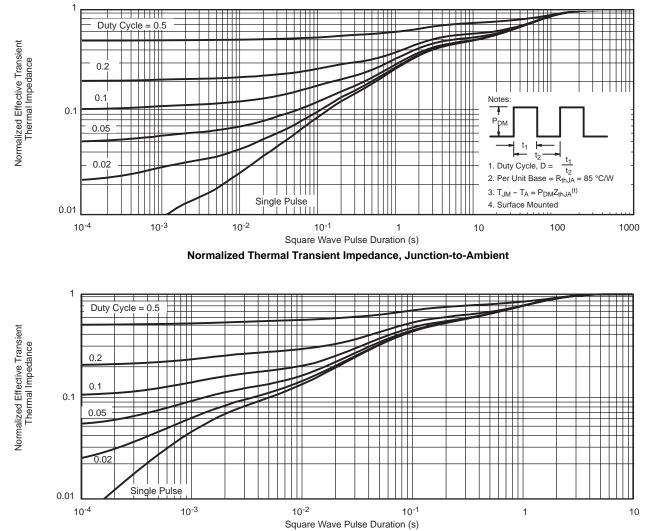
MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



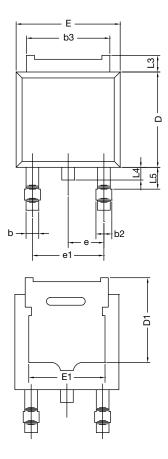
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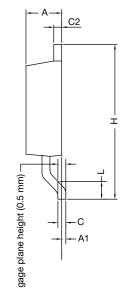


Normalized Thermal Transient Impedance, Junction-to-Foot



TO-252AA CASE OUTLINE





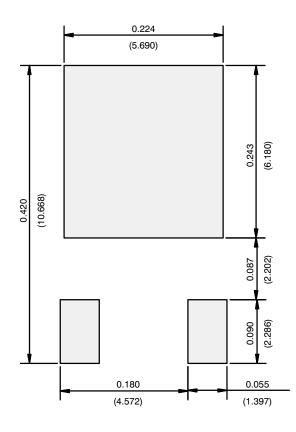
	MILLIN	IETERS	INC	HES	
DIM.	MIN.	MAX.	MIN.	MAX.	
А	2.18	2.38	0.086	0.094	
A1	-	0.127	-	0.005	
b	0.64	0.88	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	
С	0.46	0.61	0.018	0.024	
C2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	
D1	5.21	-	0.205	-	
E	6.35	6.73	0.250	0.265	
E1	4.32	-	0.170	-	
Н	9.40	10.41	0.370	0.410	
е	2.28	BSC	0.090 BSC		
e1	4.56	BSC	0.180 BSC		
L	1.40	1.78	0.055	0.070	
L3	0.89	1.27	0.035	0.050	
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	
ECN: X12-0247-Rev. M, 24-Dec-12 DWG: 5347					

Note

• Dimension L3 is for reference only.



RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)



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