

Features

- Input Voltage up to 36V
- output current up to 100mA
- output Voltage 3.3V
- 8 μ A Current at no Load
- $\pm 2\%$ Output Accuracy
- Compact package: SOT-223

Applications

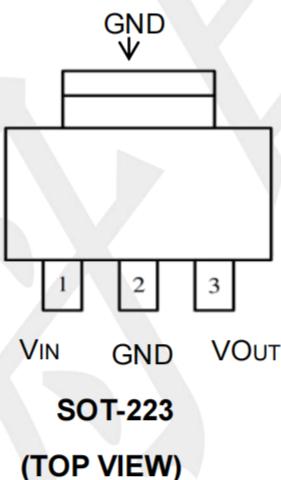
- Hand-Held Instruments
- Battery Powered Consumer Products
- Test and Measurement Equipment
- Industrial Power Supplies

Description

The is micropower low dropout voltage regulators available in a wide variety of output voltages. These devices feature a very low quiescent current and thermal limiting protection are provided by the presence of a short circuit at the output and an internal thermal shutdown circuit.

Due to the low input-to-output voltage differential and bias current specifications, these devices are ideally suited for battery powered computer, consumer, and industrial equipment where an extension of useful battery life is desirable.

PIN CONFIGURATION



Pin Number	Pin Name	Pin Function
1	VIN	Input of Supply Voltage
2	GND	Ground
3	VOUT	Output of the Regulator

Absolute Maximum Ratings

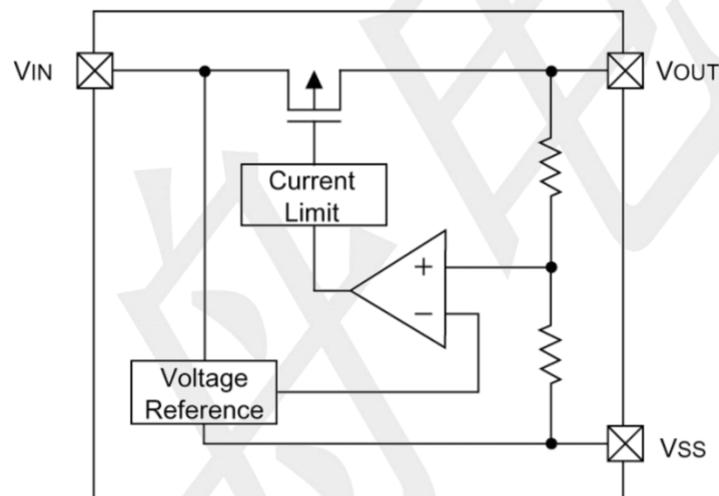
over operating free-air temperature range (unless otherwise noted)

SYMBOL	PARAMETER	RATINGS	UNIT
V _{IN}	Continuous input voltage range	-0.3 ~ +40	V
Current	Maximum output current	Internally limited	mA
T _J	Operating Junction Temperature Range	-40 ~ +85	°C
T _{stg}	Storage temperature range	-55~150	°C

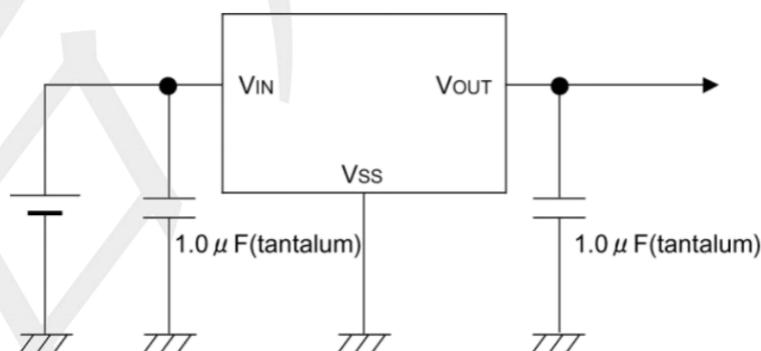
THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ _{JA}	245	°C/W
Junction to Case	θ _{JC}	15	°C/W

BLOCK DIAGRAM



Typical Application Circuit



Electrical Characteristics

(TA=25°C, unless otherwise specified)

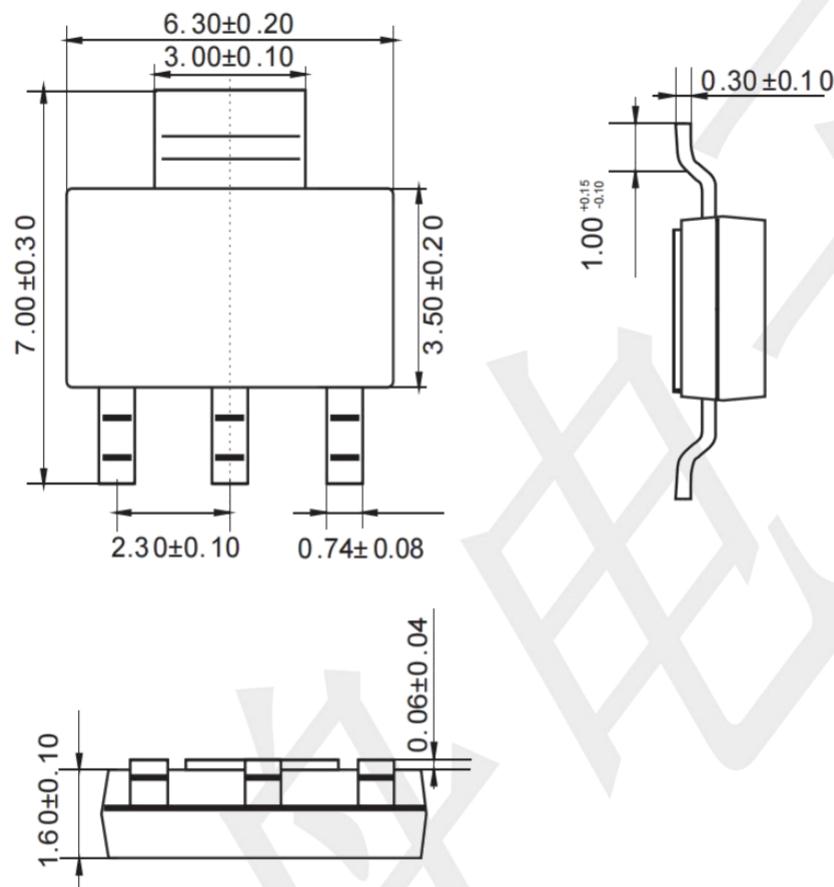
PARAMETER	SYMBOL	TEST Conditions	MIN	TYP	MAX	UNIT
Supply Voltage	VIN	I _{OUT} = 100mA	3.0	--	36	V
Output current	I _{OUT}		0	--	100	mA
DC Output Voltage Accuracy		I _{OUT} = 0.1mA	-2	--	+2	%
Dropout Voltage (VIN-V _{OUT})	I _{OUT} = 100mA	V _{OUT} = 3.3V	--	600	--	mV
Ground Current (I _{OUT} = 0mA)	I _Q		--	1.5	8	uA
Line Regulation	ΔLINE	I _{OUT} = 1mA, 10 ≤ VIN ≤ 18V	--	0.3	--	%
Load Regulation	ΔLOAD	10mA ≤ I _{OUT} ≤ 100mA	--	0.3	--	
Output Current Limit	I _{LIM}	V _{OUT} = 0.9 × V _{OUT(NOM)}	200	--	--	mA
Power Supply Rejection Ratio	PSRR	V _{OUT} = 5V, I _{OUT} = 30mA, VIN = 12V, f = 1kHz	--	70	--	dB
Thermal Shutdown Temperature	T _{SD}	I _{OUT} = 10mA	--	160	--	°C
Thermal Shutdown Hysteresis	ΔT _{SD}		--	15	--	

Note:

1. Test condition: the device is mounted on FR-4 substrate PC board, with minimum recommended pad layout.
2. Vdif : The Difference Of Output Voltage And Input Voltage When Input Voltage Is Decreased Gradually Till Output Voltage Equals To 98% Of V_{OUT} .

Package Outline Dimensions (unit: mm)

SOT-223



Mounting Pad Layout (unit: mm)

