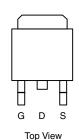


COMPLIANT

## AOB256L-VB Datasheet N-Channel 200 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)		
200	0.048 at V <sub>GS</sub> = 10 V	40		
	0.060 at V <sub>GS</sub> = 6.5 V	35		



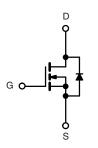
#### TO-263

#### FEATURES

- TrenchFET<sup>®</sup> Power MOSFET
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- PWM Optimized for Fast Switching
- Compliant to RoHS Directive 2002/95/EC

#### **APPLICATIONS**

- Isolated DC/DC Converters
  Primany Side Switch
  - Primary-Side Switch



N-Channel MOSFET

ABSOLUTE MAXIMUM RATIN	<b>GS</b> (T <sub>C</sub> = 25 °C, unless c	otherwise noted)			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V <sub>DS</sub>	200	V	
Gate-Source Voltage	V <sub>GS</sub>	± 20	V		
Continuous Drain Current ( $T_1 = 175 \text{ °C}$ )	T <sub>C</sub> = 25 °C	1-	40		
	T <sub>C</sub> = 125 °C	I <sub>D</sub>	25		
Pulsed Drain Current	I <sub>DM</sub>	80	- A		
Avalanche Current	I <sub>AR</sub>	20			
Repetitive Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AR</sub>	16.2	mJ	
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 25 °C	P	200 <sup>b</sup>	w	
	$T_A = 25 \ ^{\circ}C^{c}$		4.5		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Limit	Unit	
Junction-to-Ambient	PCB Mount (TO-263) <sup>c</sup>	R <sub>thJA</sub>	40	°C/W	
Junction-to-Case (Drain)		R <sub>thJC</sub>	1	0/10	

Notes:

a. Duty cycle  $\leq$  1 %.

b. See SOA curve for voltage derating.

c. When mounted on 1" square PCB (FR-4 material).

<b>SPECIFICATIONS</b> ( $T_J = 25$	°C, unless o	otherwise noted)				
Parameter	Symbol	Test Conditions	Min .	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = 250 \mu A$	200			v
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	2		4	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V$ , $V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current		$V_{DS} = 160 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA
	I <sub>DSS</sub>	$V_{DS}$ = 160 V, $V_{GS}$ = 0 V, $T_{J}$ = 125 °C			50	
		$V_{DS} = 160 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$			250	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 15 \text{ V}, V_{GS} = 10 \text{ V}$	60			А
Drain-Source On-State Resistance <sup>a</sup>		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A		0.048		
	P	$V_{GS}$ = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C		0.150		Ω
	R <sub>DS(on)</sub>	$V_{GS}$ = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 175 °C		0.180		
Drain-Source on State Resistance		V <sub>GS</sub> = 6.5 V, I <sub>D</sub> = 15 A		0.060		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A	15			S
Dynamic <sup>b</sup>	-	•	•	•		
Input Capacitance	C <sub>iss</sub>			2820		pF
Output Capacitance	C <sub>oss</sub>	$V_{GS}$ = 0 V, $V_{DS}$ = 25 V, f = 1 MHz		300		
Reverse Transfer Capacitance	C <sub>rss</sub>			120		
Total Gate Charge <sup>c</sup>	Qg			35		nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 20 \text{ A}$		11		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			14		
Gate Resistance	R <sub>G</sub>			2		Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			15	25	ns
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = 100 V, R <sub>L</sub> = 5 $\Omega$ I <sub>D</sub> $\cong$ 20 A, V <sub>GEN</sub> = 10 V, R <sub>G</sub> = 2.5 $\Omega$		35	55	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			40	60	
Fall Time <sup>c</sup>	t <sub>f</sub>			30	45	
Source-Drain Diode Ratings and Cha	aracteristics (	T <sub>C</sub> = 25 °C) <sup>b</sup>		•		
Continuous Current	۱ <sub>S</sub>				40	^
Pulsed Current	I <sub>SM</sub>				60	A
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 20 A, V <sub>GS</sub> = 0 V		1	1.5	V
Reverse Recovery Time	t <sub>rr</sub>			115	170	ns
Peak Reverse Recovery Charge	I <sub>RM(REC)</sub>	l <sub>F</sub> = 50 A, dl/dt = 100 A/μs		7.5	12	Α
Reverse Recovery Charge	Q <sub>rr</sub>			0.43	1.02	μC

Notes:

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

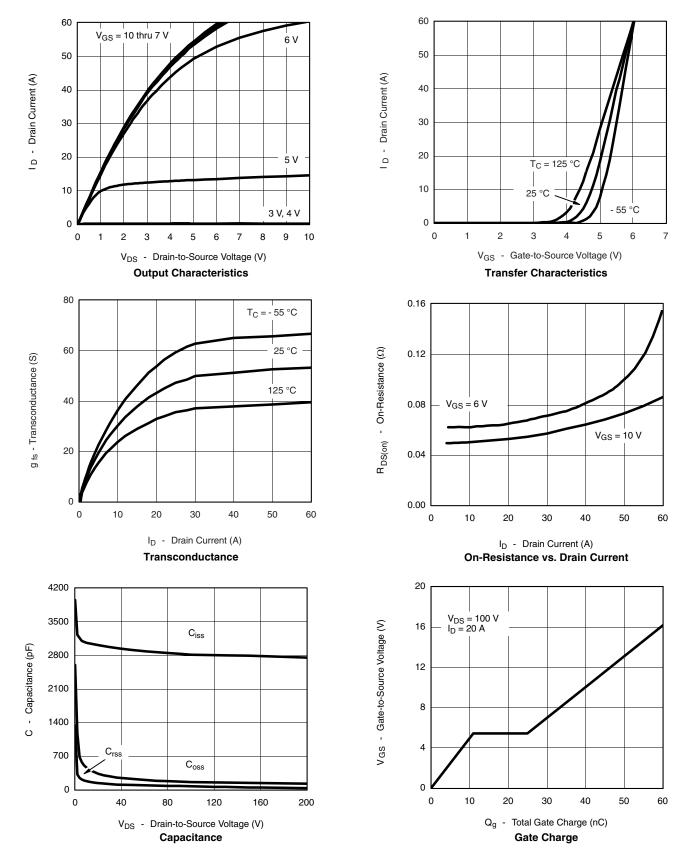
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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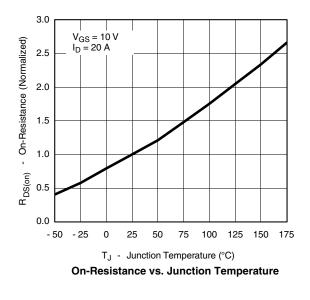


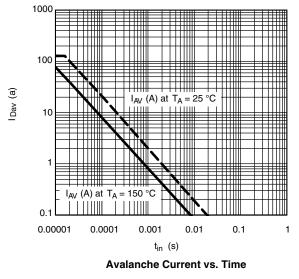


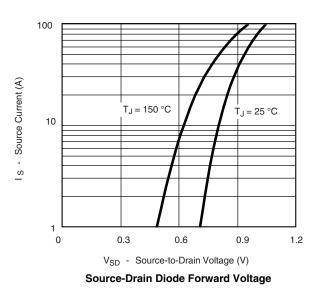


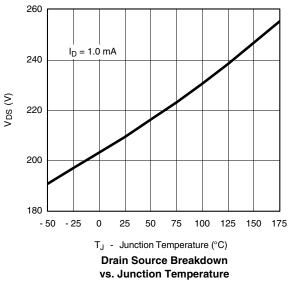


#### TYPICAL CHARACTERISTICS (25 °C unless noted)





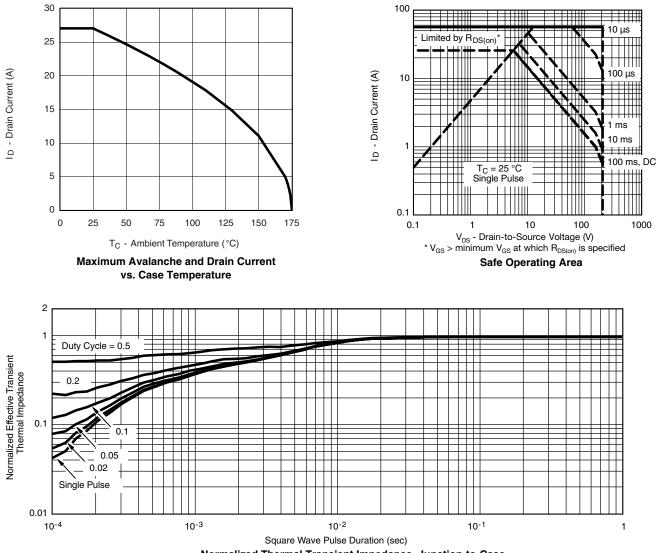




### AOB256L-VB



#### THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case



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