

AM7431P-T1-PF-VB Datasheet

P-Channel 30 V (D-S) MOSFET

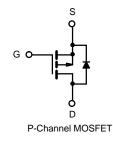
| PRODUCT SUMMARY | | | | | |
|---------------------|-------------------------------------|-----------------------------|-----------------------|--|--|
| V _{DS} (V) | R _{DS(on)} (Ω) | I _D ^a | Q _g (Typ.) | | |
| | 0.0080 at V _{GS} = - 10 V | - 60 | | | |
| - 30 | 0.0090 at V _{GS} = - 6 V | - 53 | 66 nC | | |
| | 0.0120 at V _{GS} = - 4.5 V | - 50 | | | |

FEATURES

- Extended V_{GS} range (± 25 V) for adaptor switch applications
- Extremely low R_{DS(on)}
- TrenchFET[®] Power MOSFET
- 100 % R_g and UIS Tested



DFN8(5*6)



| Parameter | Symbol | Limit | Unit | | |
|--|------------------------|-----------------------------------|------------------------|----|--|
| Drain-Source Voltage | | V _{DS} | - 30 | V | |
| Gate-Source Voltage | | V _{GS} | ± 20 | v | |
| | T _C = 25 °C | | - 60 | | |
| Continuous Drain Current ($T_1 = 150 \ ^{\circ}C$) | T _C = 70 °C | | - 50.7 | | |
| continuous Drain Current (1) = 130°C) | T _A = 25 °C | I _D | - 47.3 | | |
| | T _A = 70 °C | 1 | - 43.9 ^{b, c} | Α | |
| Pulsed Drain Current (t = 300 µs) | | I _{DM} | - 150 | ^ | |
| Continuous Source-Drain Diode Current | T _C = 25 °C | la la | - 58 ^{b, c} | | |
| Continuous Source-Drain Diode Current | T _A = 25 °C | I _S | - 46 ^{b, c} | | |
| Single Pulse Avalanche Current | | I _{AS} | - 40 | | |
| Single Pulse Avalanche Energy L = 0.1 mH | | E _{AS} | 80 | mJ | |
| | T _C = 25 °C | | 75 | | |
| Maximum Dowar Discinction | T _C = 70 °C | P _D | 40 | W | |
| Maximum Power Dissipation | T _A = 25 °C | | 3.1 ^{b, c°} | VV | |
| | T _A = 70 °C | 1 | 2 ^{b, c} | | |
| Operating Junction and Storage Temperature Range | | T _J , T _{stq} | - 55 to 150 | °C | |

| THERMAL RESISTANCE RATI | NGS | | | | |
|---|--------------|-------------------|---------|---------|------|
| Parameter | | Symbol | Typical | Maximum | Unit |
| Maximum Junction-to-Ambient ^{b, d} | t ≤ 10 s | R _{thJA} | 33 | 40 | °C/W |
| Maximum Junction-to-Foot (Drain) | Steady State | R _{thJF} | 15 | 17 | 0/11 |

Notes:

a. Based on $T_C = 25 \ ^{\circ}C$.

b. Surface mounted on 1" x 1" FR4 board.

c. t = 10 s.

d. Maximum under steady state conditions is 90 $^{\circ}\text{C/W}.$

AM7431P-T1-PF-VB

| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit |
|---|-------------------------|---|-------|--------|-------|---|
| Static | | | | 1 71 | | |
| Drain-Source Breakdown Voltage | V _{DS} | V _{GS} = 0 V, I _D = - 250 μA | - 30 | | | V |
| V _{DS} Temperature Coefficient | $\Delta V_{DS}/T_J$ | | | - 24 | | |
| V _{GS(th)} Temperature Coefficient | $\Delta V_{GS(th)}/T_J$ | I _D = - 250 μΑ | | 6 | | mV/°C |
| Gate-Source Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$ | - 1.0 | | - 2.5 | V |
| 5 | | V _{DS} = 0 V, V _{GS} = ± 25 V | | | ± 150 | |
| Gate-Source Leakage | I _{GSS} | $V_{DS} = 0 V, V_{GS} = \pm 20 V$ | | | ± 15 | |
| | | V _{DS} = - 30 V, V _{GS} = 0 V | | | - 1 | μA |
| Zero Gate Voltage Drain Current | IDSS | V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 55 °C | | | - 10 | mV/°C V μA Ω S pF nC Ω |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} \le -5 \text{ V}, \text{ V}_{GS} = -10 \text{ V}$ | - 20 | | | Α |
| | | V _{GS} = - 10 V, I _D = - 13 A | | 0.0080 | | |
| Drain-Source On-State Resistance ^a | R _{DS(on)} | V _{GS} = - 6 V, I _D = - 10 A | | 0.0090 | | |
| | - (-) | V _{GS} = - 4.5 V, I _D = - 8 A | | 0.0120 | | |
| Forward Transconductance ^a | 9 _{fs} | V _{DS} = - 15 V, I _D = - 13 A | | 44 | | S |
| Dynamic ^b | | | | | | |
| Input Capacitance | C _{iss} | | | 4620 | | |
| Output Capacitance | C _{oss} | V_{DS} = - 15 V, V_{GS} = 0 V, f = 1 MHz | | 880 | | pF |
| Reverse Transfer Capacitance | C _{rss} | | | 820 | | |
| Total Cata Charge | | V_{DS} = - 15 V, V_{GS} = - 10 V, I_{D} = - 17.3 A | | 102 | 153 | |
| Total Gate Charge | Qg | | | 66 | 80 | |
| Gate-Source Charge | Q _{gs} | V_{DS} = - 15 V, V_{GS} = - 5 V, I_{D} = - 17.3 A | | 16 | | |
| Gate-Drain Charge | Q _{gd} | | | 28 | | |
| Gate Resistance | Rg | f = 1 MHz | 0.3 | 1.3 | 2.6 | Ω |
| Turn-On Delay Time | t _{d(on)} | | | 70 | 105 | |
| Rise Time | t _r | V_{DD} = 0 V, R_L = 1.5 Ω | | 70 | 105 | |
| Turn-Off Delay Time | t _{d(off)} | $I_{D}\cong$ - 10 A, V_{GEN} = - 4.5 V, R_{g} = 1 Ω | | 45 | 68 | |
| Fall Time | t _f | | | 27 | 41 | nc |
| Turn-On Delay Time | t _{d(on)} | | | 18 | 30 | 115 |
| Rise Time | t _r | V_{DD} = - 15 V, R_L = 1.5 Ω | | 15 | 25 | - |
| Turn-Off Delay Time | t _{d(off)} | $\text{I}_\text{D}\cong$ - 10 A, V_GEN = - 10 V, R_g = 1 Ω | | 52 | 80 | |
| Fall Time | t _f | | | 14 | 25 | |
| Drain-Source Body Diode Characteristic | s | | | _ | | |
| Continuous Source-Drain Diode Current | ا _S | T _C = 25 °C | | | - 5.8 | A |
| Pulse Diode Forward Current | I _{SM} | | | | - 60 | , , , , , , , , , , , , , , , , , , , |
| Body Diode Voltage | V_{SD} | I _S = - 10 A, V _{GS} = 0 V | | - 0.78 | - 1.2 | V |
| Body Diode Reverse Recovery Time | t _{rr} | | | 35 | 53 | ns |
| Body Diode Reverse Recovery Charge | Q _{rr} | I _F = - 10 A, dl/dt = 100 A/μs, T _J = 25 °C | | 25 | 38 | nC |
| Reverse Recovery Fall Time | t _a | | | 19 | | ns |
| Reverse Recovery Rise Time | t _b | | | 16 | | 110 |

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %

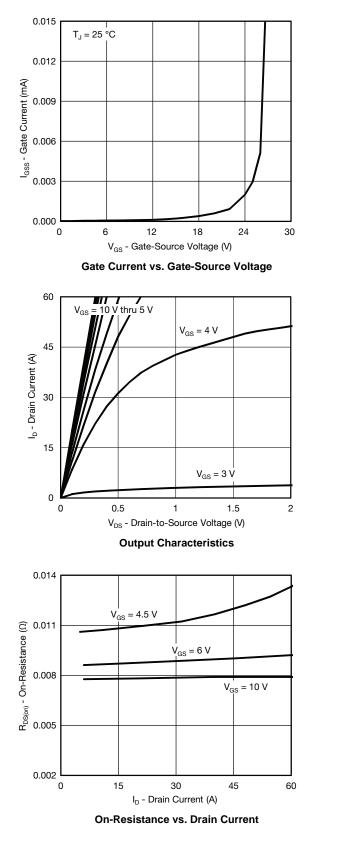
b. Guaranteed by design, not subject to production testing.

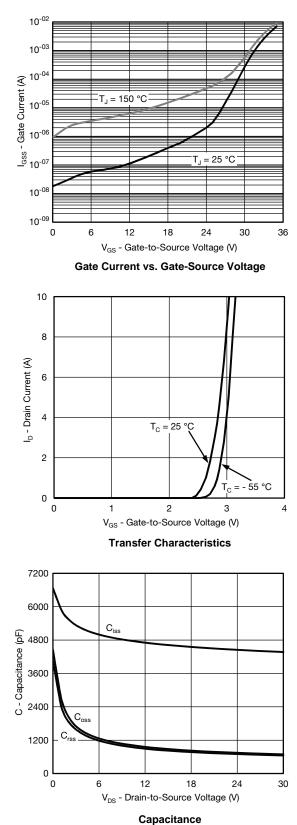
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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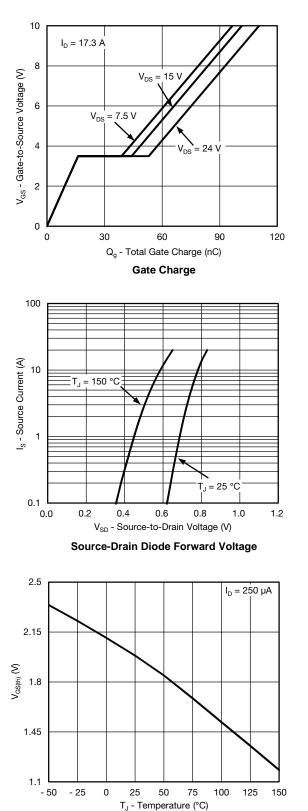


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



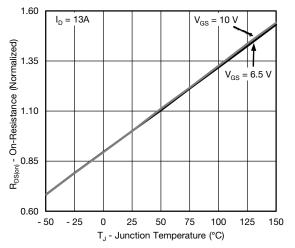




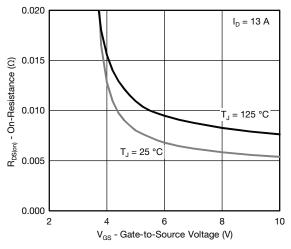


Threshold Voltage

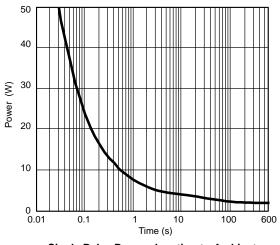
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



On-Resistance vs. Junction Temperature



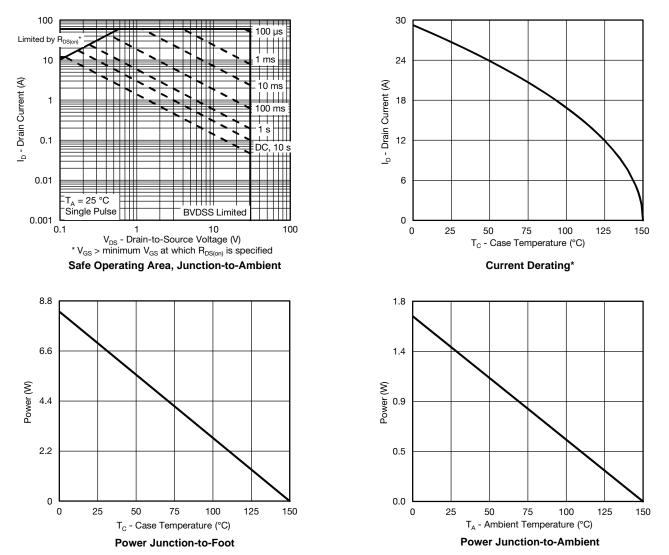
On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient





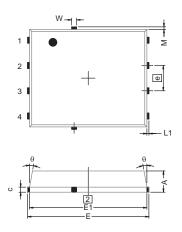


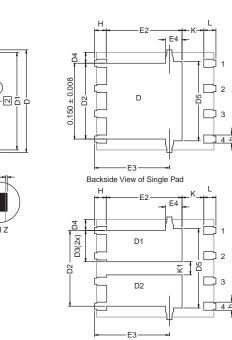
* The power dissipation P_D is based on $T_{J(max.)} = 150 \text{ °C}$, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

AM7431P-T1-PF-VB



PowerPAK SO-8, (SINGLE/DUAL)





Notes

1. Inch will govern.

2 Dimensions exclusive of mold gate burrs.

3. Dimensions exclusive of mold flash and cutting burrs.

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A1-

Detail Z

Backside View of Dual Pad

| MILLIMETERS | | | INCHES | | | | |
|-------------|------------|-----------|--------|------------|-------------|-------|--|
| DIM. | MIN. | NOM. | MAX. | MIN. | NOM. | MAX. | |
| A | 0.97 | 1.04 | 1.12 | 0.038 | 0.041 | 0.044 | |
| A1 | 0.00 | - | 0.05 | 0.000 | - | 0.002 | |
| b | 0.33 | 0.41 | 0.51 | 0.013 | 0.016 | 0.020 | |
| С | 0.23 | 0.28 | 0.33 | 0.009 | 0.011 | 0.013 | |
| D | 5.05 | 5.15 | 5.26 | 0.199 | 0.203 | 0.207 | |
| D1 | 4.80 | 4.90 | 5.00 | 0.189 | 0.193 | 0.197 | |
| D2 | 3.56 | 3.76 | 3.91 | 0.140 | 0.148 | 0.154 | |
| D3 | 1.32 | 1.50 | 1.68 | 0.052 | 0.059 | 0.066 | |
| D4 | | 0.57 TYP. | | | 0.0225 TYP. | | |
| D5 | | 3.98 TYP. | | | 0.157 TYP. | | |
| E | 6.05 | 6.15 | 6.25 | 0.238 | 0.242 | 0.246 | |
| E1 | 5.79 | 5.89 | 5.99 | 0.228 | 0.232 | 0.236 | |
| E2 | 3.48 | 3.66 | 3.84 | 0.137 | 0.144 | 0.151 | |
| E3 | 3.68 | 3.78 | 3.91 | 0.145 | 0.149 | 0.154 | |
| E4 | | 0.75 TYP. | | 0.030 TYP. | | | |
| е | 1.27 BSC | | | 0.050 BSC | | | |
| К | | 1.27 TYP. | | 0.050 TYP. | | | |
| K1 | 0.56 | - | - | 0.022 | - | - | |
| Н | 0.51 | 0.61 | 0.71 | 0.020 | 0.024 | 0.028 | |
| L | 0.51 | 0.61 | 0.71 | 0.020 | 0.024 | 0.028 | |
| L1 | 0.06 | 0.13 | 0.20 | 0.002 | 0.005 | 0.008 | |
| θ | 0° | - | 12° | 0° | - | 12° | |
| W | 0.15 | 0.25 | 0.36 | 0.006 | 0.010 | 0.014 | |
| М | 0.125 TYP. | | | 0.005 TYP. | | | |



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