

N-Channel MOSFET

General Description

The WSD3066DN33 is the highest performance trench N-Channel MOSFETs with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications.

The WSD3066DN33 meet the RoHS and Green Product requirement, 100% E_{AS} guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% E_{AS} Guaranteed
- Green Device Available

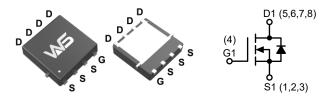
Product Summery

BV _{DSS}	R _{DS(ON)}	l _D	
30V	4.7mΩ	66A	

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

DFN3x3-8L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V _{GS} Gate-Source Voltage		±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	66	
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	40	
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 10V ¹ 15		Α
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 10V ¹	10	
I _{DM} @T _C =25°C	Pulsed Drain Current ²	150	
E _{AS}	Avalanche Energy, Single Pulse (L=0.1mH) ³	125	mJ
I _{AS}	Avalanche Current, Single pulse(L=0.1mH) ³	50	Α
P _D @T _C =25°C	Total Power Dissipation ⁴	45	1 0/
P _D @T _A =25°C Total Power Dissipation ⁴		1.78	W
T _{STG} Storage Temperature Range		-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	C

Thermal Data

Symbol	Parameter	Тур.	Max.	Units
$R_{ heta JA}$	Thermal Resistance, Junction-to-Ambient ¹		70	°C/W
$R_{ heta JC}$	Thermal Resistance, Junction-to-Case ¹	n-to-Case ¹ 2.7		C/VV

N-Channel MOSFET

Electrical Characteristics (T_J=25°C, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250μA	30			V
$\Delta BV_{DSS}/\Delta T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =1mA		0.028		V/°C
В	Static Projectory Co. Projectory 2	V _{GS} =10V , I _D =40A		4.7	5.7	m0
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =4.5V , I _D =20A		5.8	7.6	mΩ
V _{GS(th)}	Gate Threshold Voltage	\\ -\\ -250\	1.5	1.8	2.5	V
$\Delta V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{\rm GS}=V_{\rm DS}$, $I_{\rm D}=250\mu{\rm A}$		-6.06		mV/°C
	Dunin Course Leakens Courset	V _{DS} =24V , V _{GS} =0V , T _J =25°C			1.0	
I _{DSS}	Drain-Source Leakage Current	V _{DS} =24V , V _{GS} =0V , T _J =55°C			30	μA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V , I _D =20A		44		S
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f = 1.0MHz		1.0	1.1	Ω
Qg	Total Gate Charge (4.5V)			27.5	38.5	
Q_gs	Gate-Source Charge	V_{DS} =15V , V_{GS} =4.5V , I_{D} =20A		9.6	13.4	nC
Q_gd	Gate-Drain Charge			9.4	13.7	
T _{d(on)}	Turn-On Delay Time			18.5	34	
T _r	Rise Time	V _{DD} =15V , V _{GEN} =10V ,		11.3	21	
T _{d(off)}	Turn-Off Delay Time	$R_G=6\Omega$, $I_D=1A$, $R_L=15\Omega$		62.5	114	ns
T _f	Fall Time			23.5	43	
C _{iss}	Input Capacitance			1320	1420	
C _{oss}	Output Capacitance	V _{DS} =15V , V _{GS} =0V , f = 1.0MHz		610	640	pF
C _{rss}	Reverse Transfer Capacitance			112	150	

Guaranteed Avalanche Characteristics

	Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
ı	E _{AS}	Single Pulse Avalanche Energy ⁵	V _{DD} =25V , L=0.5mH , I _{AS} =20A		125		mJ

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
I _S	Continuous Source Current 1,6	V =V =0V Force Current			15	Α
I _{SM}	Pulsed Source Curren ^{2,6}	V _G =V _D =0V , Force Current			45	Α
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25°C			1.0	V
t _{rr}	Reverse Recovery Time	L =40.0 dl/dt=400.0 /u.o T =25°C		23		ns
Q_{rr}	Reverse Recovery Charge	l _F =40A, dl/dt=100A/μs,T _J =25°C		7		nC

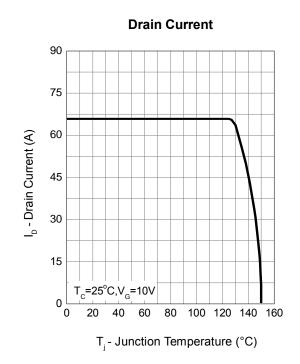
Note:

- 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, t<10sec.
- 2. The data tested by pulsed , pulse width $\leq 300 us$, duty cycle $\leq 2\%$
- 3. The E $_{\rm AS}$ data shows Max. rating . The test condition is $\rm\,V_{DD}$ =25V, $\rm\,V_{GS}$ =10V, L=0.5mH, I $_{\rm AS}$ =20A
- 4. The power dissipation is limited by 150 $^{\circ}$ C junction temperature.
- 5. The Min. value is 100% $\,{\rm E}_{\rm AS}\,$ tested guarantee.
- 6. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



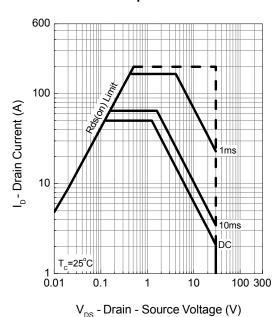
Typical Characteristics

Power Dissipation 70 60 50 40 30 10 T_c=25°C 0 20 40 60 80 100 120 140 160

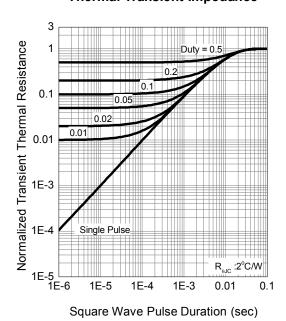


Safe Operation Area

T_i - Junction Temperature (°C)



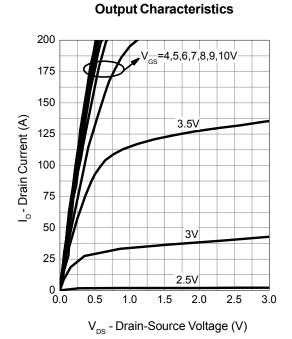
Thermal Transient Impedance



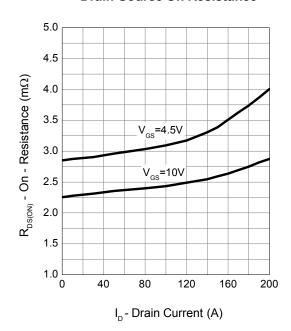


Typical Characteristics (Cont.)

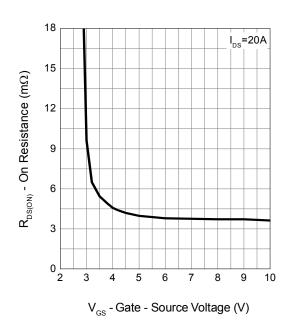
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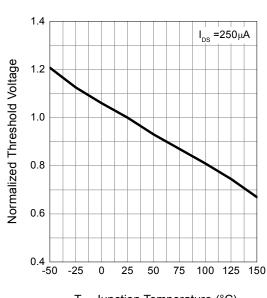
Drain-Source On Resistance



Gate-Source On Resistance



Gate Threshold Voltage

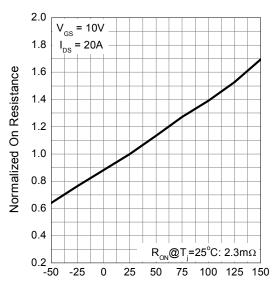


T_i - Junction Temperature (°C)



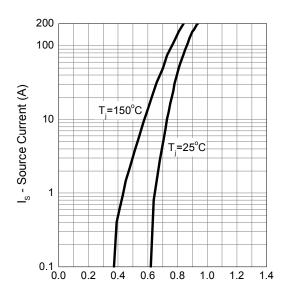
Typical Characteristics (Cont.)

Drain-Source On Resistance



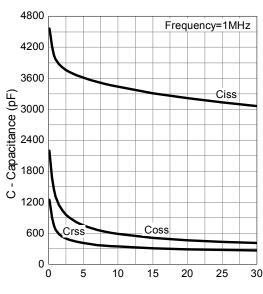
T_i - Junction Temperature (°C)

Source-Drain Diode Forward



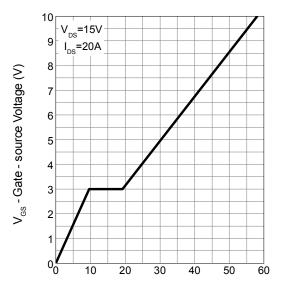
 V_{SD} - Source - Drain Voltage (V)

Capacitance



V_{DS} - Drain - Source Voltage (V)

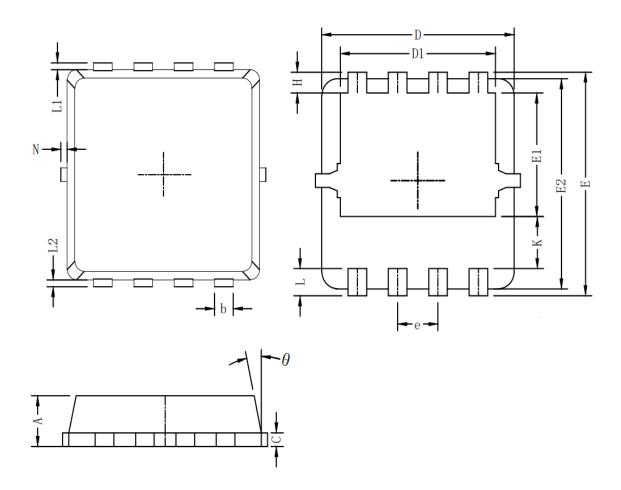
Gate Charge



Q_G - Gate Charge (nC)



Packaging information



Symbol	Dim in mm				
Symbol	min	typ	max		
A	0.6	0.75	0.9		
b	0.2	0.3	0.4		
С	0.15	0.2	0.25		
D	3	3.1	3.2		
D1	2.3	2.45	2.6		
E	3.15	3.3	3.45		
E1	1.43	1.73	1.93		
E2	2.9	3.05	3.2		
е	0.65BSC				
Н	0.2	0.35	0.5		
K	0.57	0.77	0.87		
L	0.3	0.4	0.5		
L1/L2	0.1REF				
θ	8°	10°	13°		
N	0		0.15		



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