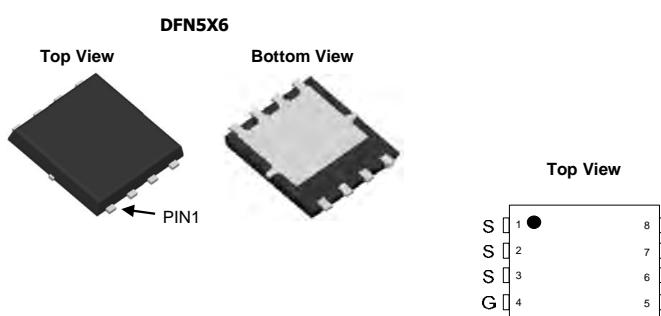


## N-Channel 200 V (D-S) MOSFET

PRODUCT SUMMARY	
$V_{DS}$ (V)	200
$R_{DS(on)}$ Typ. ( $\Omega$ ) at $V_{GS} = 10$ V	0.038
$R_{DS(on)}$ Typ. ( $\Omega$ ) at $V_{GS} = 7.5$ V	0.043
$Q_g$ typ. (nC)	20
$I_D$ (A)	30
Configuration	Single



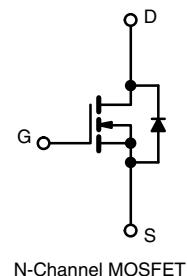
### FEATURES

- ThunderFET® technology optimizes balance of  $R_{DS(on)}$ ,  $Q_g$ ,  $Q_{SW}$  and  $Q_{OSS}$
- 100 %  $R_g$  and UIS tested



### APPLICATIONS

- Fixed telecom
- DC/DC converter
- Primary and secondary side switch
- Synchronous rectification
- LED lighting
- Power supplies
- Class D amplifier



N-Channel MOSFET

### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-source voltage	$V_{DS}$	200	V
Gate-source voltage	$V_{GS}$	$\pm 20$	
Continuous drain current ( $T_J = 150$ °C)	$I_D$	30	A
		23	
		7.6 b, c	
		5.9 b, c	
Pulsed drain current ( $t = 100$ $\mu$ s)	$I_{DM}$	70	A
Continuous source-drain diode current	$I_S$	30	
		5.6 b, c	
Single pulse avalanche current	$I_{AS}$	30	mJ
Single pulse avalanche energy	$E_{AS}$	45	
Maximum power dissipation	$P_D$	104	W
		66.6	
		6.25 b, c	
		4 b, c	
Operating junction and storage temperature range	$T_J, T_{stg}$	-55 to +150	°C
Soldering recommendations (peak temperature) c		260	

### THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient b	$t \leq 10$ s	$R_{thJA}$	15	°C/W
Maximum junction-to-case (drain)	Steady state	$R_{thJC}$	0.9	

#### Notes

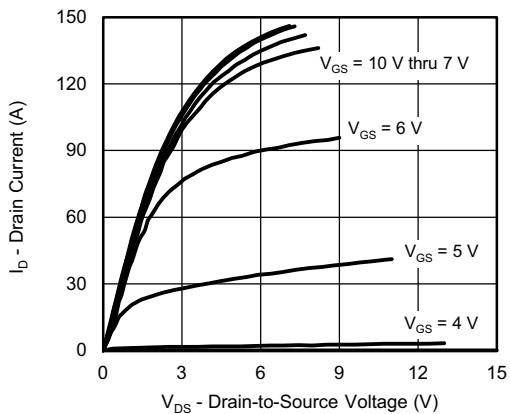
- Package limited.
- Surface mounted on 1" x 1" FR4 board.
- $t = 10$  s.
- The DFN5x6 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.
- Maximum under steady state conditions is 54 °C/W.
- $T_C = 25$  °C.

SPECIFICATIONS ( $T_J = 25^\circ\text{C}$ , unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain-source breakdown voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}$ , $I_D = 250 \mu\text{A}$	200	-	-	V
$V_{DS}$ temperature coefficient	$\Delta V_{DS}/T_J$	$I_D = 10 \text{ mA}$	-	173	-	$\text{mV}/^\circ\text{C}$
$V_{GS(\text{th})}$ temperature coefficient	$\Delta V_{GS(\text{th})}/T_J$	$I_D = 250 \mu\text{A}$	-	-7.1	-	
Gate-source threshold voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$ , $I_D = 250 \mu\text{A}$	2.0		4.0	
Gate-source leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}$ , $V_{GS} = \pm 20 \text{ V}$	-	-	100	nA
Zero gate voltage drain current	$I_{DSS}$	$V_{DS} = 200 \text{ V}$ , $V_{GS} = 0 \text{ V}$	-	-	1	$\mu\text{A}$
		$V_{DS} = 200 \text{ V}$ , $V_{GS} = 0 \text{ V}$ , $T_J = 70^\circ\text{C}$	-	-	15	
On-state drain current <sup>a</sup>	$I_{D(\text{on})}$	$V_{DS} \geq 10 \text{ V}$ , $V_{GS} = 10 \text{ V}$	30	-	-	A
Drain-source on-state resistance <sup>a</sup>	$R_{DS(\text{on})}$	$V_{GS} = 10 \text{ V}$ , $I_D = 10 \text{ A}$	-	0.038	-	$\Omega$
		$V_{GS} = 7.5 \text{ V}$ , $I_D = 10 \text{ A}$	-	0.043	-	
Forward transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 15 \text{ V}$ , $I_D = 10 \text{ A}$	-	27	-	S
<b>Dynamic <sup>b</sup></b>						
Input capacitance	$C_{iss}$	$V_{DS} = 100 \text{ V}$ , $V_{GS} = 0 \text{ V}$ , $f = 1 \text{ MHz}$	-	1380	-	$\text{pF}$
Output capacitance	$C_{oss}$		-	142	-	
Reverse transfer capacitance	$C_{rss}$		-	11	-	
Total gate charge	$Q_g$	$V_{DS} = 100 \text{ V}$ , $V_{GS} = 10 \text{ V}$ , $I_D = 10 \text{ A}$	-	25	38	$\text{nC}$
		$V_{DS} = 100 \text{ V}$ , $V_{GS} = 7.5 \text{ V}$ , $I_D = 10 \text{ A}$	-	20	30	
Gate-source charge	$Q_{gs}$		-	6.4	-	
Gate-drain charge	$Q_{gd}$		-	6.8	-	
Output charge	$Q_{oss}$	$V_{DS} = 100 \text{ V}$ , $V_{GS} = 0 \text{ V}$	-	52	-	
Gate resistance	$R_g$	$f = 1 \text{ MHz}$	0.6	2.1	4	$\Omega$
Turn-on delay time	$t_{d(\text{on})}$	$V_{DD} = 100 \text{ V}$ , $R_L = 10 \Omega$ , $I_D \geq 10 \text{ A}$ , $V_{GEN} = 10 \text{ V}$ , $R_g = 1 \Omega$	-	9	18	$\text{ns}$
Rise time	$t_r$		-	20	40	
Turn-off delay time	$t_{d(\text{off})}$		-	20	40	
Fall time	$t_f$		-	24	48	
Turn-on delay time	$t_{d(\text{on})}$	$V_{DD} = 100 \text{ V}$ , $R_L = 10 \Omega$ , $I_D \geq 10 \text{ A}$ , $V_{GEN} = 7.5 \text{ V}$ , $R_g = 1 \Omega$	-	11	22	
Rise time	$t_r$		-	27	54	
Turn-off delay time	$t_{d(\text{off})}$		-	18	36	
Fall time	$t_f$		-	24	48	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous source-drain diode current	$I_S$	$T_C = 25^\circ\text{C}$	-	-	35.4	$\text{A}$
Pulse diode forward current	$I_{SM}$		-	-	80	
Body diode voltage	$V_{SD}$	$I_S = 5 \text{ A}$ , $V_{GS} = 0 \text{ V}$	-	0.77	1.1	V
Body diode reverse recovery time	$t_{rr}$	$I_F = 10 \text{ A}$ , $dI/dt = 100 \text{ A}/\mu\text{s}$ , $T_J = 25^\circ\text{C}$	-	100	200	$\text{ns}$
Body diode reverse recovery charge	$Q_{rr}$		-	400	800	$\text{nC}$
Reverse recovery fall time	$t_a$		-	80	-	$\text{ns}$
Reverse recovery rise time	$t_b$		-	20	-	

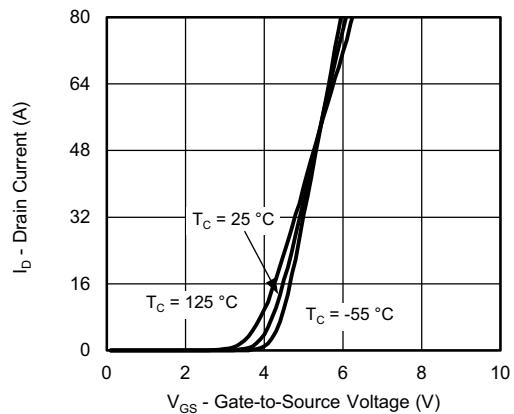
**Notes**

a. Pulse test; pulse width  $\leq 300 \mu\text{s}$ , duty cycle  $\leq 2 \%$ .  
 b. Guaranteed by design, not subject to production testing.

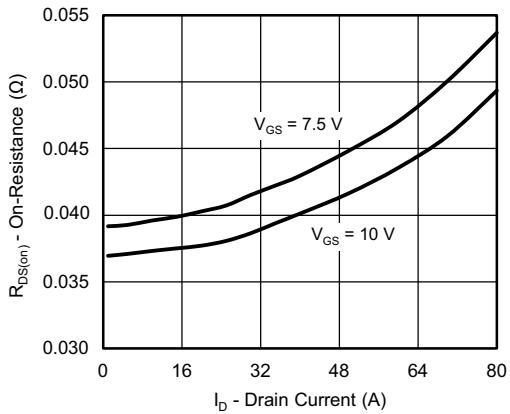
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)


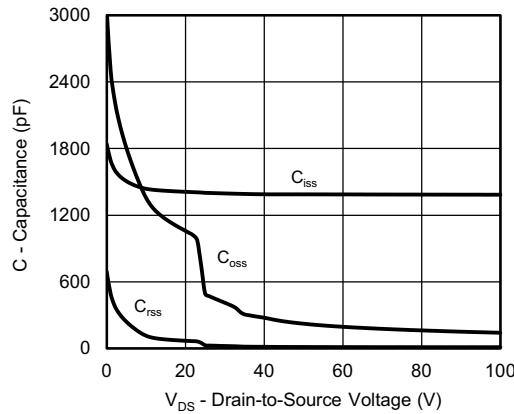
Output Characteristics



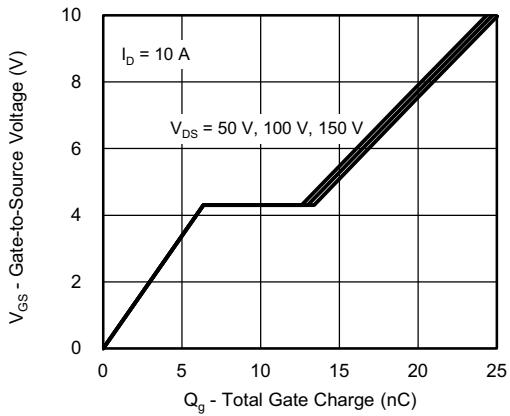
Transfer Characteristics



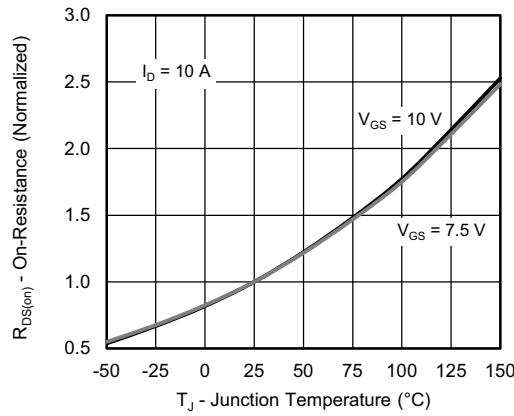
On-Resistance vs. Drain Current and Gate Voltage



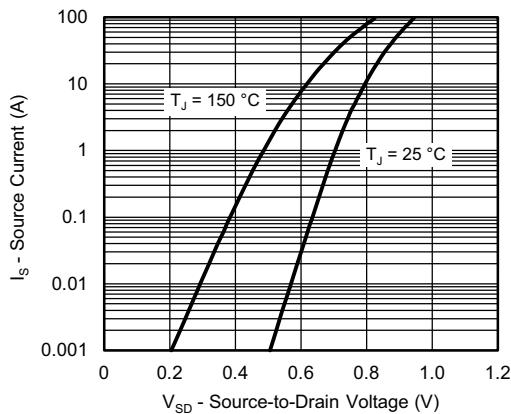
Capacitance



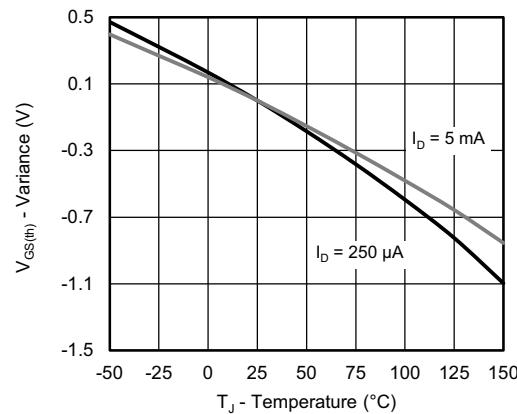
Gate Charge



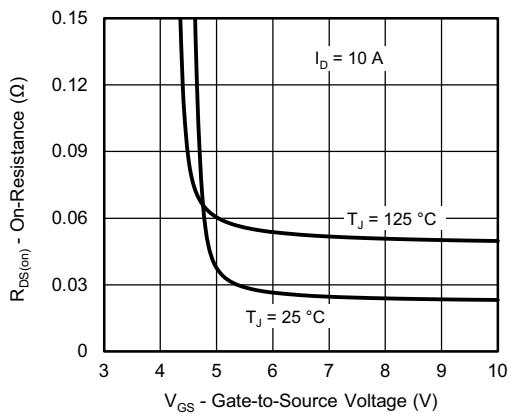
On-Resistance vs. Junction Temperature

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)


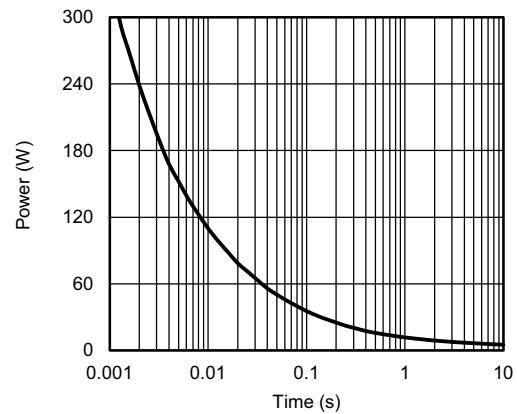
Source-Drain Diode Forward Voltage



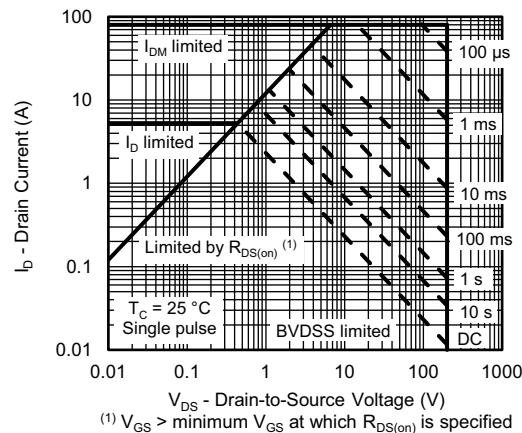
Threshold Voltage



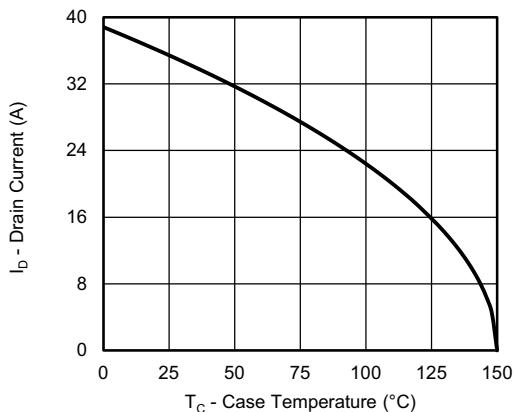
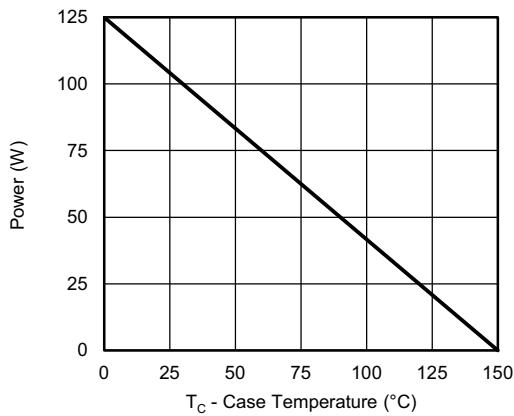
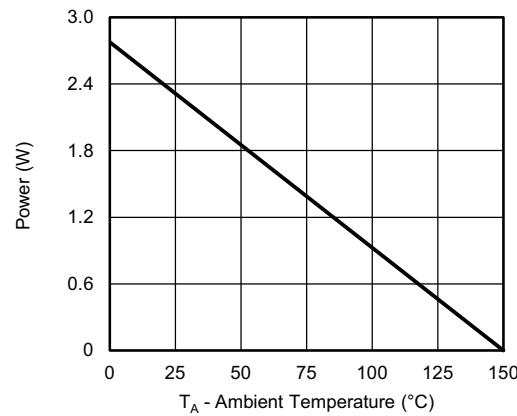
On-Resistance vs. Gate-to-Source Voltage



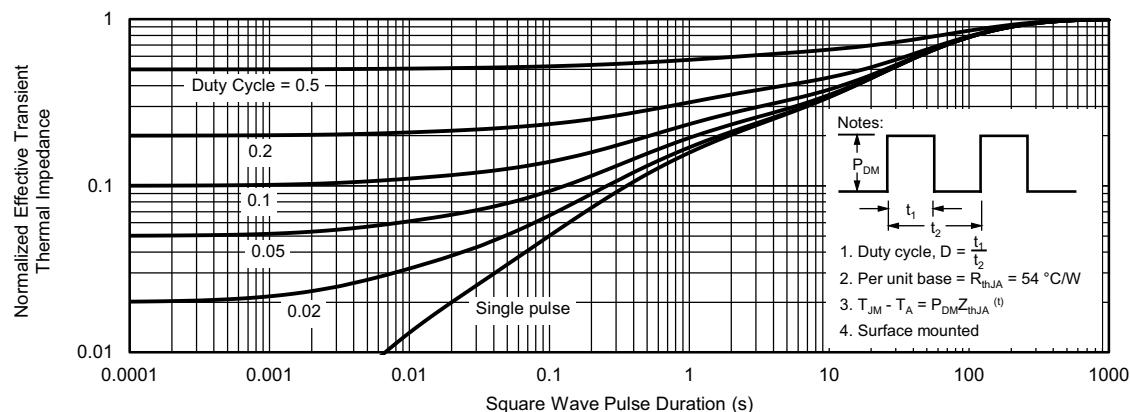
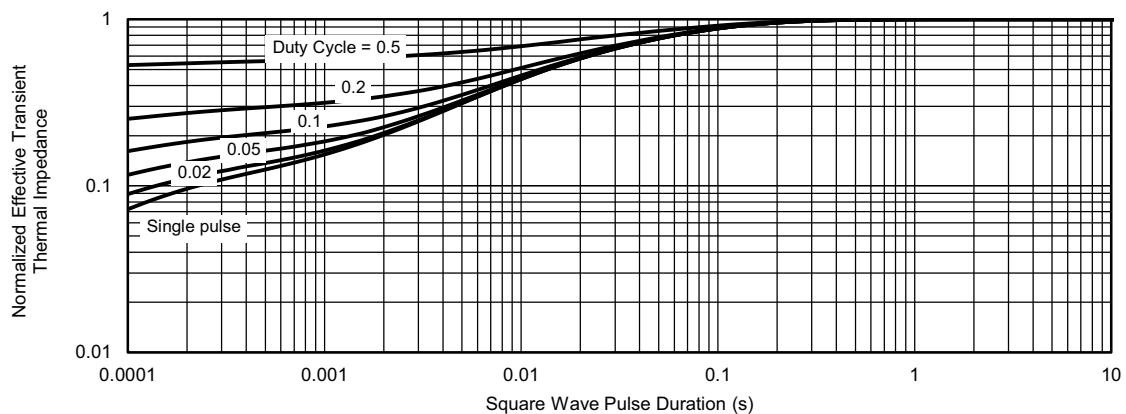
Single Pulse Power, Junction-to-Ambient



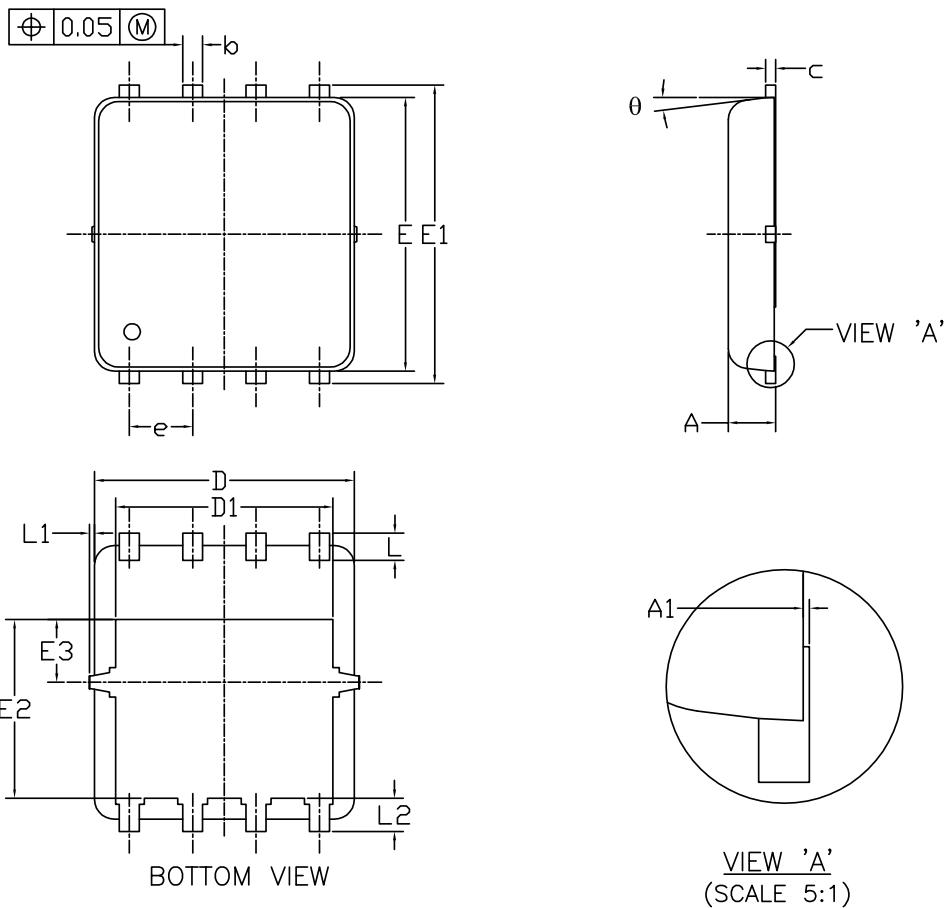
Safe Operating Area, Junction-to-Ambient

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

**Current Derating <sup>a</sup>**

**Power, Junction-to-Case**

**Power, Junction-to-Ambient**
**Note**

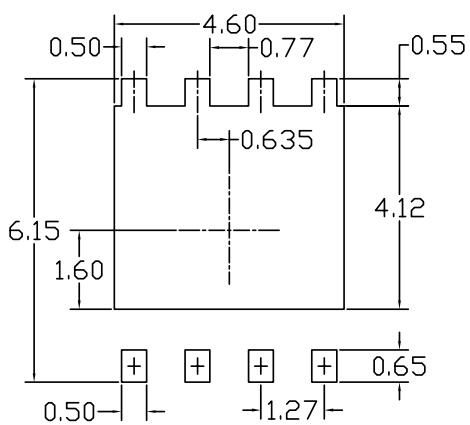
a. The power dissipation  $P_D$  is based on  $T_J$  max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

**Normalized Thermal Transient Impedance, Junction-to-Ambient**

**Normalized Thermal Transient Impedance, Junction-to-Case**

## DFN5x6\_8L\_EP1\_P PACKAGE OUTLINE



## RECOMMENDED LAND PATTERN



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.85	0.95	1.00	0.033	0.037	0.039
A1	0.00	---	0.05	0.000	---	0.002
b	0.30	0.40	0.50	0.012	0.016	0.020
c	0.15	0.20	0.25	0.006	0.008	0.010
D	5.10	5.20	5.30	0.201	0.205	0.209
D1	4.25	4.35	4.45	0.167	0.171	0.175
E	5.45	5.55	5.65	0.215	0.219	0.222
E1	5.95	6.05	6.15	0.234	0.238	0.242
E2	3.525	3.625	3.725	0.139	0.143	0.147
E3	1.175	1.275	1.375	0.046	0.050	0.054
e	1.27 BSC			0.050 BSC		
L	0.45	0.55	0.65	0.018	0.022	0.026
L1	0	---	0.15	0	---	0.006
L2	0.68 REF			0.027 REF		
θ	0°	---	10°	0°	---	10°

UNIT: mm

## NOTE

1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.  
MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
2. CONTROLLING DIMENSION IS MILLIMETER.  
CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.

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