

■ PRODUCT CHARACTERISTICS

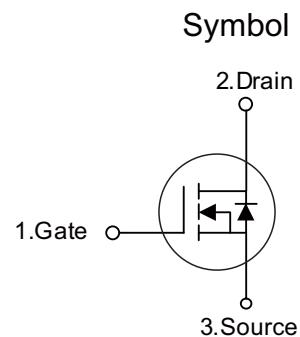
VDSS	650V
R _{DS(on)typ} (@V _{GS} = 10 V)	1.15Ω
Qg@type	29nC
ID	7A

■ APPLICATIONS

- High efficiency switch mode power supplies
- Electronic lamp ballasts based on half bridge
- LED power supplies

■ FEATURES

- * Ultra low gate charge
- * Low reverse transfer Capacitance
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness



■ ORDER INFORMATION

Order codes		Package	Packing
Halogen-Free	Halogen		
N/A	MOT7N65YF	TO-220F	50 pieces/Tube
N/A	MOT7N65YA	TO-220	50 pieces/Tube

■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	650	V
Gate-Source Voltage		V _{GSS}	±30	V
Avalanche Current (Note 2)		I _{AR}	7	A
Drain Current	Continuous	I _D	7	A
	Pulsed (Note 2)	I _{DM}	29.6	A
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	530	mJ
	Repetitive (Note 2)	E _{AR}	14.2	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220	P _D	142	W
	TO-220F		48	
Junction Temperature		T _J	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature

3. L = 19.5mH, I_{AS} = 7A, V_{DD} = 50V, R_G = 25 Ω, Starting T_J = 25°C

4. I_{SD}≤7A, di/dt≤200A/μs, V_{DD}≤BV_{DSS}, Starting T_J = 25°C

■ ELECTRICAL CHARACTERISTICS ($T_c = 25^\circ\text{C}$, unless otherwise specified)

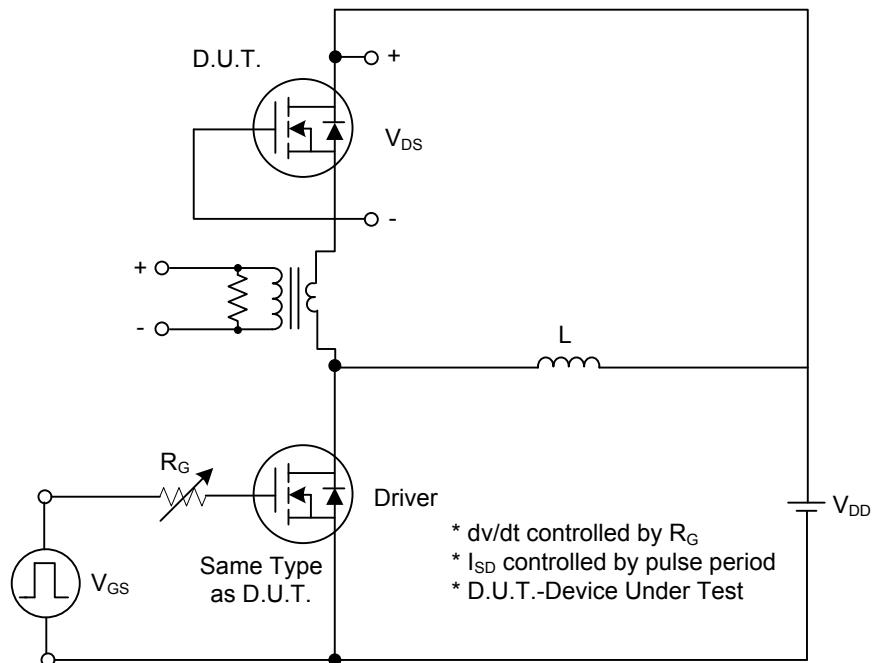
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Off characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 250\mu\text{A}$	650	-	-	V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}} = 650\text{V}, V_{\text{GS}} = 0\text{V}$	-	-	1	μA
Gate- Source Leakage Current	Forward	$V_{\text{GS}} = 30\text{V}, V_{\text{DS}} = 0\text{V}$	-	-	100	nA
	Reverse	$V_{\text{GS}} = -30\text{V}, V_{\text{DS}} = 0\text{V}$	-	-	-100	nA
Breakdown Voltage Temperature Coefficient	$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$I_{\text{D}}=250\mu\text{A}$, Referenced to 25°C	-	0.67	-	$\text{V}/^\circ\text{C}$
Gate Threshold Voltage	$V_{\text{GS(TH)}}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250\mu\text{A}$	2.0	-	4.0	V
Static Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}} = 10\text{V}, I_{\text{D}} = 3.5\text{A}$	-	1.15	1.25	Ω
Dynamic characteristics						
Input Capacitance	C_{ISS}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{ MHz}$	-	-	1400	pF
Output Capacitance	C_{OSS}		-	-	180	pF
Reverse Transfer Capacitance	C_{RSS}		-	16	21	pF
Switching characteristics						
Turn-On Delay Time	$t_{\text{D(ON)}}$	$V_{\text{DD}} = 325\text{V}, I_{\text{D}} = 7.4\text{A}, R_{\text{G}} = 25\Omega$ (Note 1, 2)	-	-	70	ns
Turn-On Rise Time	t_{R}		-	-	170	ns
Turn-Off Delay Time	$t_{\text{D(OFF)}}$		-	-	140	ns
Turn-Off Fall Time	t_{F}		-	-	130	ns
Switching characteristics						
Total Gate Charge	Q_{G}	$V_{\text{DS}}=520\text{V}, I_{\text{D}} = 7\text{A}, V_{\text{GS}}=10\text{ V}$ (Note 1, 2)	-	29	38	nC
Gate-Source Charge	Q_{GS}		-	7	-	nC
Gate-Drain Charge	Q_{GD}		-	14.5	-	nC
Source-drain diode ratings and characteristics						
Drain-Source Diode Forward Voltage	V_{SD}	$V_{\text{GS}} = 0\text{V}, I_{\text{S}} = 7\text{A}$	-	-	1.4	V
Maximum Continuous Drain-Source Diode Forward Current	I_{S}		-	-	7	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}		-	-	29.6	A
Reverse Recovery Time	t_{rr}	$V_{\text{GS}} = 0\text{V}, I_{\text{S}} = 7\text{A}, dI_{\text{F}} / dt = 100\text{A}/\mu\text{s}$ (Note 1)	-	320	-	ns
Reverse Recovery Charge	Q_{RR}		-	2.4	-	μC

Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$

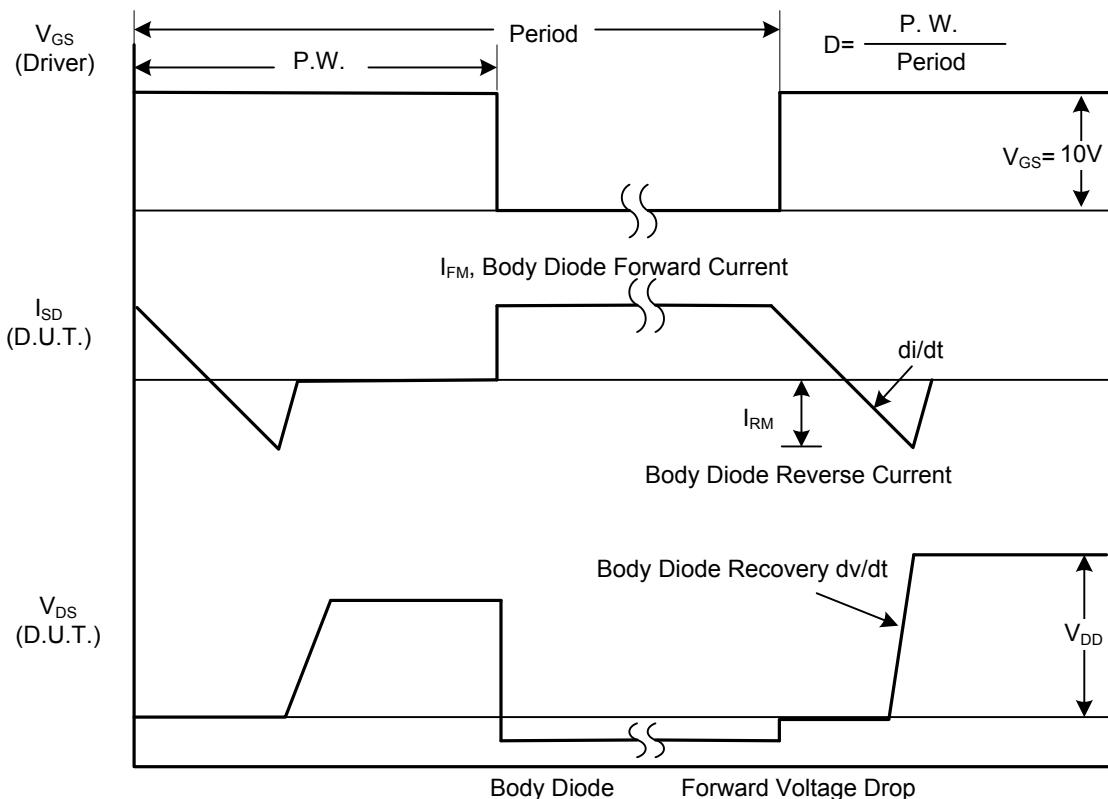
2. Essentially independent of operating temperature



■ TEST CIRCUITS AND WAVEFORMS



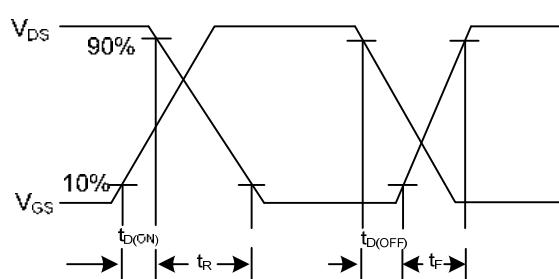
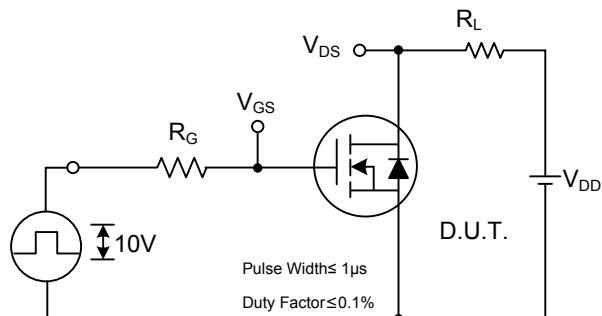
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

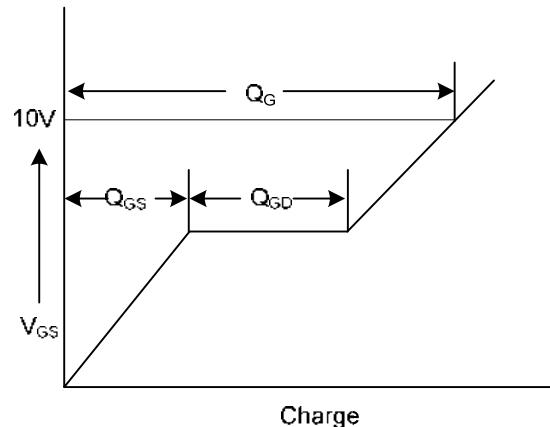
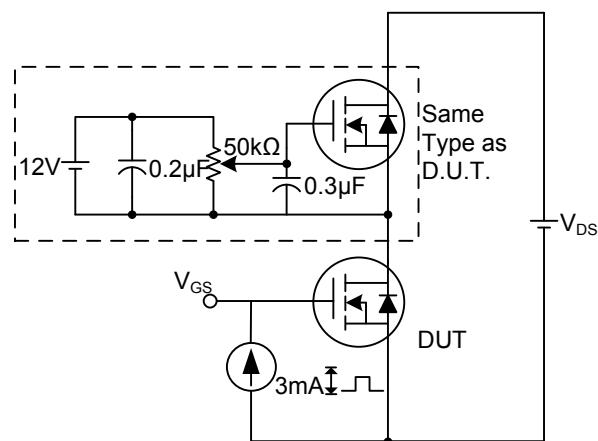


■ TEST CIRCUITS AND WAVEFORMS(Cont.)



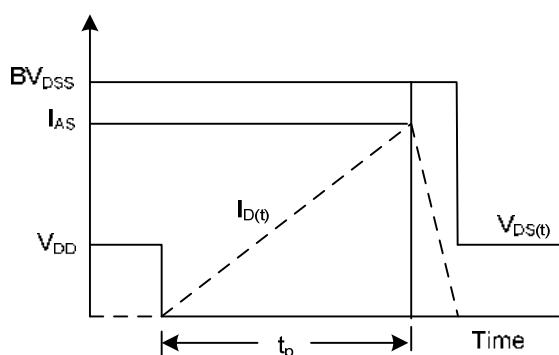
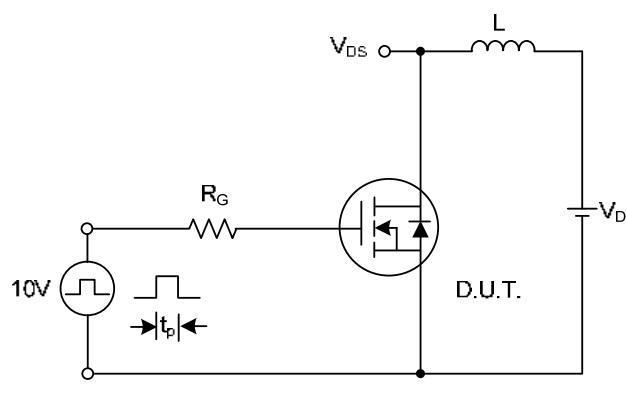
Switching Test Circuit

Switching Waveforms



Gate Charge Test Circuit

Gate Charge Waveform

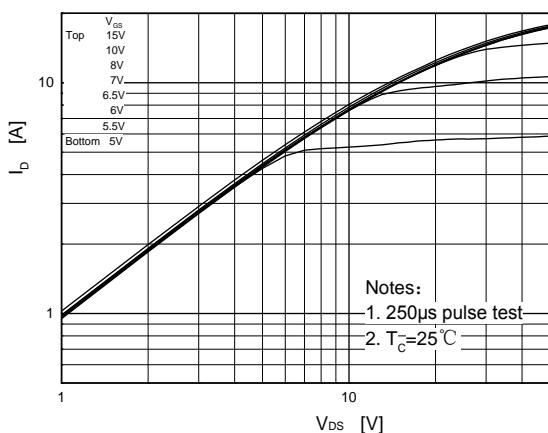


Unclamped Inductive Switching Test Circuit

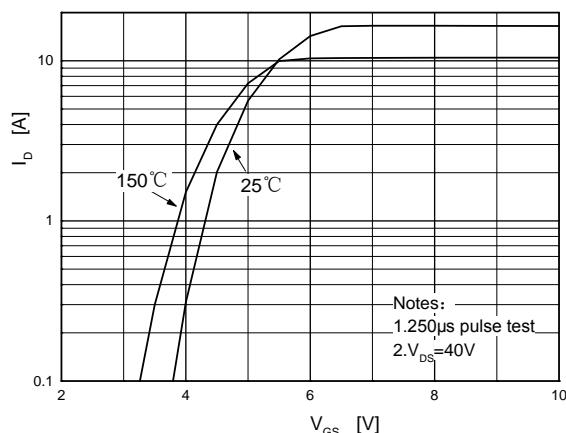
Unclamped Inductive Switching Waveforms

■ ELECTRICAL CHARACTERISTICS

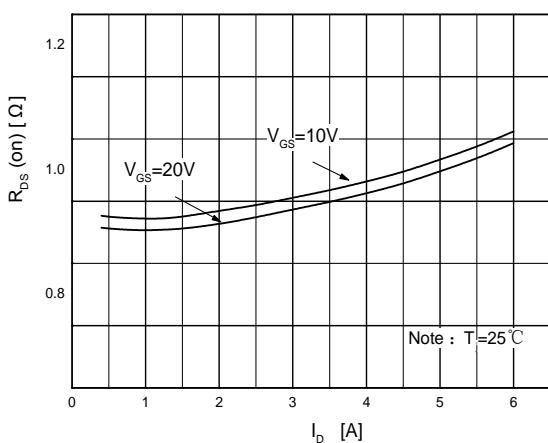
On-Region Characteristics



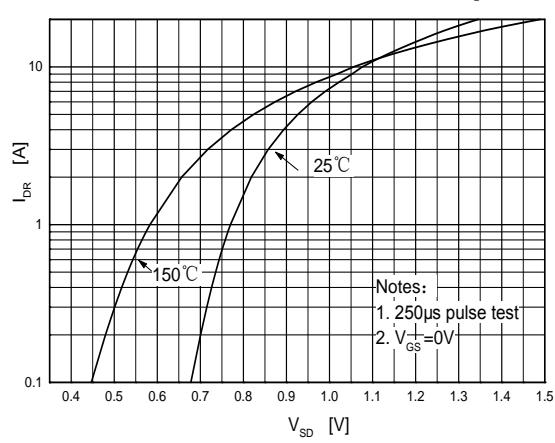
Transfer Characteristics



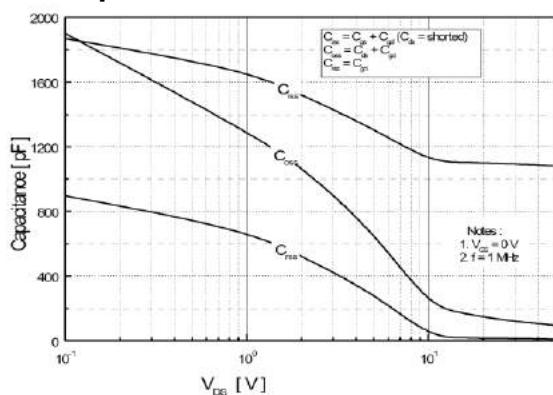
On-Resistance Variation vs. Drain Current and Gate Voltage



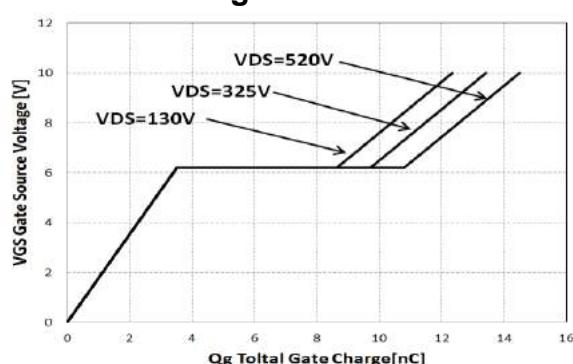
Body Diode Forward Voltage Variation vs. Source Current and Temperature



Capacitance Characteristics



Gate Charge Characteristics



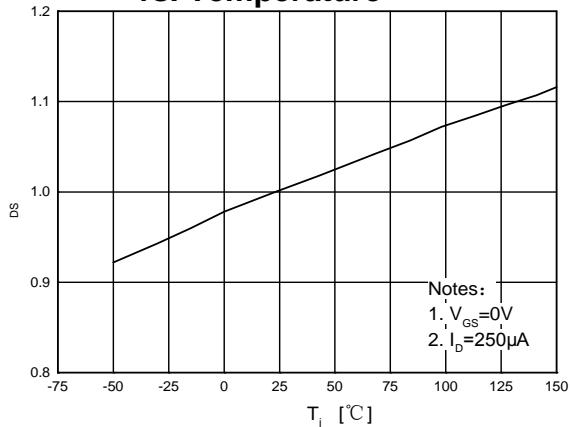


仁懋电子

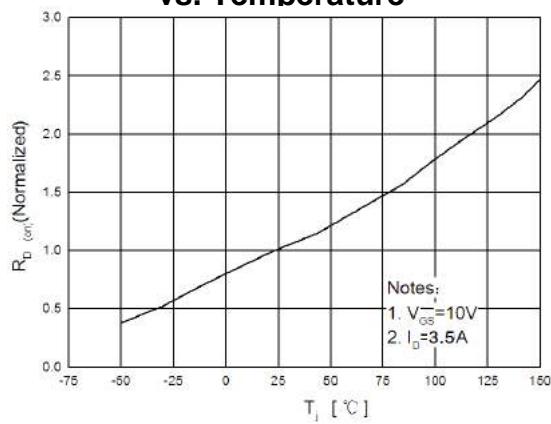
MOT7N65YA
MOT7N65YF
N-CHANNEL MOSFET

■ ELECTRICAL CHARACTERISTICS(Cont.)

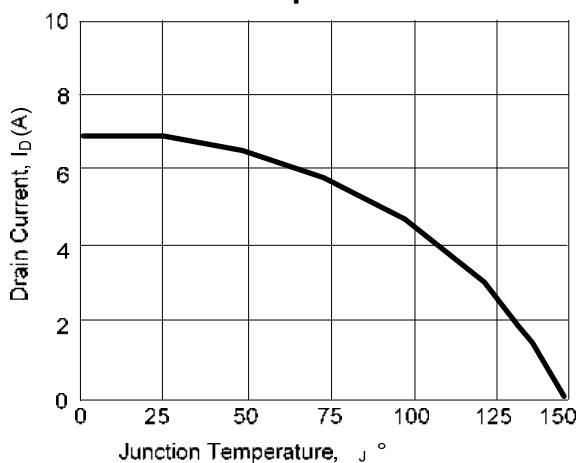
**Breakdown Voltage Variation
vs. Temperature**



**On-Resistance Variation
vs. Temperature**



**Drain Current vs Junction
Temperature**

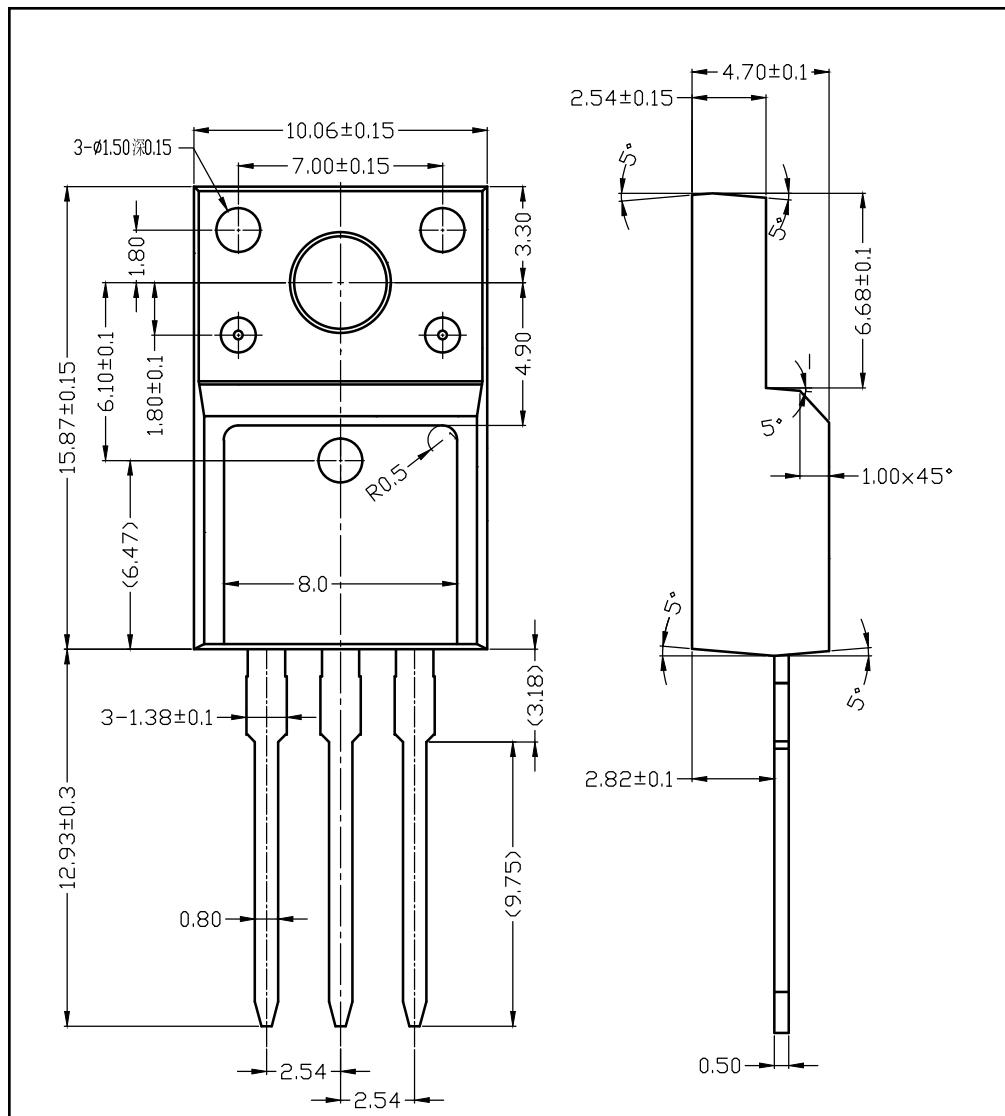




仁懋电子

MOT7N65YA
MOT7N65YF
N-CHANNEL MOSFET

■ TO-220F-3L PACKAGE OUTLINE DIMENSIONS





仁懋电子

MOT7N65YA
MOT7N65YF
N-CHANNEL MOSFET

■ TO-220-3L PACKAGE OUTLINE DIMENSIONS

