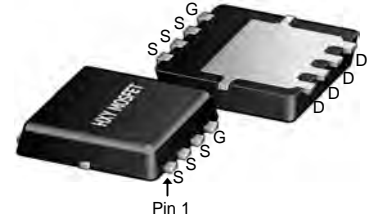




## General Description

The BSC037N08NS5 use advanced SGT MOSFET technology to provide low  $R_{DS(ON)}$ , low gate charge, fast switching and excellent avalanche characteristics. This device is specially designed to get better ruggedness and suitable.

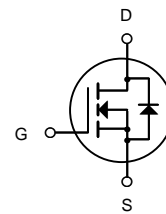


DFN5X6-8L

## General Features

$V_{DS} = 85V$   $I_D = 100A$

$R_{DS(ON)} < 5.6m\Omega @ V_{GS} = 10V$



N-Channel MOSFET

## Applications

Consumer electronic power supply Motor control  
Synchronous-rectification Isolated DC  
Synchronous-rectification applications

## Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
BSC037N08NS5	DFN5X6-8L	HXY MOSFET	5000

## Absolute Maximum Ratings ( $T_C = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	85	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	100	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	63.3	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	400	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	273.8	mJ
$P_D @ T_C = 25^\circ C$	Total Power Dissipation <sup>4</sup>	107.8	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$
$R_{\theta JC}$	Thermal Resistance from Junction-to-Ambient <sup>3</sup>	1.16	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	60	$^\circ C/W$



**Electrical Characteristics (T<sub>J</sub> = 25°C, unless otherwise noted)**

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	<b>V<sub>(BR)DSS</sub></b>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	85	-	-	V
Gate-body Leakage current	<b>I<sub>GSS</sub></b>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T <sub>J</sub> =25°C	V <sub>DS</sub> = 85V, V <sub>GS</sub> = 0V	-	-	1	μA
	T <sub>J</sub> =100°C		-	-	100	
Gate-Threshold Voltage	<b>V<sub>GS(th)</sub></b>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2	3	4	V
Drain-Source on-Resistance <sup>4</sup>	<b>R<sub>DS(on)</sub></b>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A	-	4.3	5.6	mΩ
Forward Transconductance <sup>4</sup>	<b>g<sub>fs</sub></b>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 20A	-	57.8	-	S
<b>Dynamic Characteristics<sup>5</sup></b>						
Input Capacitance	<b>C<sub>iss</sub></b>	V <sub>DS</sub> = 40V, V <sub>GS</sub> = 0V, f = 1MHz	-	4645	-	pF
Output Capacitance	<b>C<sub>oss</sub></b>		-	673	-	
Reverse Transfer Capacitance	<b>C<sub>rss</sub></b>		-	41	-	
Gate Resistance	<b>R<sub>g</sub></b>	f=1MHz	-	2.0	-	Ω
<b>Switching Characteristics<sup>5</sup></b>						
Total Gate Charge	<b>Q<sub>g</sub></b>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 40V, I <sub>D</sub> = 20A	-	61.3	-	nC
Gate-Source Charge	<b>Q<sub>gs</sub></b>		-	21	-	
Gate-Drain Charge	<b>Q<sub>gd</sub></b>		-	11	-	
Turn-on Delay Time	<b>t<sub>d(on)</sub></b>	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 40V, R <sub>G</sub> = 3Ω, I <sub>D</sub> = 20A	-	16.5	-	ns
Rise Time	<b>t<sub>r</sub></b>		-	51.8	-	
Turn-off Delay Time	<b>t<sub>d(off)</sub></b>		-	37.1	-	
Fall Time	<b>t<sub>f</sub></b>		-	8.2	-	
Body Diode Reverse Recovery Time	<b>t<sub>rr</sub></b>	I <sub>F</sub> =20A, di/dt = 100A/μS	-	69	-	ns
Body Diode Reverse Recovery Charge	<b>Q<sub>rr</sub></b>		-	141	-	nC
<b>Drain-Source Body Diode Characteristics</b>						
Diode Forward Voltage <sup>4</sup>	<b>V<sub>SD</sub></b>	I <sub>S</sub> = 20A, V <sub>GS</sub> = 0V	-	-	1.2	V
Continuous Source Current	T <sub>C</sub> =25°C	<b>I<sub>S</sub></b>	-	-	100	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature T<sub>J</sub>(MAX)=150°C
2. The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=50V, V<sub>GS</sub>=10V, L=0.4mH, I<sub>AS</sub>=37A
3. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
5. This value is guaranteed by design hence it is not included in the production test.



### Typical Characteristics

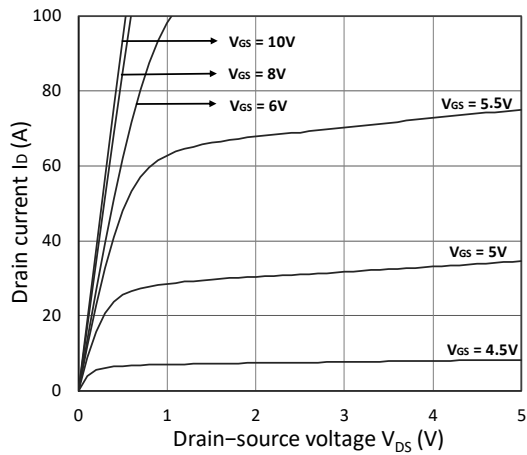


Figure 1. Output Characteristics

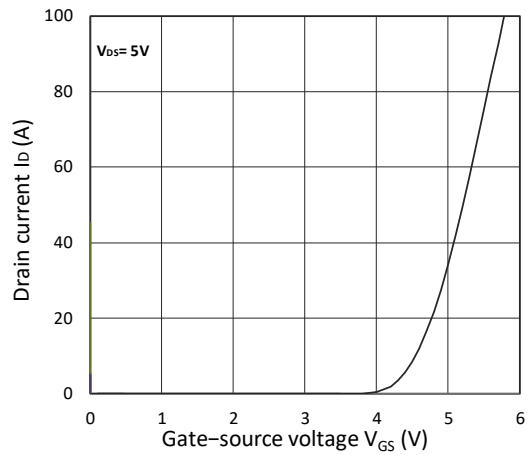


Figure 2. Transfer Characteristics

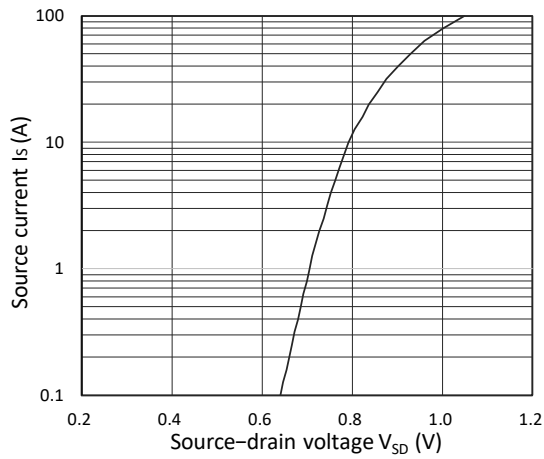


Figure 3. Forward Characteristics of Reverse

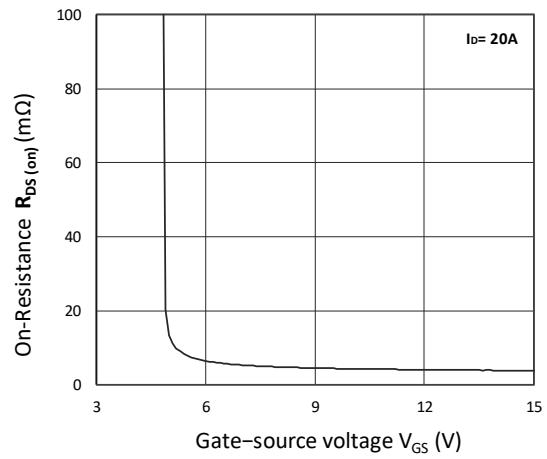


Figure 4.  $R_{DS(ON)}$  vs.  $V_{GS}$

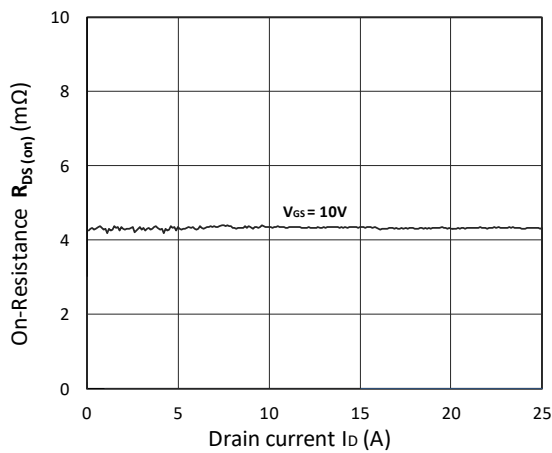


Figure 5.  $R_{DS(ON)}$  vs.  $I_D$

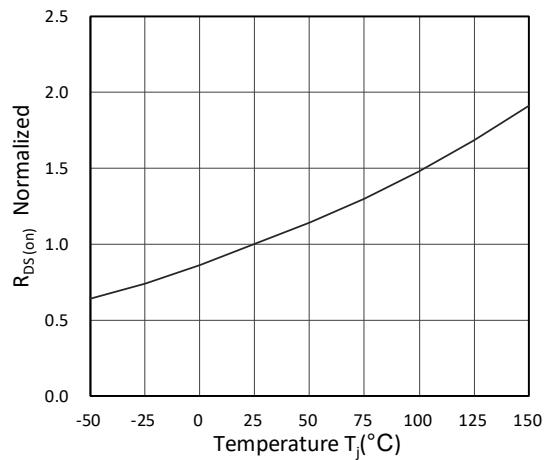


Figure 6. Normalized  $R_{DS(ON)}$  vs. Temperature

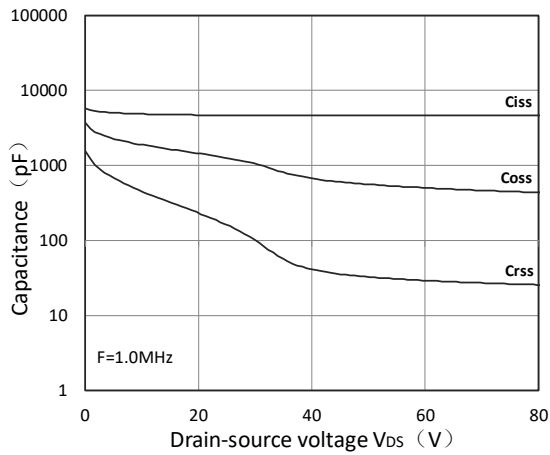


Figure 7. Capacitance Characteristics

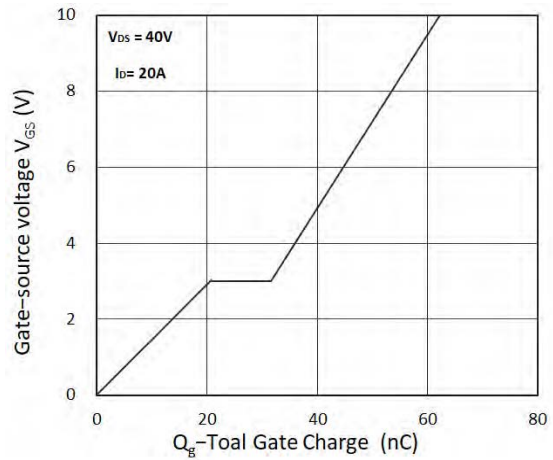


Figure 8. Gate Charge Characteristics

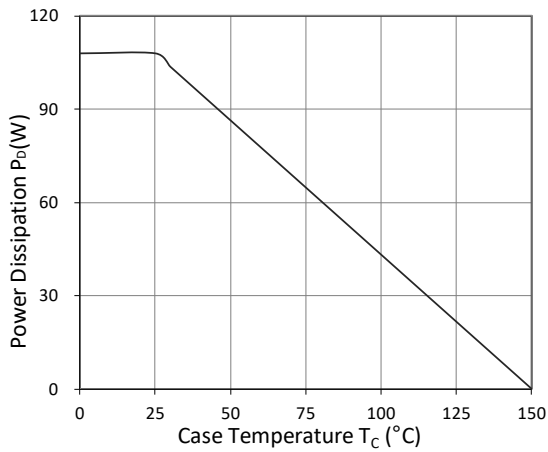


Figure 9. Power Dissipation

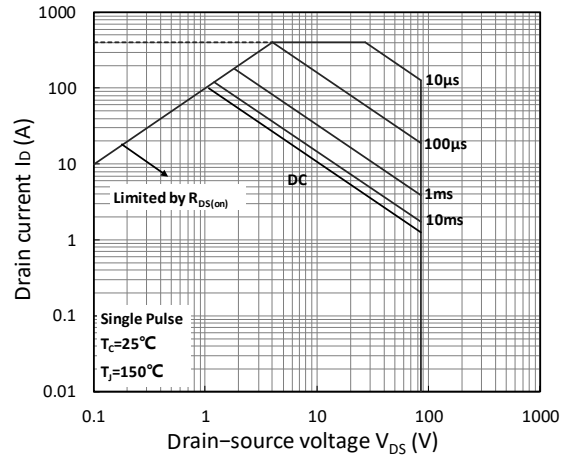


Figure 10. Safe Operating Area

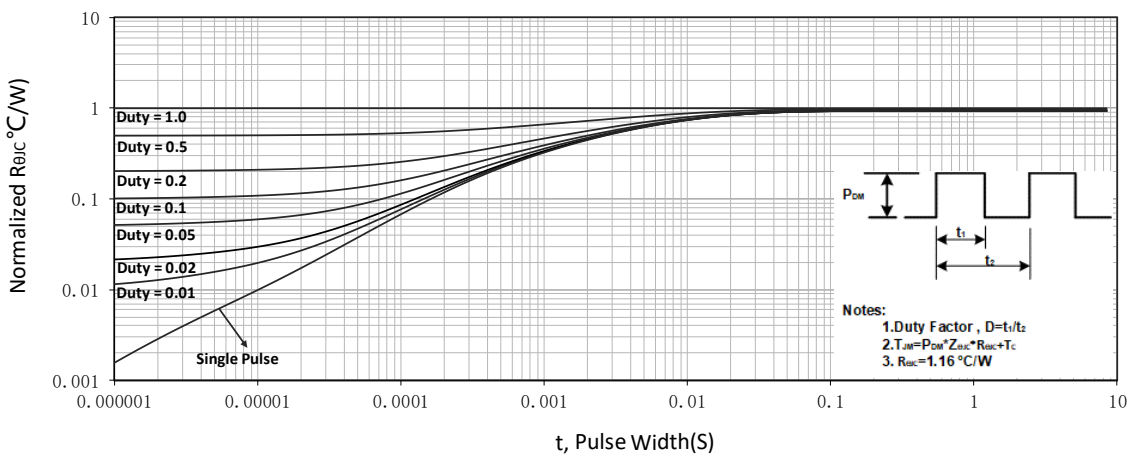
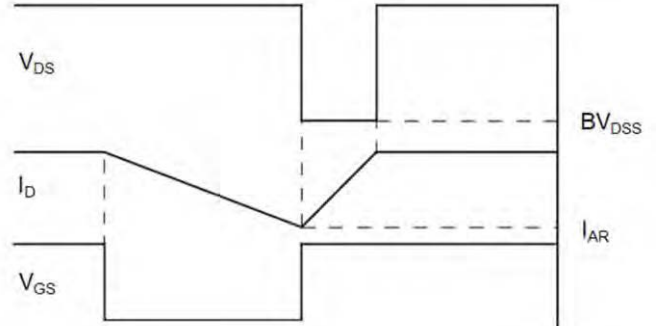
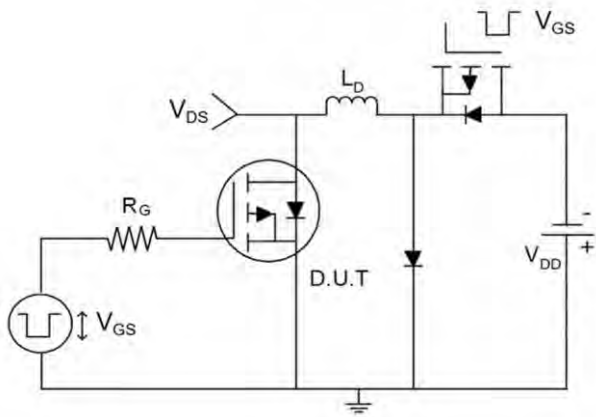


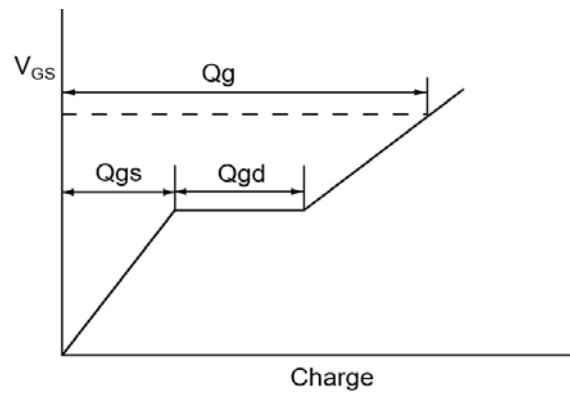
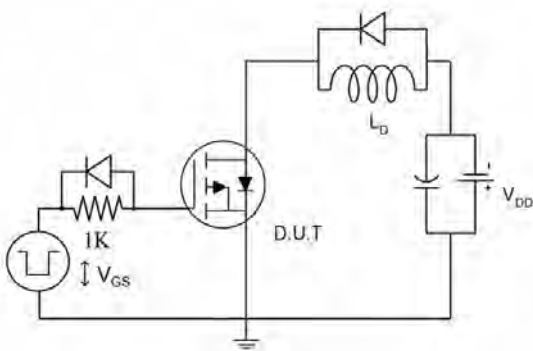
Figure 11. Normalized Maximum Transient Thermal Impedance



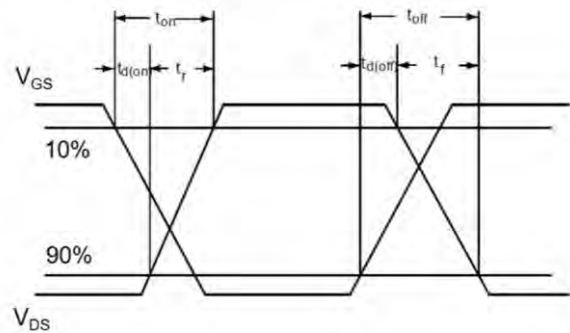
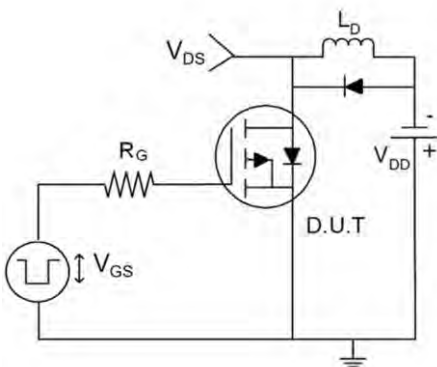
### 1) $E_{AS}$ Test Circuits



### 2) Gate Charge Test Circuit

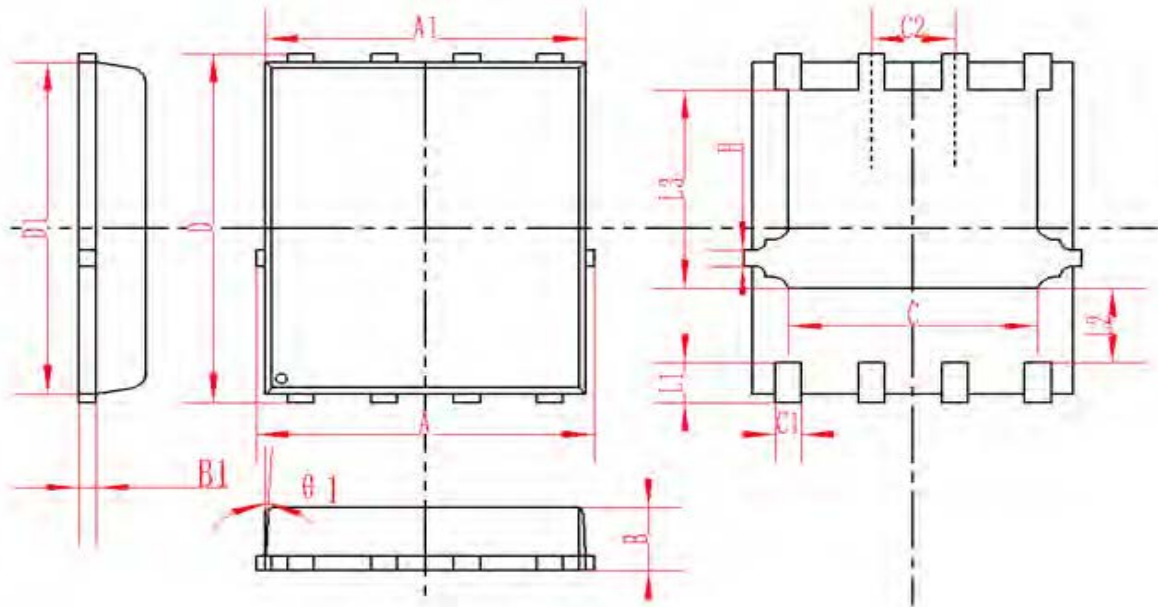


### 3) Switch Time Test Circuit





**DFN5X6-8L Package Information**



SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX
A	4.95	5	5.05	0.195	0.197	0.199
A1	4.82	4.9	4.98	0.190	0.193	0.196
D	5.98	6	6.02	0.235	0.236	0.237
D1	5.67	5.75	5.83	0.223	0.226	0.230
B	0.9	0.95	1	0.035	0.037	0.039
B1	0.254REF			0.010REF		
C	3.95	4	4.05	0.156	0.157	0.159
C1	0.35	0.4	0.45	0.014	0.016	0.018
C2	1.27TYP			0.5TYP		
θ1	8°	10°	12°	8°	10°	12°
L1	0.63	0.64	0.65	0.025	0.025	0.026
L2	1.2	1.3	1.4	0.047	0.051	0.055
L3	3.415	3.42	3.425	0.134	0.135	0.135
H	0.24	0.25	0.26	0.009	0.010	0.010



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