

**SuperMOS –TO-252, 100V  $V_{DSS}$ , 100m $\Omega$   $R_{DS(on)}$ , N-channel MOSFET**

**1. Description**

The IRFR120NTRPBF-ES is N-Channel enhancement MOS Field Effect Transistor. Uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. Device is suitable for use in DC-DC conversion, power switch and charging circuit. Standard Product IRFR120NTRPBF-ES is Pb-free.

**2. Features**

- 100V,  $R_{DS(ON)}$ =100m $\Omega$ (Typ.),  $V_{GS}$ =10V
- $R_{DS(ON)}$ =110m $\Omega$ (Typ.),  $V_{GS}$ =4.5V
- Use trench MOSFET technology
- High density cell design for low  $R_{DS(on)}$
- Material: Halogen free
- Reliable and rugged
- Avalanche Rated
- Low leakage current

**3. Applications**

- PWM applications
- Load switch
- Power management in portable/desktop PCs
- DC/DC conversion

**100% UIS TESTED**

**4. Ordering Information**

Part Number	Package	Marking	Material	Packing	Quantity per reel	Flammability Rating	Reel Size
IRFR120NTRPBF-ES	TO-252	ES15N10B /LOT	Halogen free	Tape & Reel	2,500 PCS	UL 94V-0	13 Inches

Table-1 Ordering information

**5. Pin Configuration and Functions**

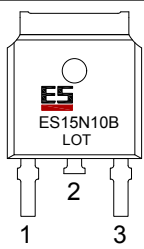
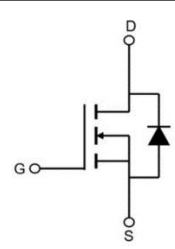
Pin	Function	Outline	Circuit Diagram
1	Gate		
2	Drain		
3	Source		

Table-2 Pin configuration

## 6. Specification

### Absolute Maximum Rating & Thermal Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	$BV_{DSS}$	100	V	
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V	
Continuous Drain Current	$T_C=25^\circ\text{C}$	9	A	
	$T_C=75^\circ\text{C}$	7		
Maximum Power Dissipation	$T_C=25^\circ\text{C}$	$P_D$	20	W
Pulsed Drain Current	$I_{DM}$	36	A	
Avalanche Current Single Pulsed <sup>a</sup>	$I_{AS}$	10.5	A	
Avalanche energy Single Pulsed <sup>a</sup>	$E_{AS}$	16	mJ	
Operating Junction Temperature	$T_J$	150	°C	
Lead Temperature	$T_L$	260	°C	
Storage Temperature Range	$T_{stg}$	-55 to 150	°C	

#### Thermal resistance ratings

Single Operation					
Parameter	Symbol	Typical	Maximum	Unit	
Junction-to-Case Thermal Resistance	$t \leq 10 \text{ s}$	$R_{\theta JC}$	6.3	°C/W	

Note:

a:  $V_{DD}=100\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=0.3\text{mH}$ ,  $R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$ .

## IRFR120NTRPBF-ES

Rev-1.6

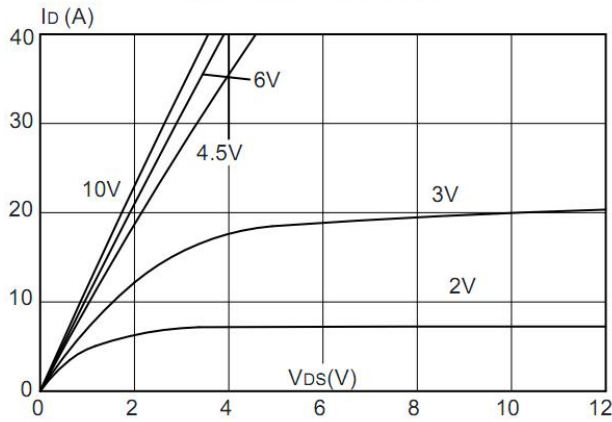
## Electrical Characteristics

At TA = 25°C unless otherwise specified

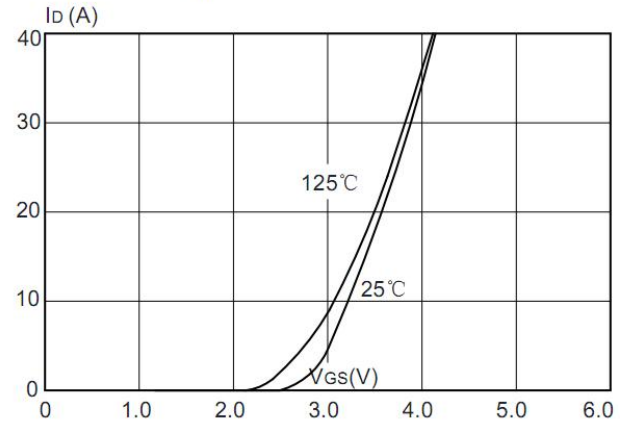
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-to-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	100			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=100V, V_{GS}=0V$			1.0	$\mu A$
Gate-to-source Leakage Current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 20V$			$\pm 100$	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	1.0	1.5	2.5	V
Drain-to-source On-resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=5A$		100	115	m $\Omega$
		$V_{GS}=4.5V, I_D=3A$		110	140	
Forward Trans conductance	$g_{FS}$	$V_{DS}=5.0V, I_D=5A$			40	S
<b>CHARGES, CAPACITANCES AND GATE RESISTANCE</b>						
Input Capacitance	$C_{ISS}$	$V_{GS}=0V, f=1MHz, V_{DS}=25V$		605		pF
Output Capacitance	$C_{OSS}$			40		
Reverse Transfer Capacitance	$C_{RSS}$			23		
Total Gate Charge	$Q_{G(TOT)}$	$V_{GS}=10V, V_{DS}=30V, I_D=5A$		11		nC
Gate-to-Source Charge	$Q_{GS}$			2.1		
Gate-to-Drain Charge	$Q_{GD}$			2.4		
<b>SWITCHING CHARACTERISTICS</b>						
Turn-On Delay Time	$t_{d(ON)}$	$V_{GS}=10V, V_{DS}=30V, I_D=5A, R_{GEN}=3\Omega$		6.8		ns
Rise Time	$t_r$			5		
Turn-Off Delay Time	$t_{d(OFF)}$			15.8		
Fall Time	$t_f$			6		
<b>BODY DIODE CHARACTERISTICS</b>						
Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_S=1A$	0.45		1.5	V

**7. Typical Characteristic**

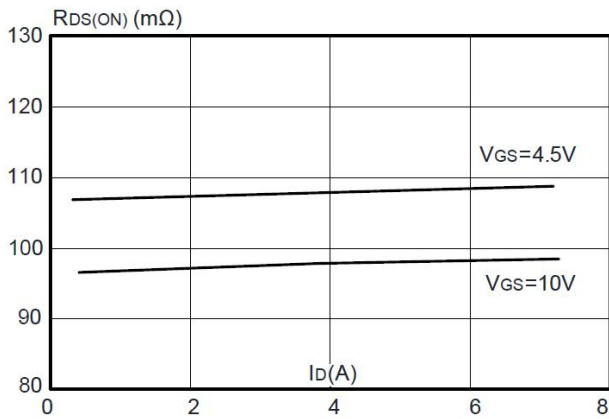
**Figure 1: Output Characteristics**



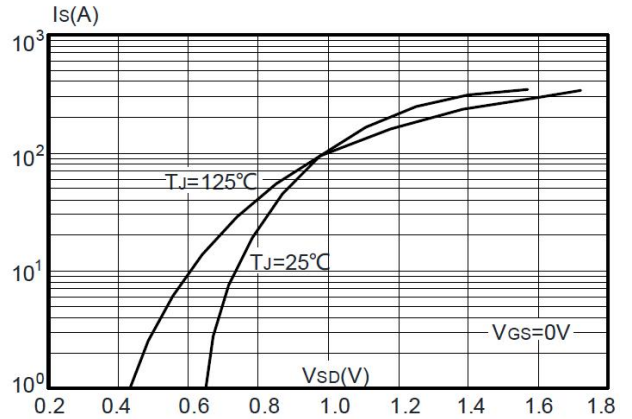
**Figure 2: Typical Transfer Characteristics**



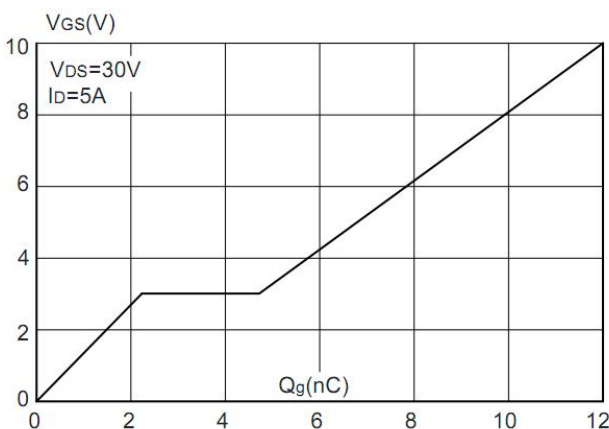
**Figure 3: On-resistance vs. Drain Current**



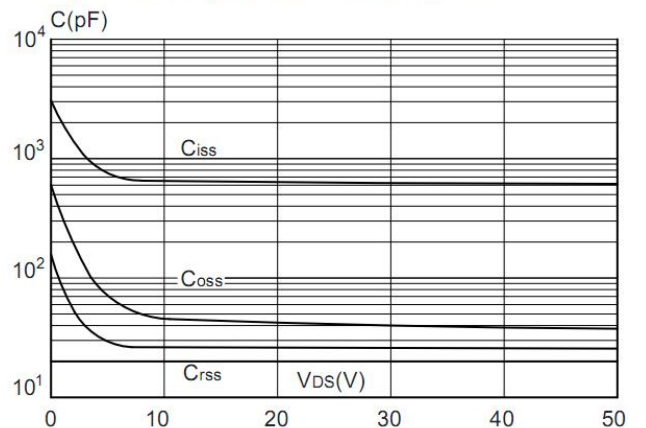
**Figure 4: Body Diode Characteristics**



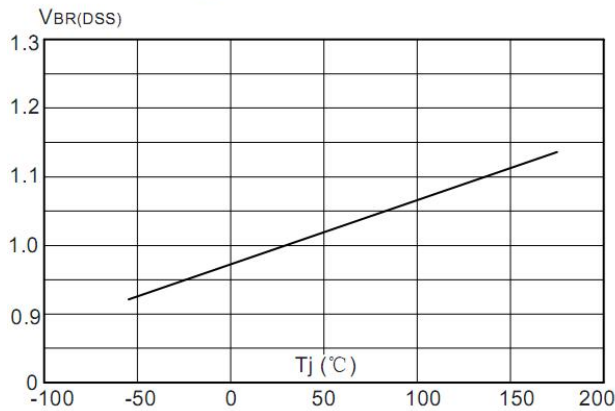
**Figure 5: Gate Charge Characteristics**



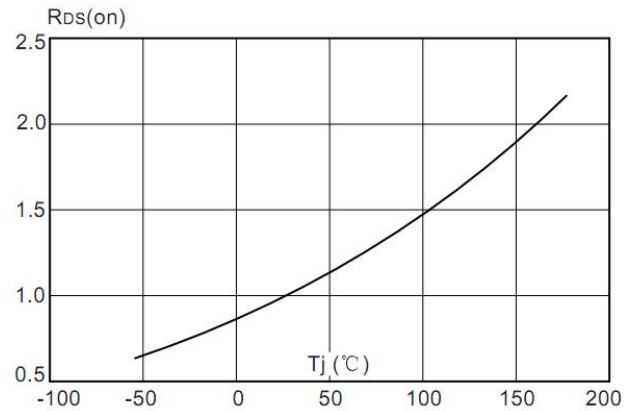
**Figure 6: Capacitance Characteristics**



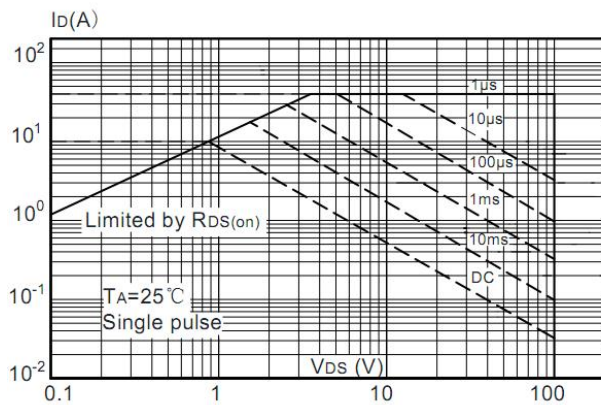
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



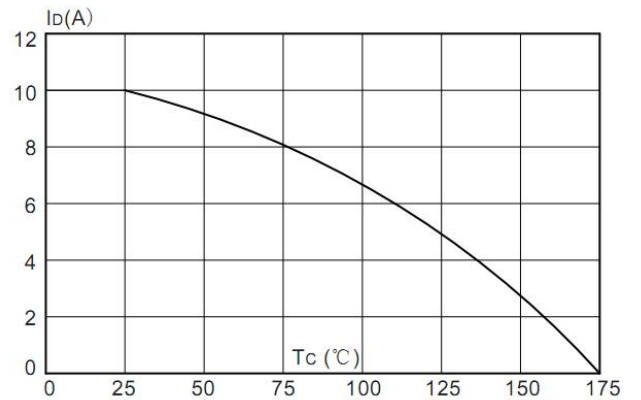
**Figure 8:** Normalized on Resistance vs. Junction Temperature



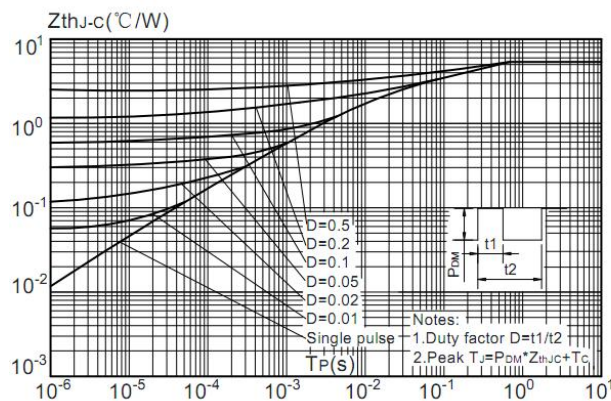
**Figure 9:** Maximum Safe Operating Area



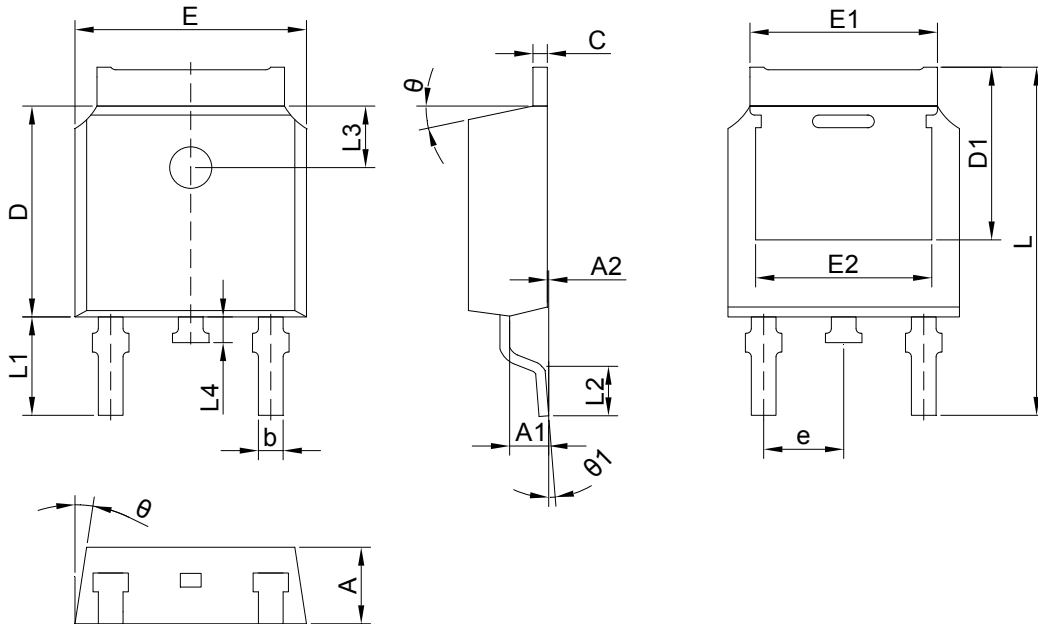
**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature



**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case



8. Dimension (TO-252)



COMMON DIMENSIONS CUNITS MEASURE=MILLIMETER							
SYMBOL	MIN	TYP	MAX	SYMBOL	MIN	TYP	MAX
A	2.10	2.30	2.50	E2	4.63	4.83	5.03
A1	0.97	1.07	1.17	L	9.90	10.10	10.30
A2	0.00	-	0.12	L1	2.74	2.94	3.14
b	0.66	0.76	0.86	L2	1.40	1.50	1.70
C	0.45	0.51	0.60	L3	1.65	1.80	1.95
D	5.90	6.10	6.30	L4	0.60	0.80	1.00
D1	5.10	5.30	5.45	e	2.286 BSC		
E	6.40	6.60	6.80	theta	5°	7°	10°
E1	5.10	5.33	5.45	theta1	0°	-	3°

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