

SuperMOS –SOT-723 60V BV_{DSS} , $1.8\Omega R_{DS(ON)}$, N-channel MOSFET

1. Description

The 2SK3541 is N-Channel enhancement MOS Field Effect Transistor. Uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. Device is suitable for use in DC-DC conversion, power switch and charging circuit. Standard Product 2SK3541 is Pb-free.

2. Features

- 60V, $R_{DS(ON)}=1.8\Omega(\text{Typ.}) @V_{GS}=10V$
 $R_{DS(ON)}=2.0\Omega(\text{Typ.}) @V_{GS}=4.5V$
- High density cell design for low $R_{DS(on)}$
- ESD Protection - HBM Class: 2
- Material: Halogen free
- Reliable and rugged
- Avalanche Rated
- Low leakage current

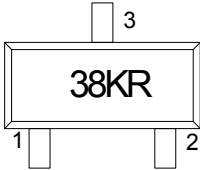
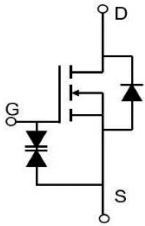
3. Applications

- PWM applications
- Load switch
- Power management in portable/desktop PCs
- DC/DC conversion

4. Ordering Information

Part Number	Package	Marking	Material	Packing	Quantity per Reel	Flammability Rating	Reel Sizes
2SK3541	SOT-723	38KR	Halogen free	Tape & Reel	8,000 PCS	UL 94V-0	7 inches

5. Pin Configuration and Functions

Pin	Function	Outline	Circuit Diagram
1	Gate		
2	Source		
3	Drain		

6. Specification

Absolute Maximum Rating & Thermal Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	BV_{DSS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	$T_A=25^\circ\text{C}$	0.3
		$T_A=100^\circ\text{C}$	0.2
Maximum Power Dissipation	P_D	0.35	W
Pulsed Drain Current	I_{DM}	1.2	A
Operating Junction Temperature	T_J	150	$^\circ\text{C}$
Lead Temperature	T_L	260	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-55 to 150	$^\circ\text{C}$

Thermal resistance ratings

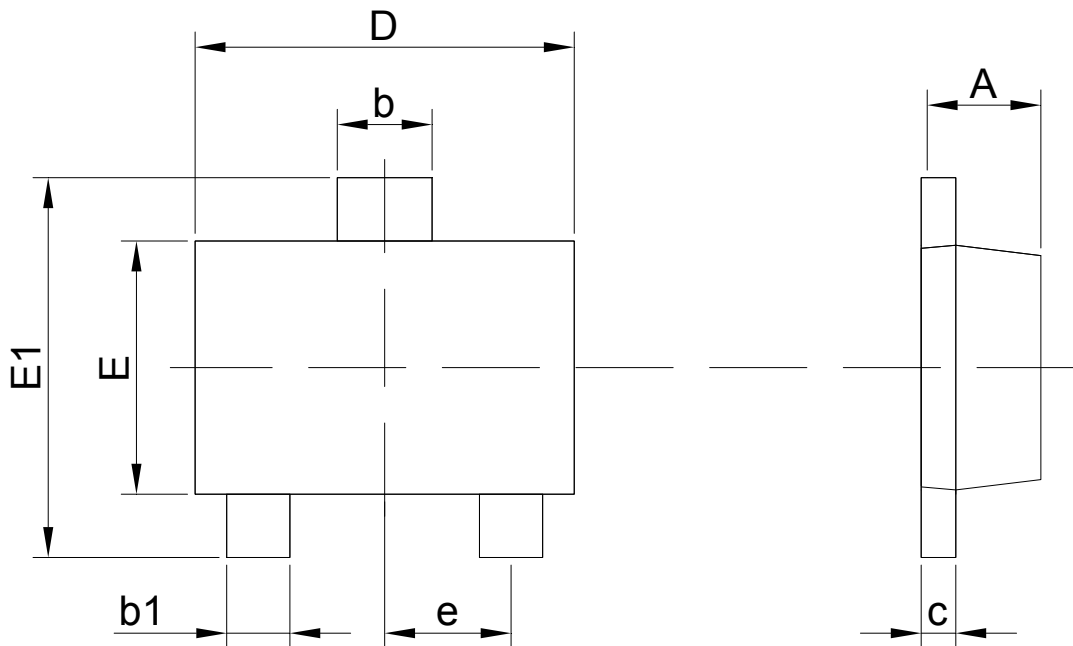
Single Operation				
Parameter	Symbol	Typical	Maximum	Unit
Junction-to-Ambient Thermal Resistance ($t \leq 10\text{s}$)	$R_{\theta JA}$		357	$^\circ\text{C/W}$

Electrical Characteristics

At TA = 25°C unless otherwise specified

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	60			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=60V, V_{GS}=0V$			1	μA
Gate-to-source Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			± 10	μA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	0.7	1.1	1.5	V
Drain-to-source On-resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=0.3A$		1.8	2.2	Ω
		$V_{GS}=4.5V, I_D=0.2A$		2.0	3.0	
CHARGES, CAPACITANCES AND GATE RESISTANCE						
Input Capacitance	C_{ISS}	$V_{GS}=0V, f=1MHz, V_{DS}=25V$		22		pF
Output Capacitance	C_{OSS}			3.3		
Reverse Transfer Capacitance	C_{RSS}			2.3		
Total Gate Charge	$Q_{G(TOT)}$	$V_{GS}=4.5V, V_{DS}=10V, I_D=0.3A$		1.6		nC
Gate-to-Source Charge	Q_{GS}			0.2		
Gate-to-Drain Charge	Q_{GD}			0.5		
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	$t_{d(ON)}$	$V_{GS}=10V, V_{DS}=10V, I_D=0.2A, R_G=10\Omega$		2		ns
Rise Time	t_r			14		
Turn-Off Delay Time	$t_{d(OFF)}$			6		
Fall Time	t_f			19		
BODY DIODE CHARACTERISTICS						
Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=0.3A$			1.5	V

7. Dimension (SOT-723)



Dimensions in Millimeters					
Symbol	Min.	Max.	Symbol	Min.	Max.
A	0.37	0.50	D	1.10	1.30
b	0.20	0.30	E	0.70	0.90
b1	0.15	0.25	E1	1.05	1.35
c	0.06	0.16	e	0.400REF	

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