

General Description

These P-Channel enhancement mode power field effect transistors use advanced trench technology and design to provide excellent RDS(ON) . This device is suitable for use as a load switch or in PWM applications.

Features

- Lower On-resistance
- 100% EAS Guaranteed
- Simple Drive Requirement
- RoHS Compliant

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	±20	V
$I_D@T_C=25^{\circ}C$	Continuous Drain Current	-150	A
$I_D@T_C=100^{\circ}C$	Continuous Drain Current	-100	A
I_{DM}	Pulsed Drain Current	-450	A
EAS	Single Pulse Avalanche Energy ¹	722	mJ
$P_D@T_C=25^{\circ}C$	Total Power Dissipation	140	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient	---	50	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-case	---	0.89	°C/W

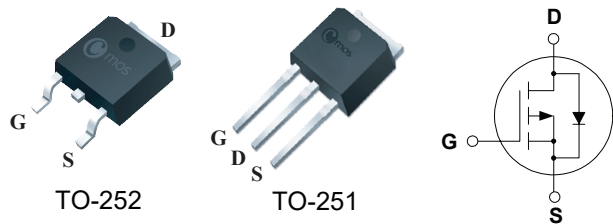
Product Summary

BVDSS	R _{DS(on)} max.	ID
-30V	2.8mΩ	-150A

Applications

- DC-DC Converters
- Load Switches
- BLDC Motor driver

TO-252 / 251 Pin Configuration



Type	Package	Marking
CMD180P03	TO-252	CMD180P03
CMU180P03	TO-251	CMU180P03

Electrical Characteristics (T_J=25°C , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-30	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-10V , I _D =-20A	---	2.5	2.8	mΩ
		V _{GS} =-4.5V , I _D =-15A	---	3.1	3.5	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-1.0	---	-2.2	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-30V, V _{GS} =0V , T _J =25°C	---	---	-1	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±20V , V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =-5V , I _D =-10A	---	55	---	S
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz	---	19	---	Ω
Q _g	Total Gate Charge	V _{DD} =-15V , I _D =-20A V _{GS} =-10V	---	163	---	nC
Q _{gs}	Gate-Source Charge		---	22	---	
Q _{gd}	Gate-Drain Charge		---	33	---	
T _{d(on)}	Turn-On Delay Time	V _{DS} =-15V, V _{GS} =-10V , R _{GEN} =3Ω R _L =0.75Ω	---	13	---	ns
T _r	Rise Time		---	18	---	
T _{d(off)}	Turn-Off Delay Time		---	135	---	
T _f	Fall Time		---	52	---	
C _{iss}	Input Capacitance	V _{DS} =-25V, V _{GS} =0V , f=1MHz	---	6900	---	pF
C _{oss}	Output Capacitance		---	950	---	
C _{rss}	Reverse Transfer Capacitance		---	630	---	

Diode Characteristics

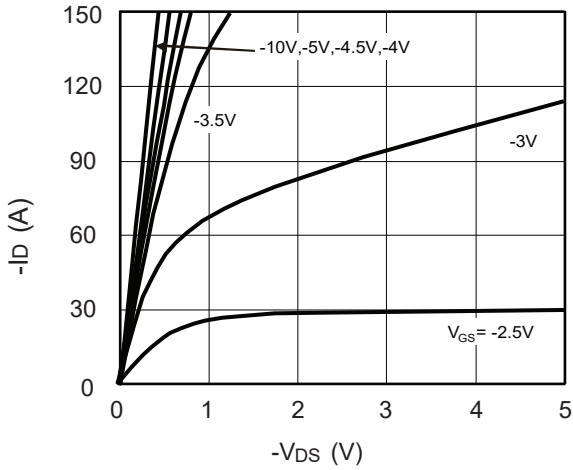
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V , Force Current	---	---	-150	A
I _{SM}	Pulsed Source Current		---	---	-450	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _F =-15A	---	-0.77	-1.2	V

Note :

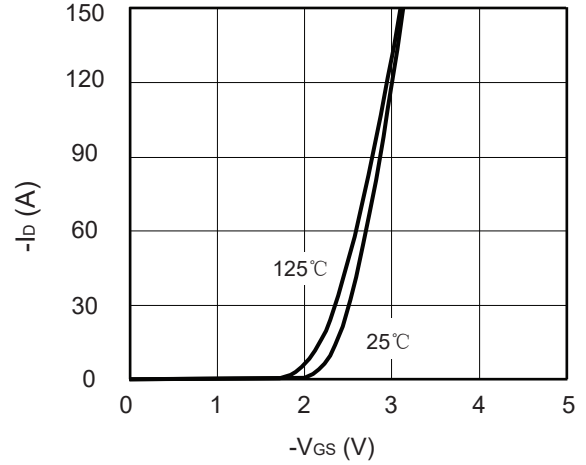
1.The EAS data shows Max. rating . The test condition is V_{DD}=-30V , V_{GS}=-10V , L=1mH , I_{AS}=-38A.

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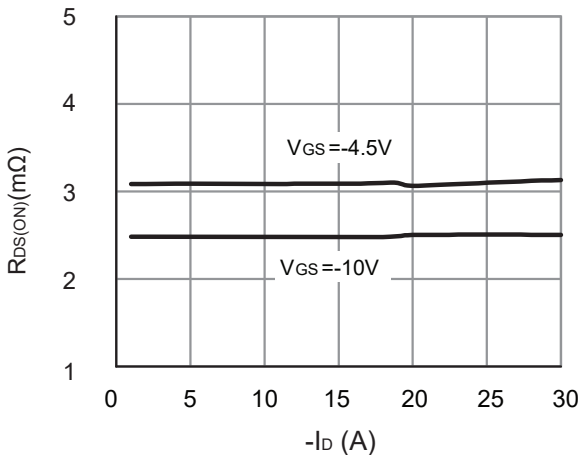
Typical Characteristics



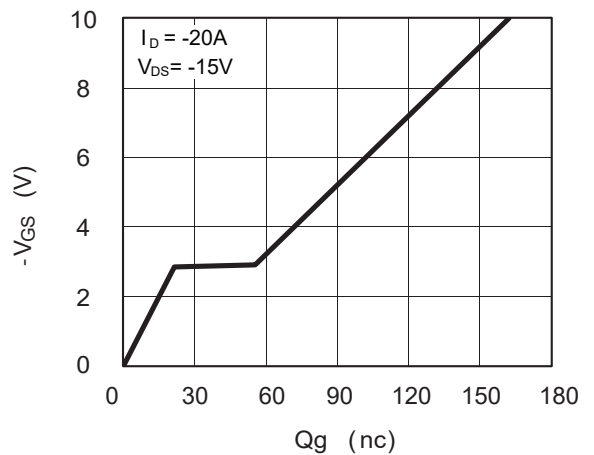
On-Region Characteristics



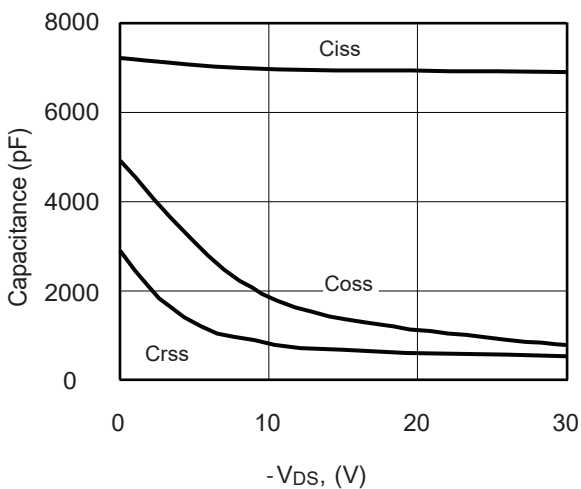
transfer characteristics



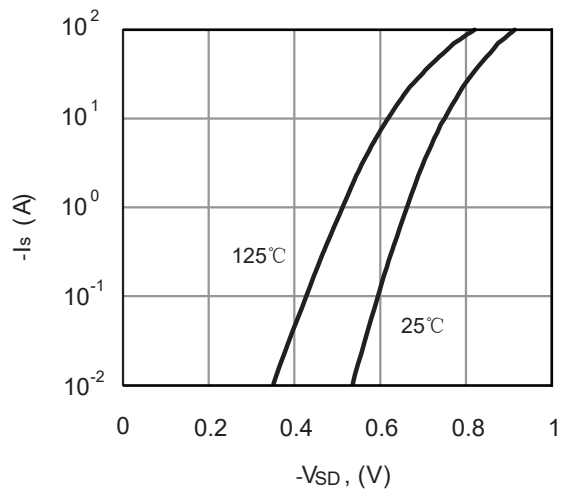
On-Resistance vs. Drain Current



Gate Charge Characteristics



Capacitance Characteristics



Body Diode Forward Voltage Variation with Source Current and Temperature