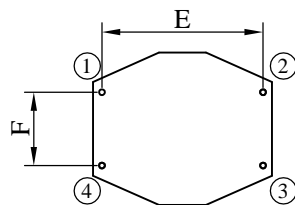
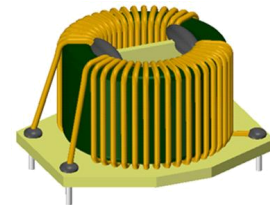
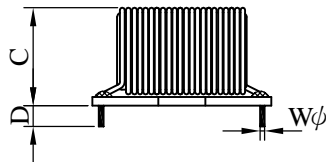
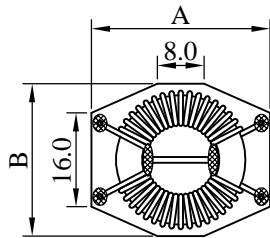


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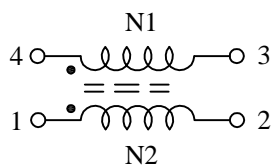
I . Configuration and dimensions :



Unit : mm

A	B	C	D	E	F	Wφ
31.00 max.	26.50 max.	18.50 max.	3.50 ±0.5	27.50 ±0.5	12.50±0.5	0.80 ±0.1

II . Schematic Diagram :



III . Description :

- a . Mn-Zn toroidal core construction.
- b . Enamelled copper wire : F class
- c . Product weight : 22.50 g (ref.)
- d . Moisture sensitivity Level 1
- e . Products comply with RoHS' requirements
- f . Halogen free

IV . General specification :

- a . Operating temp. : -40°C ---- +125°C
(Temp. rise included)

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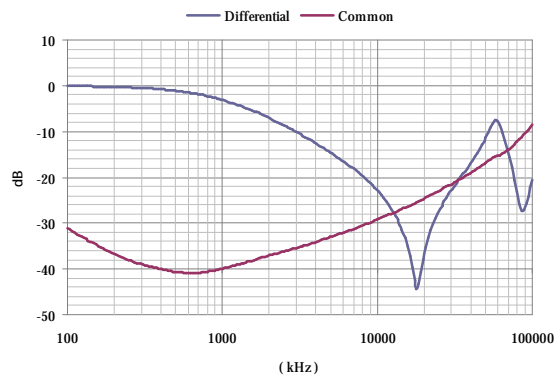
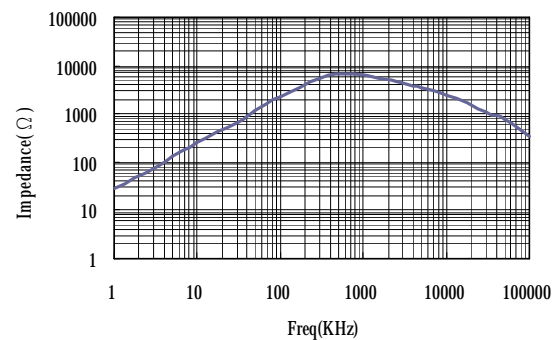
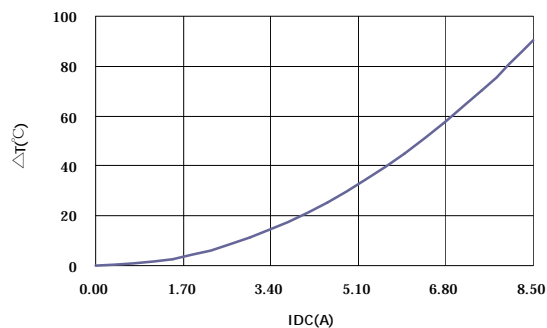
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V . Electrical characteristics :

DWG. No.	Inductance (mH)	Test Freq. (Hz)	RDC (mΩ) max.	Irms (A) typ.
TF3118332Y2AP01-□□□	3.30 ±30%	10k / 0.1V	30.0	5.0

- 1). Electrical specifications at 25°C
- 2). I rms base on temp. rise 40°C typ.
- 3). Hi-Pot Test N1 to N2 : AC 1.5 kV / 50Hz / 5mA / 2 Sec.

VI . Curve :



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VII . Drawing number expression :

The diagram illustrates a 16-bit code structure. The bits are represented by boxes, some containing values (T, F, 3, 1, 1, 8) and others being empty. The code is divided into sections by horizontal lines. Labels on the right side of the diagram point to specific bit positions:

- Type code: Points to the first bit (T).
- Dimension code: Points to the sixth bit (8).
- Electrical code: Points to the eighth bit.
- Tolerance code: Points to the ninth bit.
- Appendix code 1: Points to the tenth bit.
- Appendix code 2: Points to the eleventh bit.
- Reference code 1: Points to the twelfth bit.
- Reference code 2: Points to the thirteenth bit.

Appendix code 1 : Product Classification

Appendix code 2 : Package Information

Code	Inner package	Cover tape	Carrier tape	Bag	Package Q'TY	Remark
A	T.B.D	NA	NA	NA	T.B.D	

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VIII . Reliability test :

Item	Reference documents	Test Condition	Test Specification
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 125±2°C 2.Time:96±2 hours.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
2.Temperature Cycling	JESD22-A 104	1.Temperature: -40°C ~ +125°C 2.Number of cycle:100 cycle 3.Dwell time:30 minutes	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
3.Biased Humidity Test	MIL-STD-202 Method 103	1.Temperature : 85±2 °C 2.Humidity: 85% RH. 3.Time:96±2 Hours	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
4.Operational Life	JESD22-A 108	1.Temperature: 125°C(Temp. rise included) 2.Time:96±2 hours. 3.Rated current : 5.0 A	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
5.External Visual	JESD22-B 101 & MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.
6.Physical Dimensions	JESD22-B 100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for 3 cycles.	1.No body change in apperance. 2.No marking blurred. 3.Inductance shall not change more than ±50%.
8.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitued :10-2000-10 Hz 2.Sweep time : 20 min 3.Acceleration : 5g 4.Direction : X , Y , Z 5.Number of sweep : 6 time/axis	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
9.Resistance To Soldering Heat Test	MIL-STD-202 Method 210	1.Method : Dip 2.Temperature : 260±5°C 3.Time : 10 second. 4.Number of times : 3 times.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
10.Over load	JIS C 6436 & User SPEC.	1.Applied one and half rated current for a period of 5 minutes. 2.Rated current : 5.0 A	No electrical or mechanical damage
11.Temperature Rise Current	JIS C 6436 & User SPEC.	1.Applied rated current for 10 minutes. 2.Temperature measure by digital surface thermometer. 3.Irms current : 5.0 A	Surface temperature rise is less than 40°C typ.
12.Solderability Test	J-STD-002 & JESD22-B 102	1.Baking in pre-testing : 150±5°C / 16Hours±30 min. 2.Dip pads in flux then dip in solder pot at 240±5°C for 5 senconds.	More than 95% soldering coverage min on terminations.
13.Electrical Characteriazation	MIL-STD-202 Method 304 & User SPEC.	1.Operating temperature : -40°C~125°C 2.Room temperature : 25°C.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
14.Withstanding Voltage Test	MIL-STD-202 Method 301 & User SPEC.	1.DC: 1.5k V (Winding to Winding) 2.Time : 2 sec.	1.During the test no breakdown. 2.No mechanical or electrical damage.
15.Drop	CNS-C6354 & GB/T 2423.8	1.Products shall be mounted on SPEC. PCB and dropped down from a height of 1m 2.Drop total time : 6 times (Every side of sample drop 2 times)	1. Adhesion on PCB shall be enough. 2. Product appearance shall not break. 3. No electrical damage.
16.Terminal Strength Test	MIL-STD-202 Method 211	1.Apply pull force to samples of terminals 2.Force of 910g for 60±1 seconds.	After test, inductors shall be no mechanical damage.

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IX . Safety notes :

1. Products do not be used in related applications that directly affect the personal safety system or cause significant impacts and losses on society. If you apply to these applications, please be sure to contact us at first to confirm.
產品不可使用於直接影響人身安全系統或對社會會造成重大影響與損失之相關用途，若您應用於此，請務必優先與我們聯繫確認。
2. The storage period is less than 12 months. Ensure to follow the storage conditions (Temperature: 5 to 30°C, Humidity: 10 to 60% RH or less). If the storage time is exceeded the limit, the electrodes might be deteriorate of terminal soldering.
儲存期不超過12個月，務必遵守儲存條件（溫度：5至30°C，濕度：10至60%RH以下）。如果超過了儲存時間，端子電極可能會氧化而影響焊接。
3. Do not use or store in locations where there are conditions such as gas corrosion (salt, acid, alkali, etc.).
不要在有氣體腐蝕等條件的地方使用或存放（鹽，酸，鹼等）。
4. Soldering corrections after mounting should be within the range of the conditions determined in the specifications. If overheated, a short circuit, performance deterioration, or lifespan shortening may occur.
安裝時的焊接條件應在規格範圍內。如果超過要求，可能會發生短路，性能下降或壽命縮短。
5. When using, it should try to avoid excessive mechanical impact on the product, such as collision / drop ... and other reasons.
使用時，應盡量避免產品受到過度機械衝擊，如碰撞/掉落...等原因。
6. When embedding a printed circuit board where a chip is mounted to a set, be sure that residual stress is not given to the chip due to the overall distortion of the printed circuit board and partial distortion such as at screw tightening portions.
將已安裝新片的電路板組裝到裝置時，請注意應盡量避免電路板受到組裝變形...等，導致產品受到應力。
7. Self heating (temperature increase) occurs when the power is turned ON, so the tolerance should be sufficient for the set thermal design.
產品會因通電而自我發熱(溫度上升)，因此在熱影響設計方面，需保留適當公差。
8. Do not expose the products to magnets or magnetic fields.
請勿將產品暴露於磁鐵或是磁場中。
9. If you would like to use this products to performance and/or quality require a more stringent level of safety or reliability, or whose failure, malfunction or trouble could cause serious damage to society, person or property, or if you have special requirements exceeding the range or conditions set forth in the each catalog, please contact us.
如果您希望將此產品用於性能和/或質量要求更嚴格的安全性或可靠性，或其失敗、故障或麻煩可能對社會、個人或財產造成嚴重損害，或者您有特殊情況要求超出目錄中規定的範圍或條件，請與我們聯繫。

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