



## Description

The IRF7316TRPBF uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.



SOP-8

$V_{DS} = -30V, I_D = -5.3A$

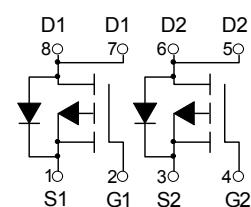
$R_{DS(ON)} < 42m\Omega @ V_{GS}=-10V$

$R_{DS(ON)} < 85m\Omega @ V_{GS}=-4.5V$

## Application

PWM application

Load switch



Dual P-Channel MOSFET

## Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
IRF7316TRPBF	SOP-8	HXY MOSFET	3000

## Absolute Maximum Ratings ( $T_A=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Limit	Unit
$V_{DS}$	Drain-Source Voltage	-30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current-Continuous	-5.3	A
$I_{DM}$	Drain Current-Pulsed (Note 1)	-20	A
$P_D$	Maximum Power Dissipation	2.6	W
$T_J, T_{STG}$	Operating Junction and Storage Temperature Range	-55 To 150	°C
$R_{\theta JA}$	Thermal Resistance,Junction-to-Ambient (Note 2)	49	°C/W



**Electrical Characteristics ( $T_A=25^\circ\text{C}$  unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-30	-33	-	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-24\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	$\mu\text{A}$
Gate-Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm100$	nA
<b>On Characteristics</b> (Note 3)						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1	-1.6	-3	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-5.3\text{A}$	-	35	42	mR
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-4.2\text{A}$	-	70	85	mR
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-4.5\text{A}$	4	7	-	S
<b>Dynamic Characteristics</b> (Note 4)						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	540	-	PF
Output Capacitance	$C_{\text{oss}}$		-	150	-	PF
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	75	-	PF
<b>Switching Characteristics</b> (Note 4)						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=-15\text{V}, I_{\text{D}}=-1\text{A}, V_{\text{GS}}=-10\text{V}, R_{\text{GEN}}=6$	-	8	-	nS
Turn-on Rise Time	$t_r$		-	14	-	nS
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	18	-	nS
Turn-Off Fall Time	$t_f$		-	10	-	nS
Total Gate Charge	$Q_g$	$V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-5.3\text{A}, V_{\text{GS}}=-10\text{V}$	-	12	-	nC
Gate-Source Charge	$Q_{\text{gs}}$		-	2.4	-	nC
Gate-Drain Charge	$Q_{\text{gd}}$		-	3.2	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=-5.3\text{A}$	-	-	-1.2	V

**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production



### Typical Electrical and Thermal Characteristics

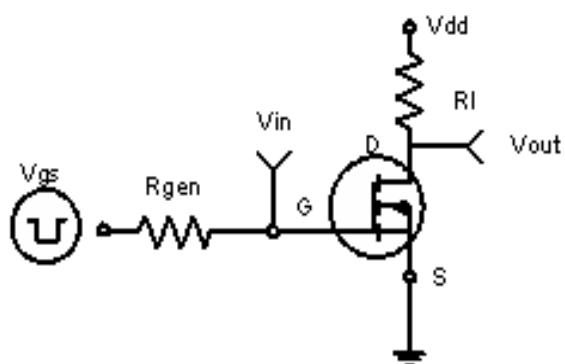


Figure 1:Switching Test Circuit

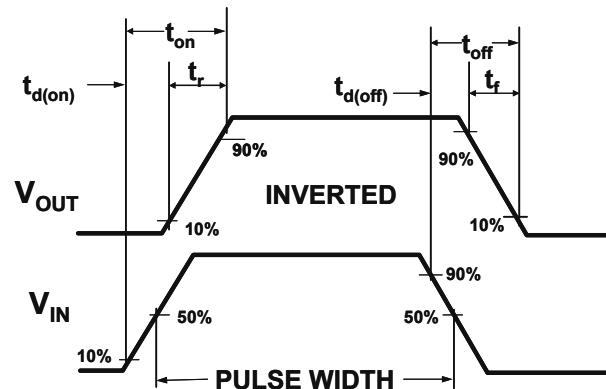


Figure 2:Switching Waveforms

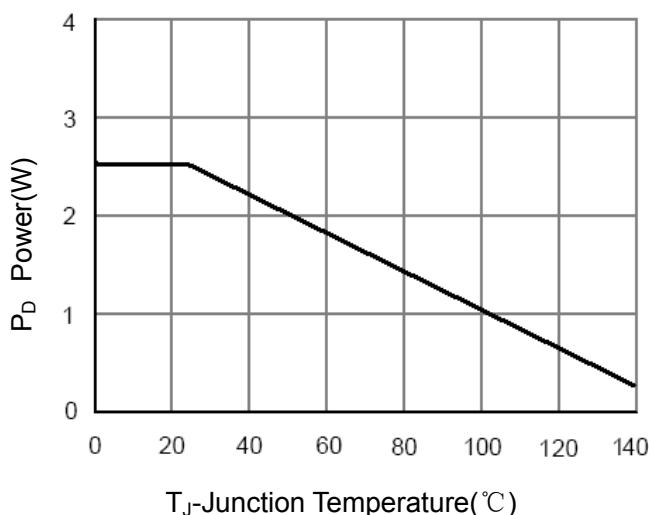


Figure 3 Power Dissipation

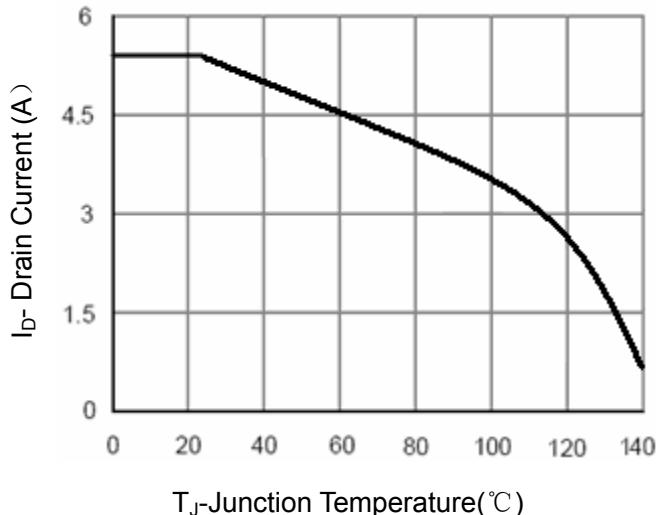


Figure 4 Drain Current

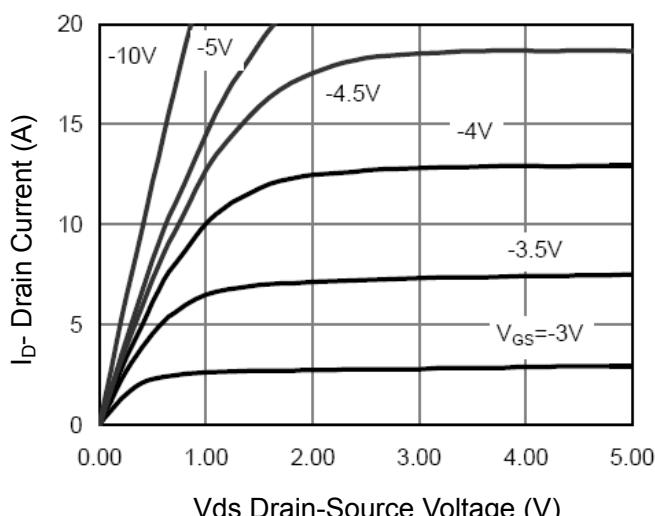


Figure 5 Output Characteristics

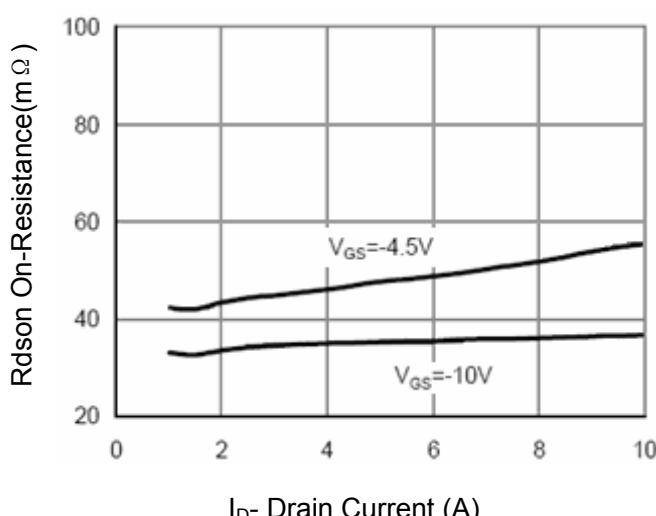


Figure 6 Drain-Source On-Resistance

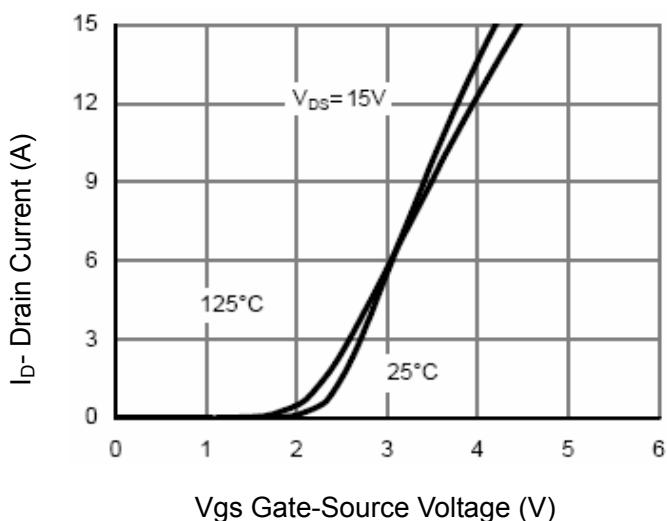


Figure 7 Transfer Characteristics

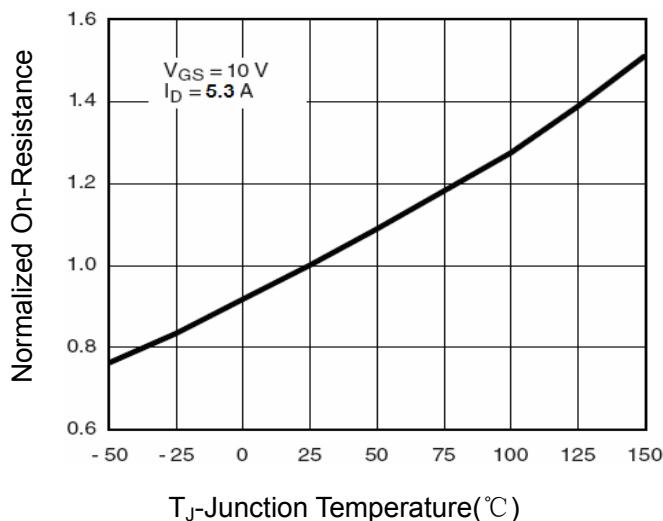


Figure 8 Drain-Source On-Resistance

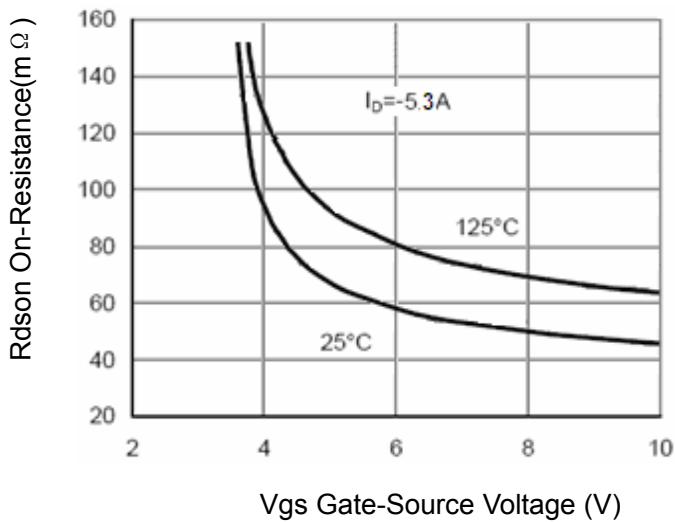


Figure 9  $R_{DS(on)}$  vs  $V_{GS}$

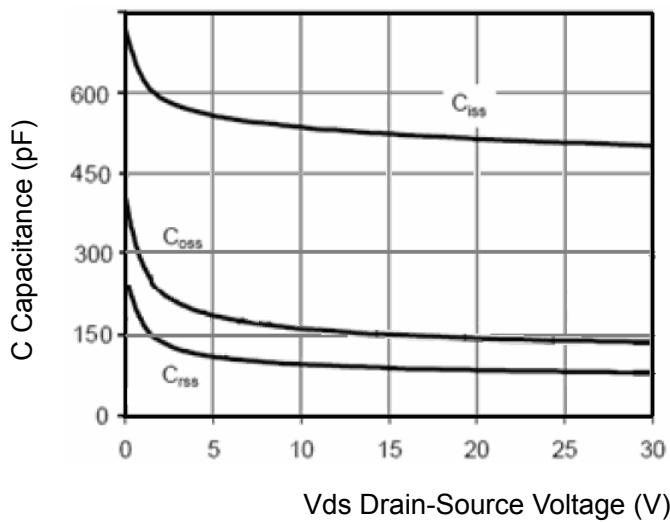


Figure 10 Capacitance vs  $V_{DS}$

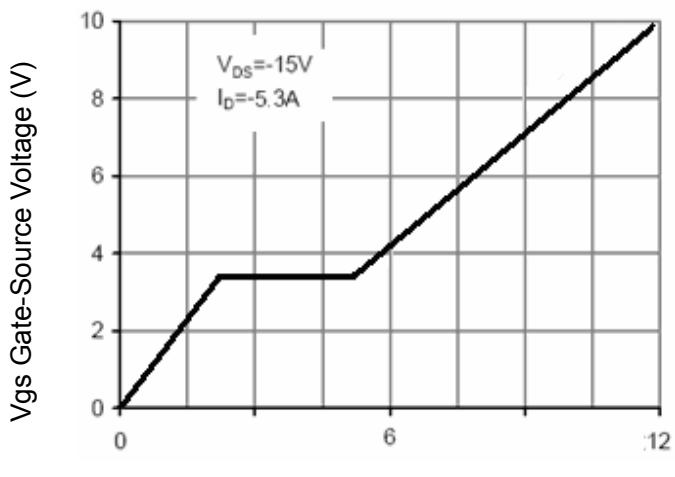


Figure 11 Gate Charge

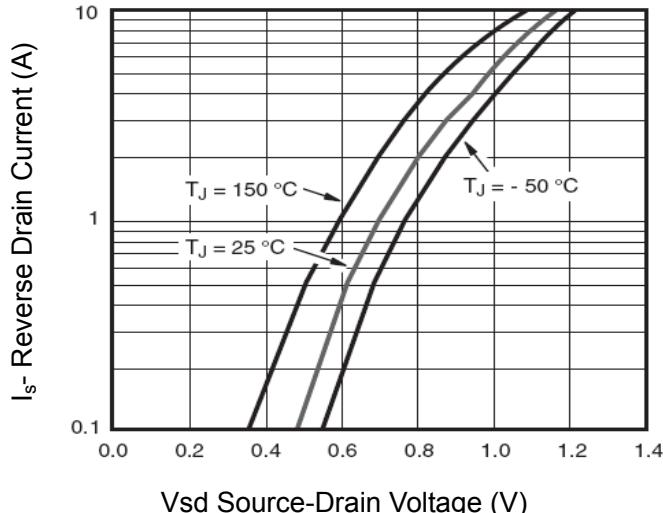
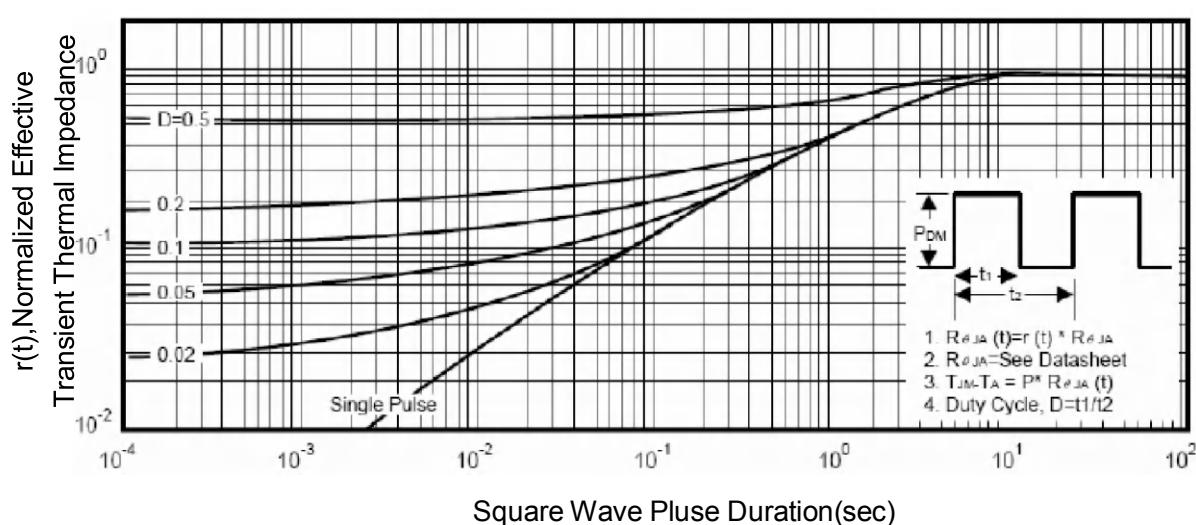
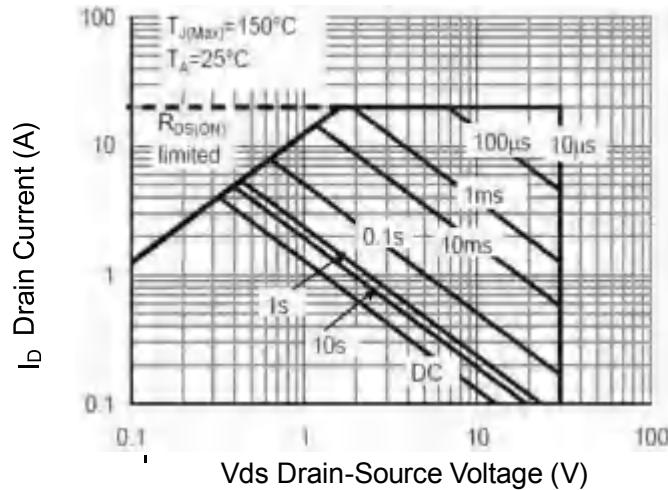
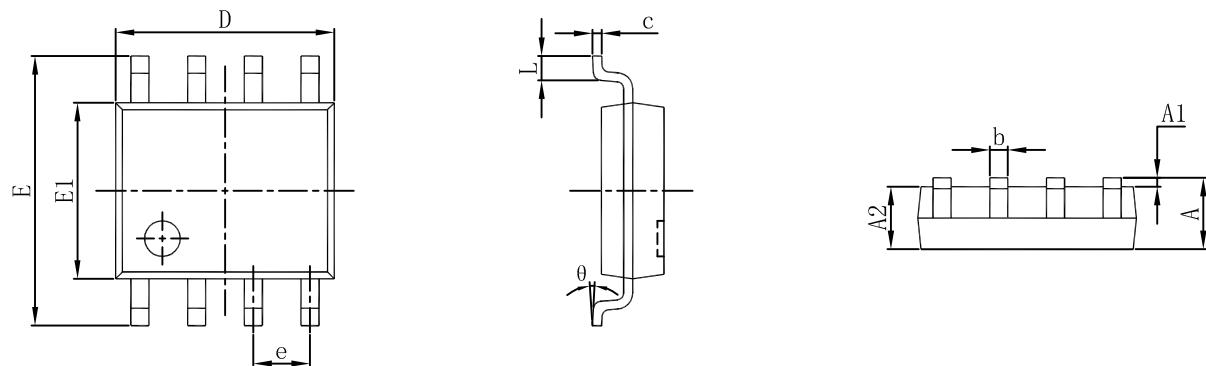


Figure 12 Source-Drain Diode Forward

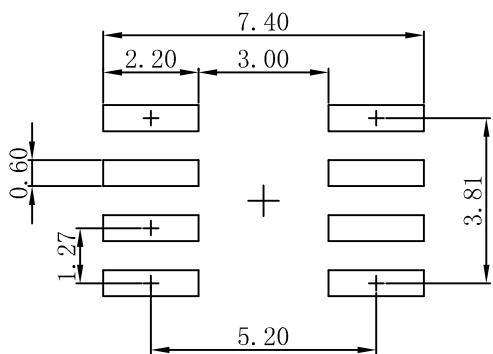




### SOP-8 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.800	5.000	0.189	0.197
e	1.270 (BSC)		0.050 (BSC)	
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
$\theta$	0°	8°	0°	8°



Note:  
1. Controlling dimension: in millimeters.  
2. General tolerance:  $\pm 0.05$ mm.  
3. The pad layout is for reference purposes only.



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