

N-Channel 60 V (D-S) 175 °C MOSFET

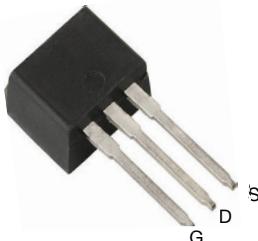
PRODUCT SUMMARY	
V_{DS} (V)	60
$R_{DS(on)}$ (Ω) at $V_{GS} = 10$ V	0.006
I_D (A)	120
Configuration	Single
Package	TO-262

FEATURES

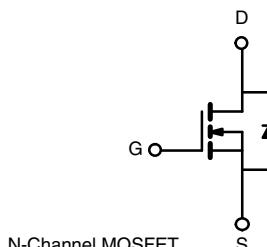
- TrenchFET® power MOSFET
- Package with low thermal resistance
- AEC-Q101 qualified ^d
- 100 % R_g and UIS tested



TO-262



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current	I_D	120	A
		80	
Continuous Source Current (Diode Conduction) ^a	I_S	120	
Pulsed Drain Current ^b	I_{DM}	480	
Single Pulse Avalanche Current	I_{AS}	65	mJ
Single Pulse Avalanche Energy	E_{AS}	211	
Maximum Power Dissipation ^b	P_D	230	W
		76	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient	R_{thJA}	40	°C/W
Junction-to-Case (Drain)		0.65	

Notes

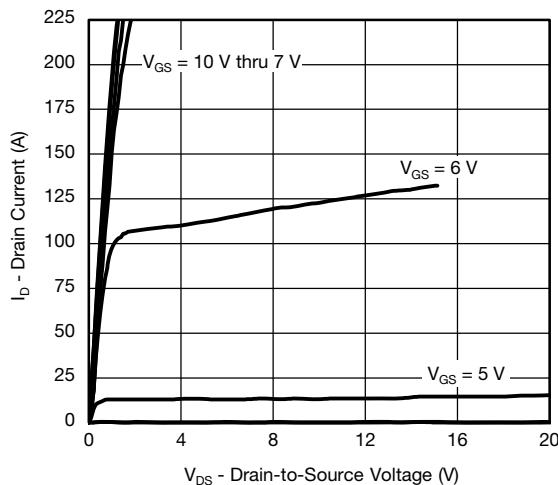
- Package limited.
- Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %.
- When mounted on 1" square PCB (FR4 material).
- Parametric verification ongoing.

SPECIFICATIONS ($T_C = 25^\circ\text{C}$, unless otherwise noted)								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static								
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0$, $I_D = 250 \mu\text{A}$		60	-	-	V	
Gate-Source Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu\text{A}$		2.5	3.0	3.5		
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}$, $V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{GS} = 0 \text{ V}$	$V_{DS} = 60 \text{ V}$	-	-	1	μA	
		$V_{GS} = 0 \text{ V}$	$V_{DS} = 60 \text{ V}$, $T_J = 125^\circ\text{C}$	-	-	50		
		$V_{GS} = 0 \text{ V}$	$V_{DS} = 60 \text{ V}$, $T_J = 175^\circ\text{C}$	-	-	250		
On-State Drain Current ^a	$I_{D(\text{on})}$	$V_{GS} = 10 \text{ V}$	$V_{DS} \geq 5 \text{ V}$	120	-	-	A	
Drain-Source On-State Resistance ^a	$R_{DS(\text{on})}$	$V_{GS} = 10 \text{ V}$	$I_D = 30 \text{ A}$	-	0.0060	-	Ω	
		$V_{GS} = 10 \text{ V}$	$I_D = 30 \text{ A}$, $T_J = 125^\circ\text{C}$	-	0.0104	-		
		$V_{GS} = 10 \text{ V}$	$I_D = 30 \text{ A}$, $T_J = 175^\circ\text{C}$	-	0.0129	-		
Forward Transconductance ^b	g_{fs}	$V_{DS} = 15 \text{ V}$, $I_D = 30 \text{ A}$		-	94	-	S	
Dynamic^b								
Input Capacitance	C_{iss}	$V_{GS} = 0 \text{ V}$	$V_{DS} = 25 \text{ V}$, $f = 1 \text{ MHz}$	-	5196	6495	pF	
Output Capacitance	C_{oss}			-	708	885		
Reverse Transfer Capacitance	C_{rss}			-	336	420		
Total Gate Charge ^c	Q_g	$V_{GS} = 10 \text{ V}$	$V_{DS} = 30 \text{ V}$, $I_D = 75 \text{ A}$	-	96.5	145	nC	
Gate-Source Charge	Q_{gs}			-	24.6	-		
Gate-Drain Charge ^c	Q_{gd}			-	27.2	-		
Gate Resistance	R_g	$f = 1 \text{ MHz}$		0.3	1	1.7	Ω	
Turn-On Delay Time ^c	$t_{d(\text{on})}$	$V_{DD} = 30 \text{ V}$, $R_L = 0.4 \Omega$ $I_D \geq 75 \text{ A}$, $V_{GEN} = 10 \text{ V}$, $R_g = 1 \Omega$		-	16	24	ns	
Rise Time ^c	t_r			-	14	21		
Turn-Off Delay Time ^c	$t_{d(\text{off})}$			-	34	51		
Fall Time ^c	t_f			-	9	14		
Source-Drain Diode Ratings and Characteristics^b								
Pulsed Current ^a	I_{SM}			-	-	480	A	
Forward Voltage	V_{SD}	$I_F = 75 \text{ A}$, $V_{GS} = 0$		-	0.9	1.5	V	

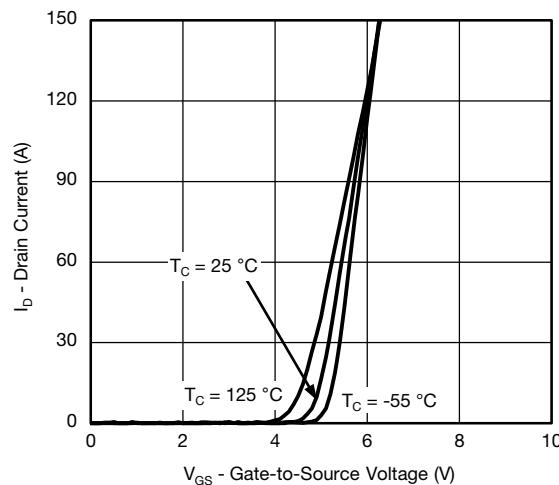
Notes

- a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2 \%$.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

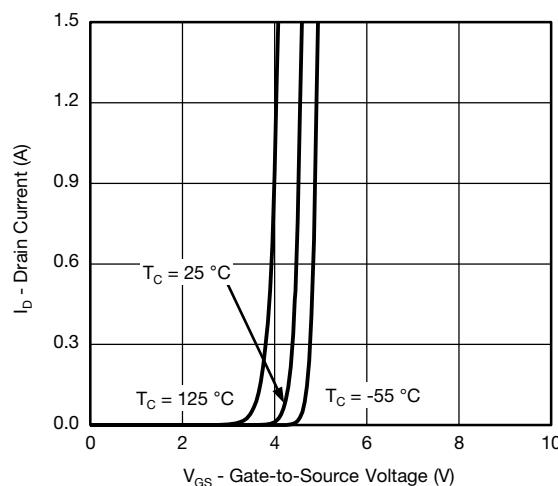
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)


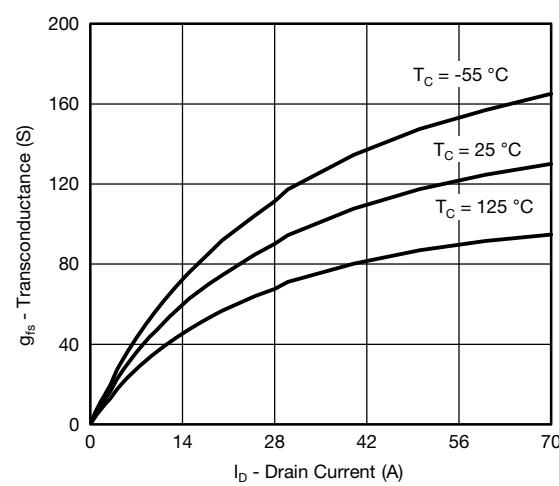
Output Characteristics



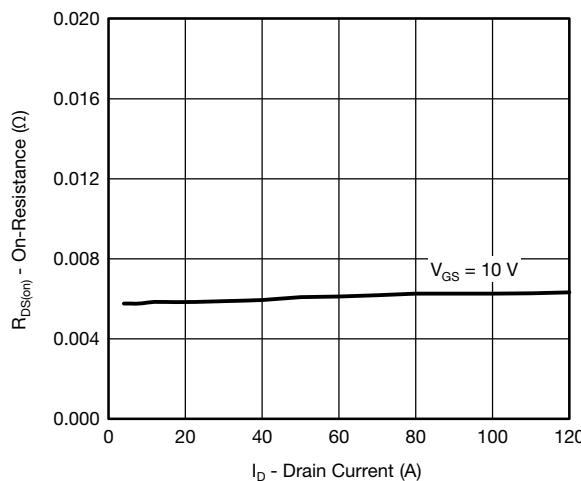
Transfer Characteristics



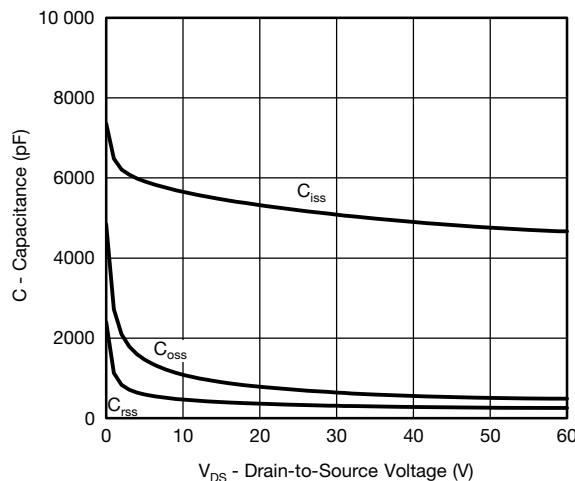
Transfer Characteristics



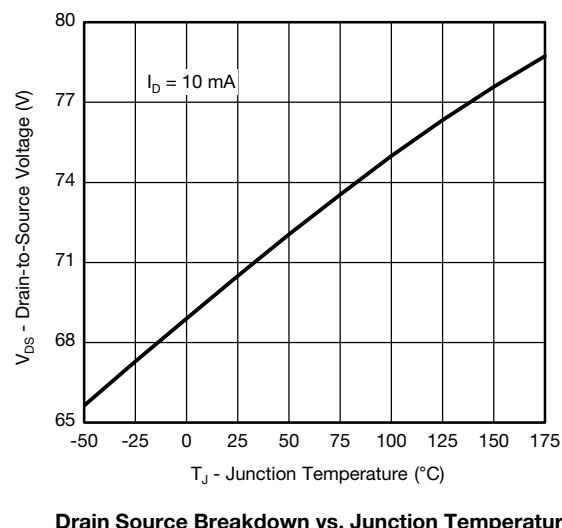
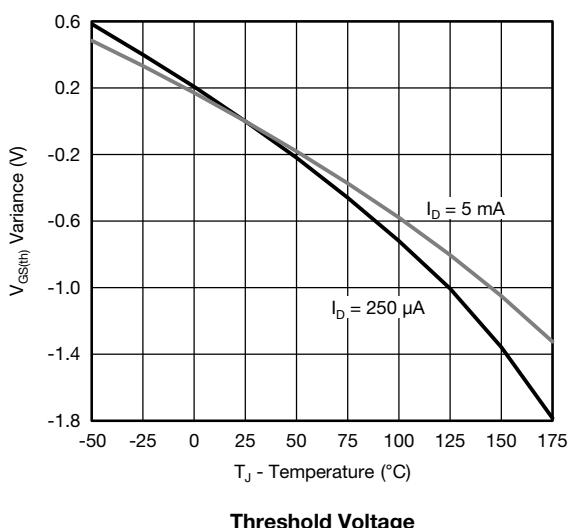
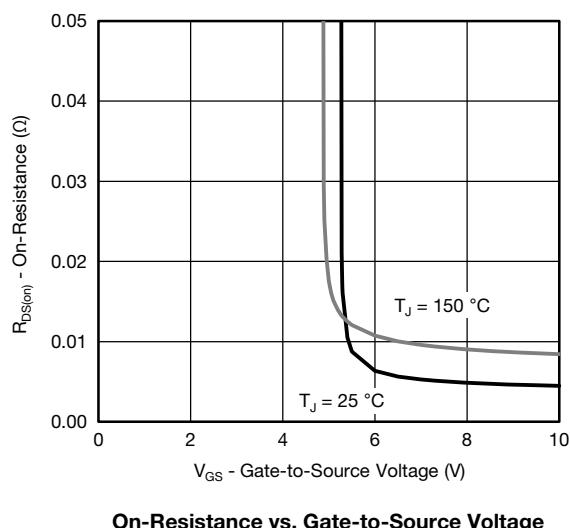
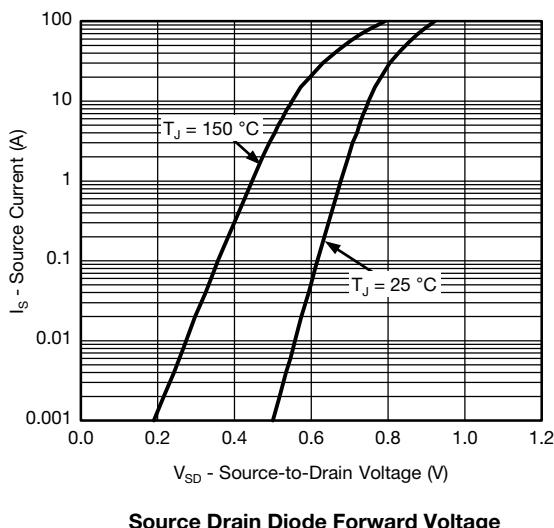
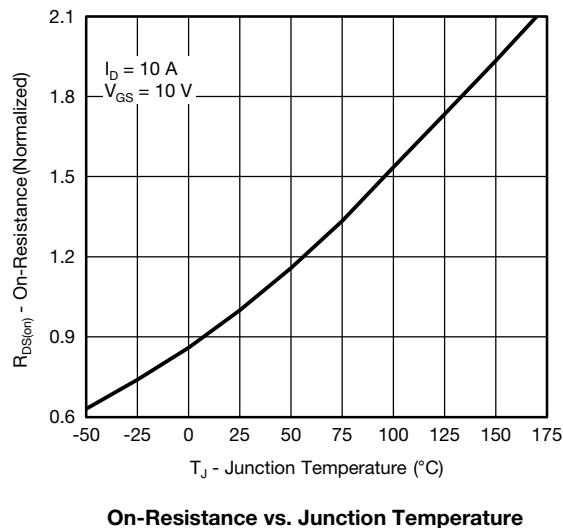
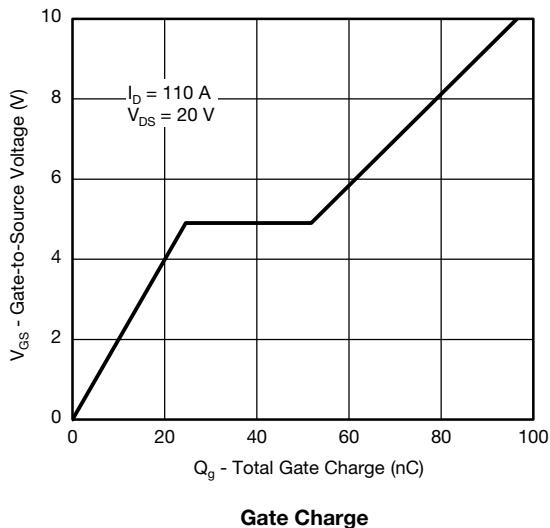
Transconductance

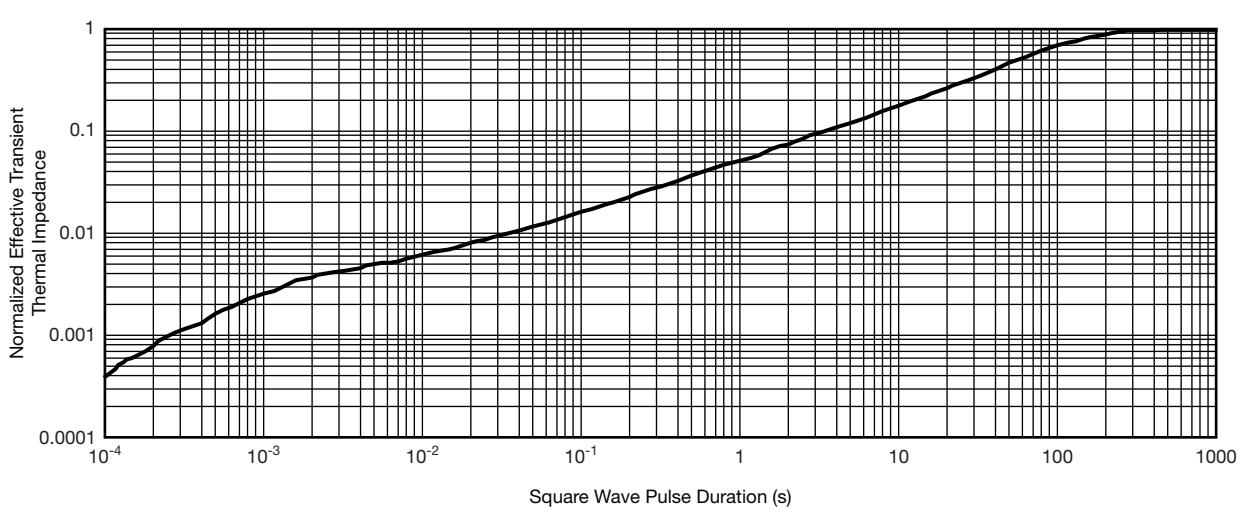
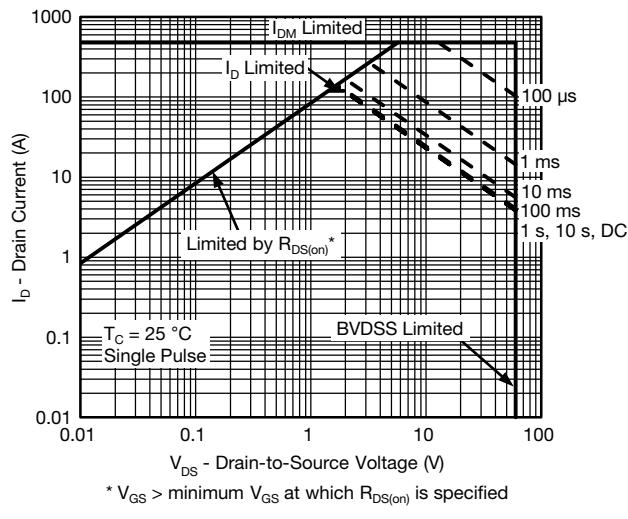


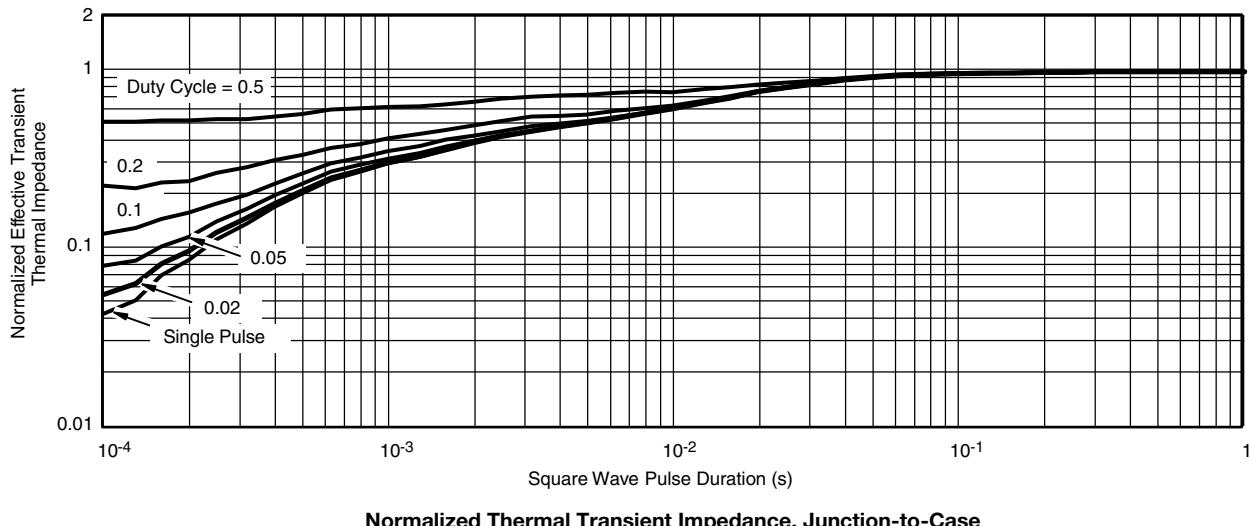
On-Resistance vs. Drain Current



Capacitance

TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)


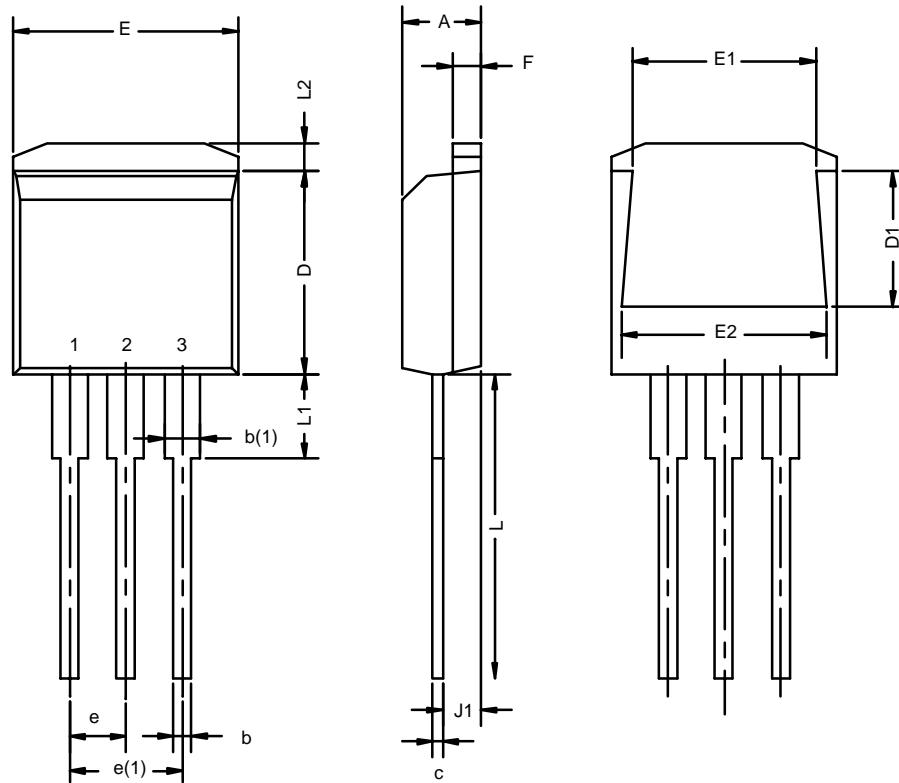
THERMAL RATINGS ($T_A = 25^\circ\text{C}$, unless otherwise noted)


THERMAL RATINGS ($T_A = 25^\circ\text{C}$, unless otherwise noted)**Note**

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25°C)
 - Normalized Transient Thermal Impedance Junction-to-Case (25°C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

TO-262: 3-LEAD



Dim	MILLIMETERS*		INCHES	
	Min	Max	Min	Max
A	4.32	4.70	0.170	0.185
b	0.64	1.00	0.025	0.039
b(1)	1.14	1.40	0.045	0.055
c	0.36	0.50	0.014	0.020
D	8.64	9.65	0.340	0.380
D1	5.59	6.10	0.220	0.240
e	2.41	2.67	0.095	0.105
e(1)	4.95	5.33	0.195	0.210
E	10.03	10.41	0.395	0.410
E1	7.87	8.64	0.310	0.340
E2	9.02	9.53	0.355	0.375
F	1.14	1.40	0.045	0.055
J1	2.41	2.79	0.095	0.110
L	13.08	14.22	0.515	0.560
L1	-	3.81	-	0.150
L2	1.02	1.40	0.040	0.055

 ECN: T-02234—Rev. C, 14-Oct-02
 DWG: 5855

*Use millimeters as the primary measurement

Disclaimer

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

Taiwan VBsemi Electronics Co., Ltd., branches, agents, employees, and all persons acting on its or their representatives (collectively, the "Taiwan VBsemi"), assumes no responsibility for any errors, inaccuracies or incomplete data contained in the table or any other any disclosure of any information related to the product.(www.VBsemi.com)

Taiwan VBsemi makes no guarantee, representation or warranty on the product for any particular purpose of any goods or continuous production. To the maximum extent permitted by applicable law on Taiwan VBsemi relinquished: (1) any application and all liability arising out of or use of any products; (2) any and all liability, including but not limited to special, consequential damages or incidental ; (3) any and all implied warranties, including a particular purpose, non-infringement and merchantability guarantee.

Statement on certain types of applications are based on knowledge of the product is often used in a typical application of the general product VBsemi Taiwan demand that the Taiwan VBsemi of. Statement on whether the product is suitable for a particular application is non-binding. It is the customer's responsibility to verify specific product features in the products described in the specification is appropriate for use in a particular application. Parameter data sheets and technical specifications can be provided may vary depending on the application and performance over time. All operating parameters, including typical parameters must be made by customer's technical experts validated for each customer application. Product specifications do not expand or modify Taiwan VBsemi purchasing terms and conditions, including but not limited to warranty herein.

Unless expressly stated in writing, Taiwan VBsemi products are not intended for use in medical, life saving, or life sustaining applications or any other application. Wherein VBsemi product failure could lead to personal injury or death, use or sale of products used in Taiwan VBsemi such applications using client did not express their own risk. Contact your authorized Taiwan VBsemi people who are related to product design applications and other terms and conditions in writing.

The information provided in this document and the company's products without a license, express or implied, by estoppel or otherwise, to any intellectual property rights granted to the VBsemi act or document. Product names and trademarks referred to herein are trademarks of their respective representatives will be all.

Material Category Policy

Taiwan VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be OHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.com)

Please note that some documents may still refer to Taiwan VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.

Taiwan VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Taiwanese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.