

N-Channel 60-V (D-S) MOSFET

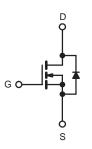
PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)			
60	0.029 at V _{GS} = 10 V	7.0			
	0.033 at V _{GS} = 4.5 V	5.6			

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFETs
- 175 °C Maximum Junction Temperature
- Compliant to RoHS Directive 2002/95/EC







N-Channel MOSFET	
------------------	--

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	60		V	
Gate-Source Voltage		V_{GS}	± 20			
Continuous Drain Current (T _J = 175 °C) ^a	T _A = 25 °C	I _D	7.0	6.0	А	
	T _A = 70 °C		6.1	5.0		
Pulsed Drain Current		I _{DM}	40		^	
Avalanche Current	I _{AS}	15				
Single Pulse Avalanche Energy		E _{AS}	11		mJ	
Maximum Power Dissipation ^a	T _A = 25 °C	P _D	3.3	1.7	W	
Maximum Power Dissipation	T _A = 70 °C	υ υ	2.3	1.2	VV	
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55	to 175	°C		

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Mariana landina to Ambient 3	t ≤ 10 s	R _{thJA}	36	45			
Maximum Junction-to-Ambient ^a	Steady State		75	90	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	17	20			

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

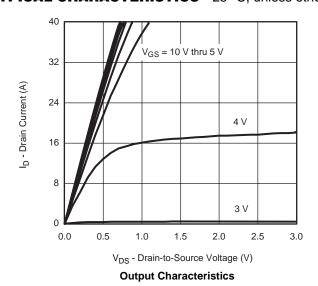


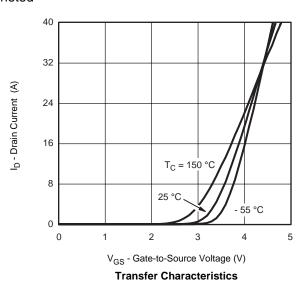
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	l l		I.	<u> </u>			
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	60			V	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1		3	V	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zara Cata Valtaga Drain Current	,	V _{DS} = 60 V, V _{GS} = 0 V			1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V, T _J = 55 °C			20		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	40			Α	
		$V_{GS} = 10 \text{ V}, I_D = 6.0 \text{ A}$		0.028			
	R _{DS(on)}	$V_{GS} = 10 \text{ V}, I_D = 6.0 \text{ A}, T_J = 125 \text{ °C}$		0.032		Ω	
Drain-Source On-State Resistance ^a		$V_{GS} = 10 \text{ V}, I_D = 6.0 \text{ A}, T_J = 175 \text{ °C}$		0.040			
		$V_{GS} = 4.5 \text{ V}, I_D = 5.1 \text{ A}$		0.033			
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, I_D = 6.0 \text{ A}$		25		S	
Diode Forward Voltage ^a	V_{SD}	I _S = 1.7 A, V _{GS} = 0 V		0.8	1.2	V	
Dynamic ^b	l l		l				
Total Gate Charge	Q_g			18	27		
Gate-Source Charge	Q_{gs} $V_{DS} = 30$	$V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 6.0 \text{ A}$		3.4		nC	
Gate-Drain Charge	Q_{gd}			5.3			
Gate Resistance	R_g	V _{GS} = 0.1 V, f = 5 MHz	0.5	1.4	2.4	Ω	
Turn-On Delay Time	t _{d(on)}			10	20		
Rise Time	t _r	V_{DD} = 30 V, R_L = 30 Ω		10	20		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ 1 A, V_{GEN} = 10 V, R_g = 6 Ω		25	50	ns	
Fall Time	t _f			12	24		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 1.7 A, dI/dt = 100 A/μs		50	80		

Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

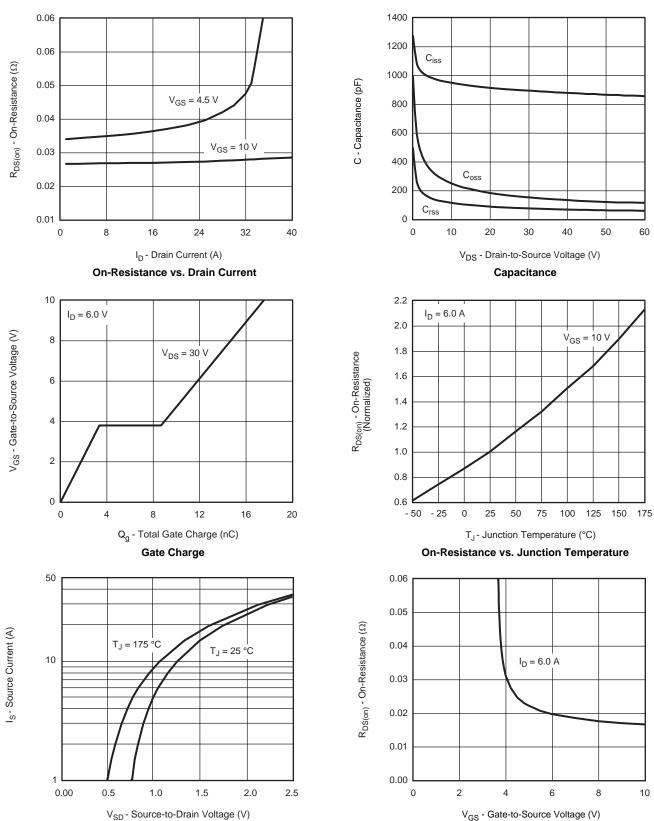
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



服务热线:400-655-8788

On-Resistance vs. Gate-to-Source Voltage

Source-Drain Diode Forward Voltage

0.1

0.05 0.02

Single Pulse

10-3

0.1

0.01

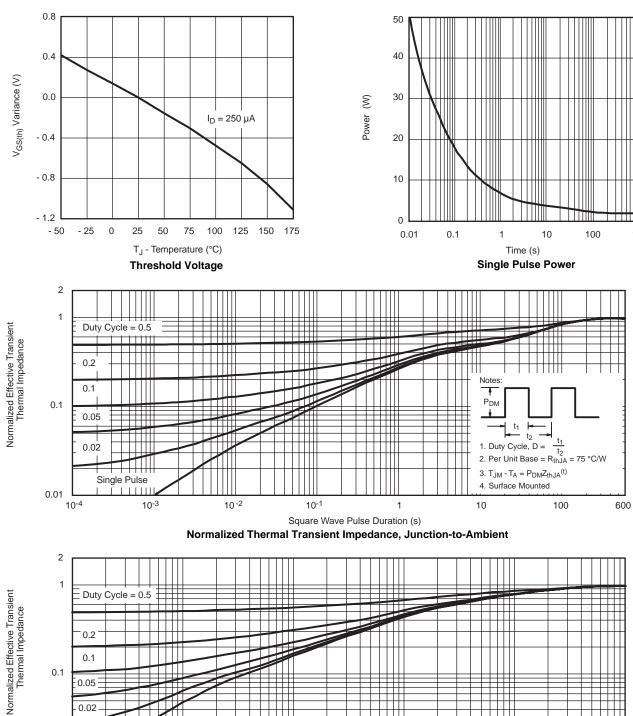
10-4



1000

10

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

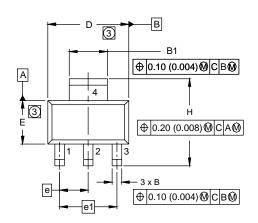


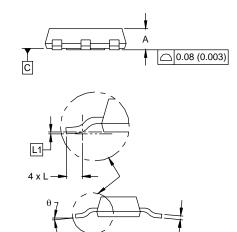
Square Wave Pulse Duration (s) Normalized Thermal Transient Impedance, Junction-to-Foot

10-1



SOT-223 (HIGH VOLTAGE)





	MILLI	METERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
А	1.55	1.80	0.061	0.071	
В	0.65	0.85	0.026	0.033	
B1	2.95	3.15	0.116	0.124	
С	0.25	0.35	0.010	0.014	
D	6.30	6.70	0.248	0.264	
Е	3.30	3.70	0.130	0.146	
е	2.30 BSC		0.0905 BSC		
e1	4.60 BSC		0.181 BSC		
Н	6.71	7.29	0.264	0.287	
L	0.91	-	0.036	-	
L1	0.061 BSC		0.0024 BSC		
θ	-	10'	-	10'	

ECN: S-82109-Rev. A, 15-Sep-08

DWG: 5969

Notes

- 1. Dimensioning and tolerancing per ASME Y14.5M-1994.
- 2. Dimensions are shown in millimeters (inches).
- 3. Dimension do not include mold flash.
- 4. Outline conforms to JEDEC outline TO-261AA.



Disclaimer

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

Taiwan VBsemi Electronics Co., Ltd., branches, agents, employees, and all persons acting on its or their representatives (collectively, the "Taiwan VBsemi"), assumes no responsibility for any errors, inaccuracies or incomplete data contained in the table or any other any disclosure of any information related to the product.(www.VBsemi.com)

Taiwan VBsemi makes no guarantee, representation or warranty on the product for any particular purpose of any goods or continuous production. To the maximum extent permitted by applicable law on Taiwan VBsemi relinquished: (1) any application and all liability arising out of or use of any products; (2) any and all liability, including but not limited to special, consequential damages or incidental; (3) any and all implied warranties, including a particular purpose, non-infringement and merchantability guarantee.

Statement on certain types of applications are based on knowledge of the product is often used in a typical application of the general product VBsemi Taiwan demand that the Taiwan VBsemi of. Statement on whether the product is suitable for a particular application is non-binding. It is the customer's responsibility to verify specific product features in the products described in the specification is appropriate for use in a particular application. Parameter data sheets and technical specifications can be provided may vary depending on the application and performance over time. All operating parameters, including typical parameters must be made by customer's technical experts validated for each customer application. Product specifications do not expand or modify Taiwan VBsemi purchasing terms and conditions, including but not limited to warranty herein.

Unless expressly stated in writing, Taiwan VBsemi products are not intended for use in medical, life saving, or life sustaining applications or any other application. Wherein VBsemi product failure could lead to personal injury or death, use or sale of products used in Taiwan VBsemi such applications using client did not express their own risk. Contact your authorized Taiwan VBsemi people who are related to product design applications and other terms and conditions in writing.

The information provided in this document and the company's products without a license, express or implied, by estoppel or otherwise, to any intellectual property rights granted to the VBsemi act or document. Product names and trademarks referred to herein are trademarks of their respective representatives will be all.

Material Category Policy

Taiwan VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be oHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.com)

Please note that some documents may still refer to Taiwan VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.

Taiwan VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Taiwanese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.