

CMSA150N03L

N-Channel Enhancement Mode Field Effect Transistor

General Description

The CMSA150N03L uses advanced technology to provide excellent RDS (ON) . This device is suitable to be used as the low side FET in SMPS, load switching and general purpose.

Features

- Fast switching speed
- Lower On-resistance
- 100% EAS Guaranteed
- Simple Drive Requirement

Absolute Maximum Ratings

Product Summary

BVDSS	RDSON	ID
30V	3.3mΩ	120A

Applications

- Load Switch
- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.

DFN-8 5x6 Pin Configuration



Type	Package	Marking
CMSA150N03L	DFN-8 5*6	CMSA150N03L

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D @ $T_c=25^\circ\text{C}$	Continuous Drain Current	120	A
I_D @ $T_c=100^\circ\text{C}$	Continuous Drain Current	96	A
I_{DM}	Pulsed Drain Current	360	A
EAS	Single Pulse Avalanche Energy ¹	260	mJ
P_D @ $T_c=25^\circ\text{C}$	Total Power Dissipation	50	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Junction-to-Ambient	---	50	°C/W
$R_{\theta JC}$	Junction-to-Case	---	1.6	°C/W

Electrical Characteristics (T_J=25°C , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	30	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V , I _D =20A	---	---	3.3	mΩ
		V _{GS} =4.5V , I _D =15A	---	---	7.5	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1	---	3	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =30V , V _{GS} =0V , T _J =25°C	---	---	1	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V , I _D =15A	---	23	---	S
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz	---	2.5	---	Ω
Q _g	Total Gate Charge	V _{DD} =15V, I _D =23A V _{GS} =10V	---	40	---	nC
Q _{gs}	Gate-Source Charge		---	8	---	
Q _{gd}	Gate-Drain Charge		---	5	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =15V, R _G =6Ω I _D =23A , V _{GEN} =10V	---	15	---	ns
T _r	Rise Time		---	6	---	
T _{d(off)}	Turn-Off Delay Time		---	33	---	
T _f	Fall Time		---	5	---	
C _{iss}	Input Capacitance	V _{DS} =15V , V _{GS} =0V , f=1MHz	---	2800	---	pF
C _{oss}	Output Capacitance		---	1500	---	
C _{rss}	Reverse Transfer Capacitance		---	340	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V , Force Current	---	---	120	A
I _{SM}	Pulsed Source Current		---	---	360	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _F =15A	---	---	1.2	V

Note :

1.The test condition is V_{DD}=25V , V_{GS}=10V , L=0.5mH , I_{AS}=32.5A

This product has been designed and qualified for the consumer market.

Cmos assumes no liability for customers' product design or applications.

Cmos reserves the right to improve product design ,functions and reliability without notice.