

General Description

The WSF160N10 is the highest performance trench N-Ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WSF160N10 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.
Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

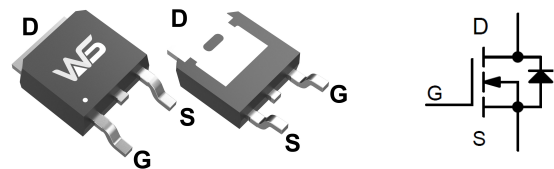
Product Summary

BV_{DSS}	$R_{DS(on)}$	I_D
100V	3.6mΩ	160A

Applications

Power Management in TV Converter.
DC-DC Converter
LED TV Back Light

TO-252 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	160	A
$I_D@T_C=100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	100	A
$I_D@T_A=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	18	A
$I_D@T_A=70^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	15	A
I_{DM}	Pulsed Drain Current ² , $T_C=25^\circ\text{C}$	400	A
EAS	Avalanche Energy, Single pulse, $L=0.5\text{mH}$	484	mJ
I_{AS}	Avalanche Current, Single pulse, $L=0.5\text{mH}$	44	A
$P_D@T_C=25^\circ\text{C}$	Total Power Dissipation ⁴	231	W
$P_D@T = 100^\circ\text{C}$	Total Power Dissipation ⁴	115	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	50	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	0.65	$^\circ\text{C/W}$

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	100	---	---	V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =1mA	---	0.096	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =20A	---	3.6	4.4	mΩ
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =4.5V, I _D =10A	---	5.0	6.5	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.2	1.8	2.5	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-5.5	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =80V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =80V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =30A	---	75	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	1.6	2.5	Ω
Q _g	Total Gate Charge (10V)	V _{DS} =30V, V _{GS} =10V, I _D =20A	---	80	---	nC
Q _{gs}	Gate-Source Charge		---	25	---	
Q _{gd}	Gate-Drain Charge		---	16	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =50V, V _{GS} =10V, RG=3Ω, I _D =1A	---	32	---	ns
T _r	Rise Time		---	18	---	
T _{d(off)}	Turn-Off Delay Time		---	80	---	
T _f	Fall Time		---	160	---	
C _{iss}	Input Capacitance	V _{DS} =50V, V _{GS} =0V, f=1MHz	---	5500	---	pF
C _{oss}	Output Capacitance		---	730	---	
C _{rss}	Reverse Transfer Capacitance		---	88	---	

Diode Characteristics

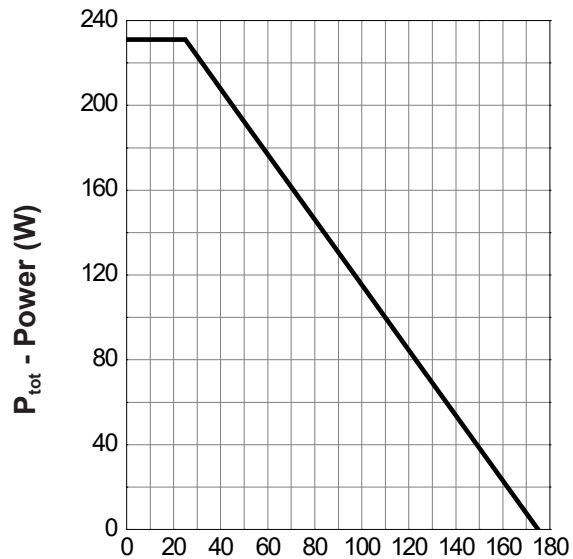
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,6}	V _G =V _D =0V, Force Current	---	---	80	A
I _{SM}	Pulsed Source Current ^{2,6}		---	---	320	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =30A, T _J =25°C	---	---	1.3	V
t _{rr}	Reverse Recovery Time	I _F =60A, dI/dt=100A/μs, T _J =25°C	---	65	---	nS
Q _{rr}	Reverse Recovery Charge		---	120	---	nC

Note :

1. The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
3. The EAS data shows Max. rating. The test condition is V_{DS}= 0V, V_{GS}=10V, L=0.5mH, I_{AS}=44A
4. The power dissipation is limited by 150°C junction temperature
5. The Min. value is 100% EAS tested guarantee.
6. The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

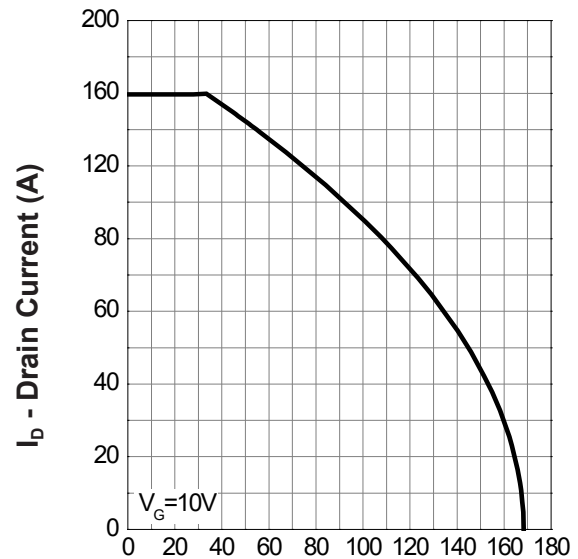
Typical Operating Characteristics

Power Dissipation



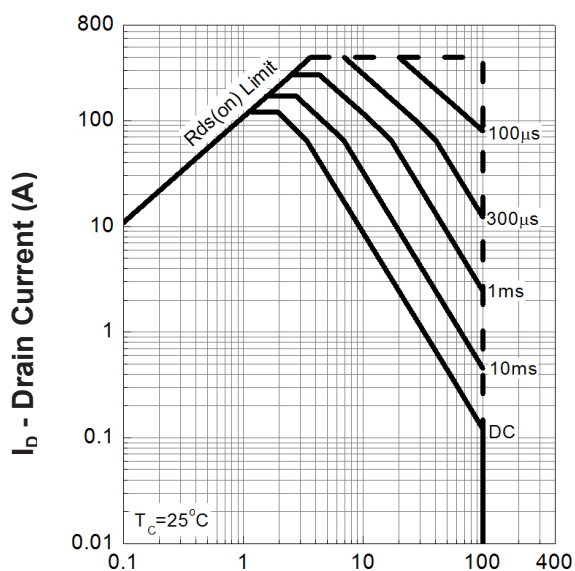
T_c - Case Temperature ($^{\circ}\text{C}$)

Drain Current



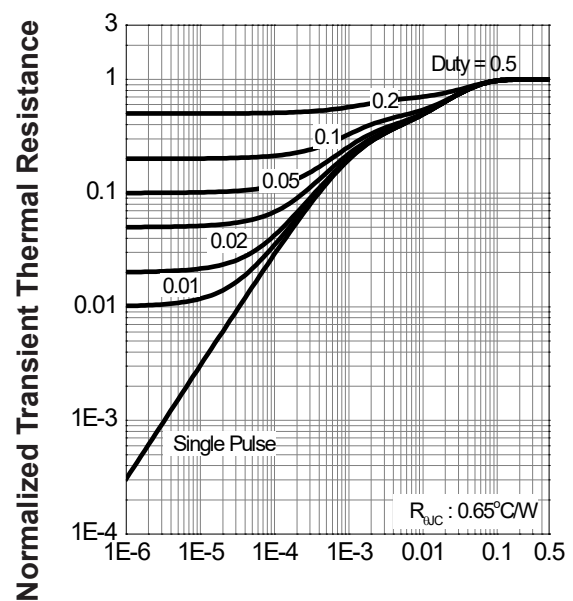
T_c - Case Temperature ($^{\circ}\text{C}$)

Safe Operation Area



V_{DS} - Drain - Source Voltage (V)

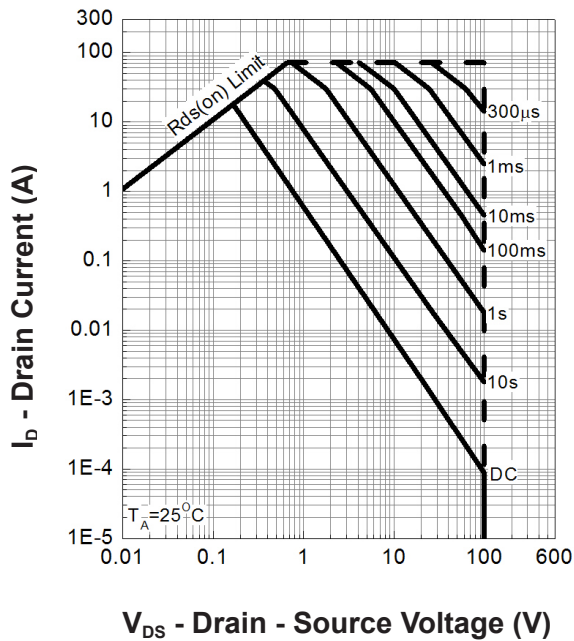
Thermal Transient Impedance



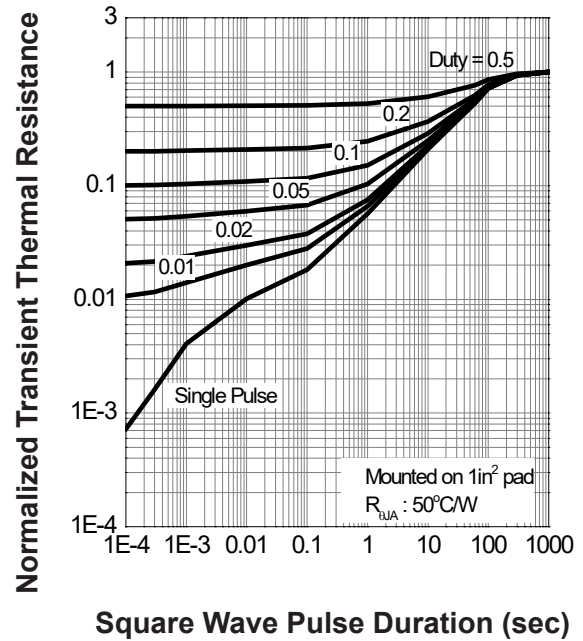
Square Wave Pulse Duration (sec)

Typical Operating Characteristics

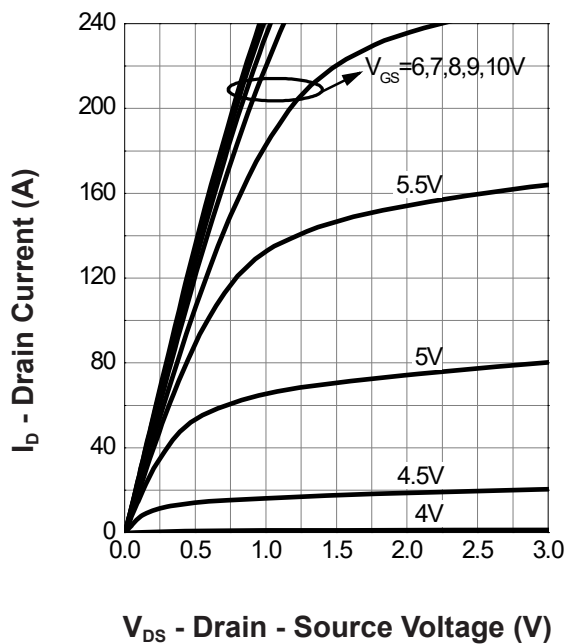
Safe Operation Area



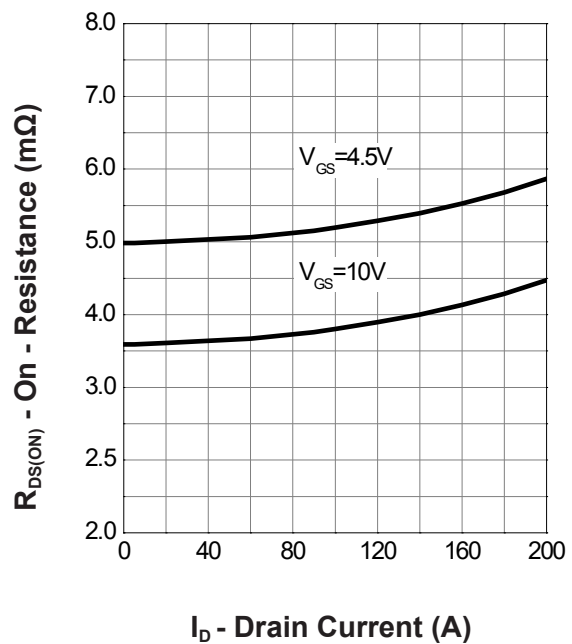
Thermal Transient Impedance



Output Characteristics

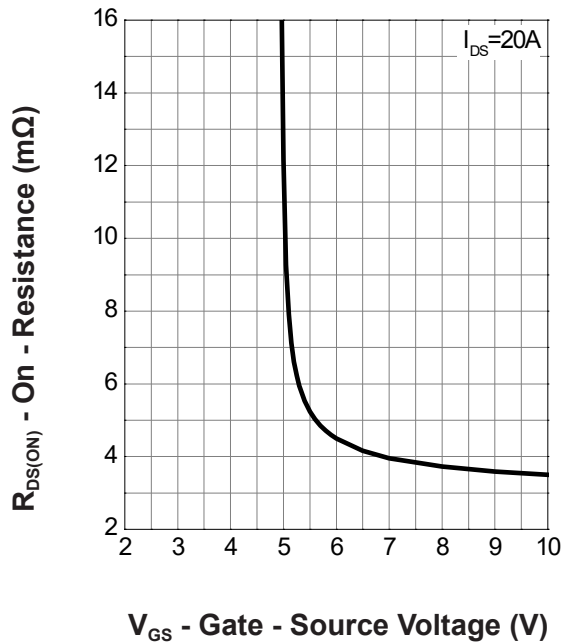


Drain-Source On Resistance

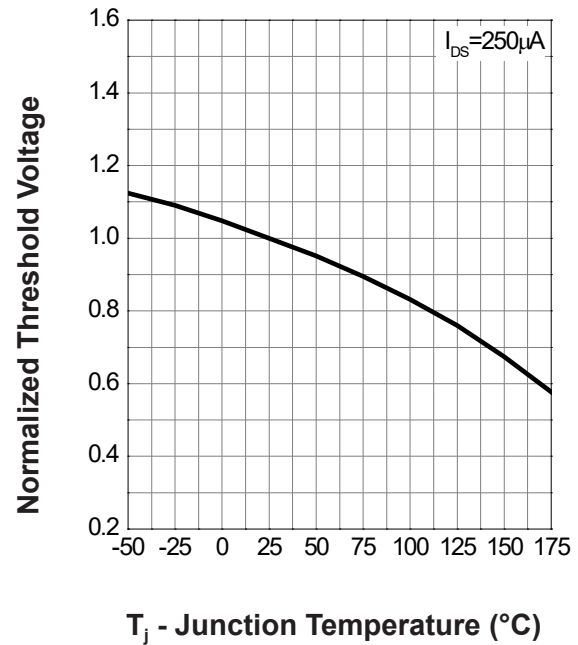


Typical Operating Characteristics

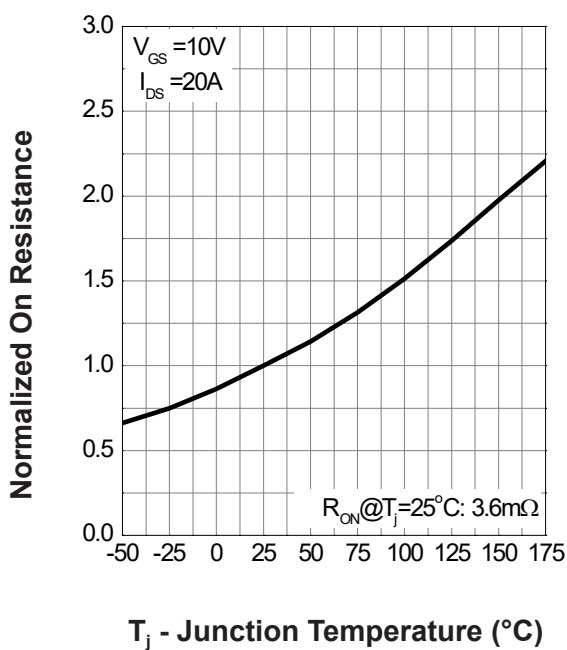
Gate-Source On Resistance



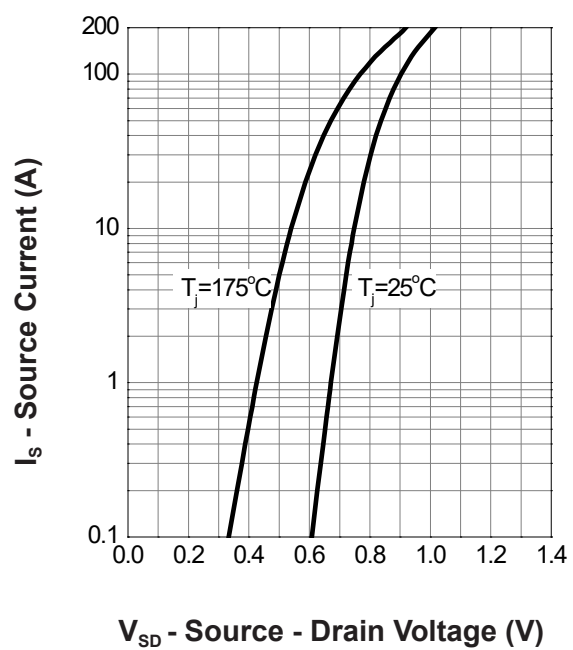
Gate Threshold Voltage



Drain-Source On Resistance

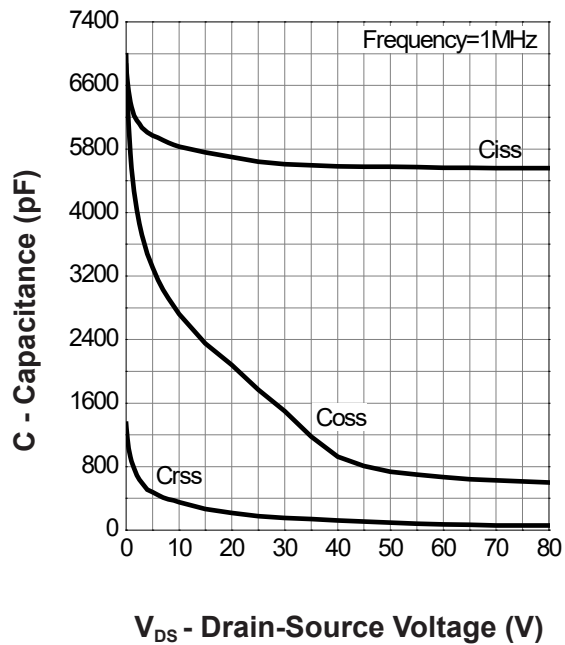


Source-Drain Diode Forward

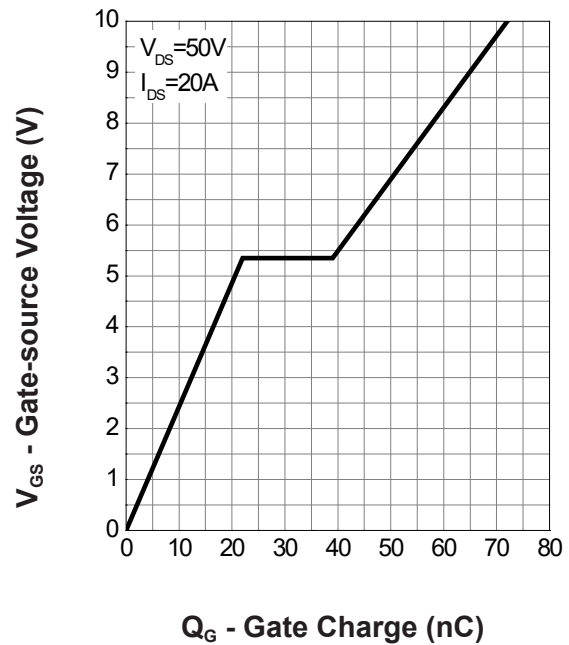


Typical Operating Characteristics

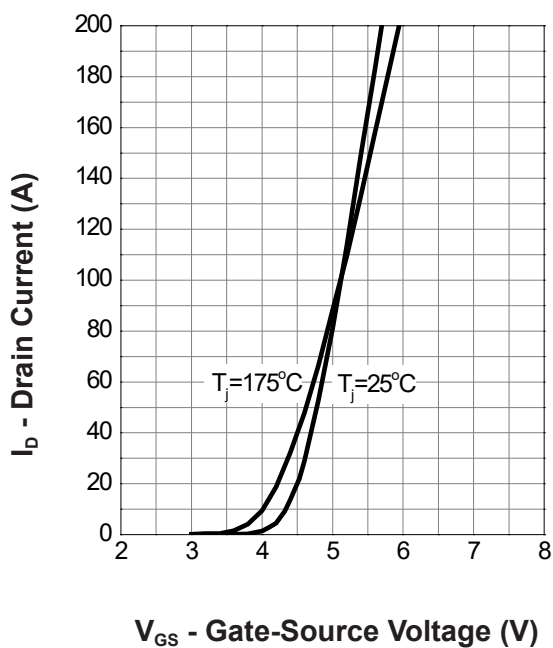
Capacitance



Gate Charge



Transfer Characteristics



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