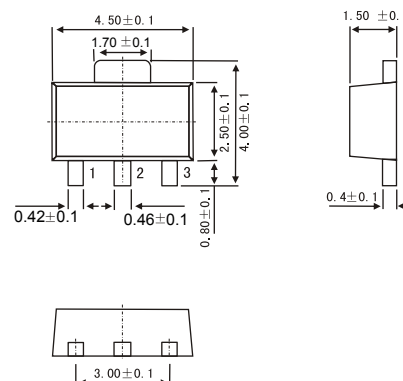


Features

- NPN Silicon, planar design
- Collector-emitter voltage $V_{CE} = 160V$
- Collector current $I_C = 600mA$
- Lead free in compliance with EU RoHS 2.0

Mechanical data

- Epoxy:UL94-V0 rated flame retardant
- Case : Molded plastic, SOT-89-3L
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Mounting Position : Any
- Weight : Approximated 0.022g ram

SOT-89-3L


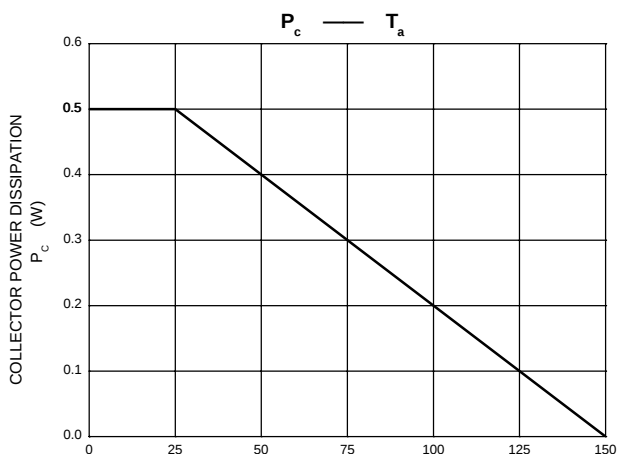
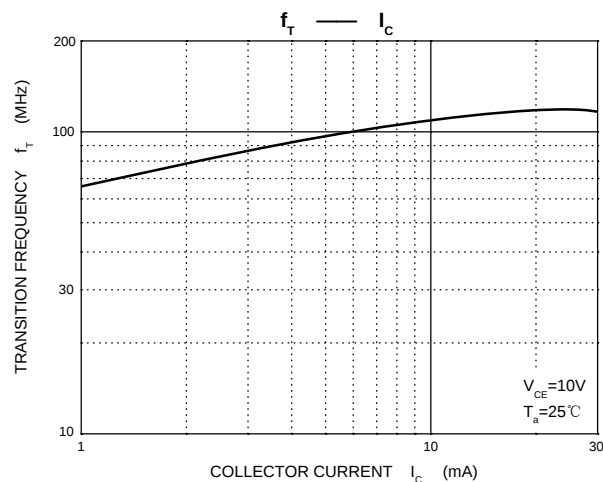
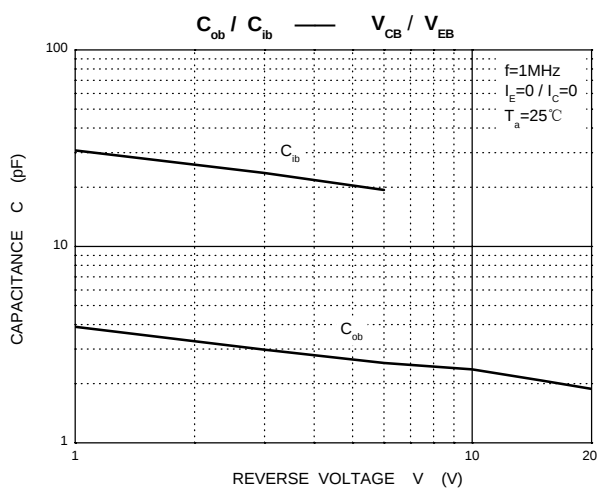
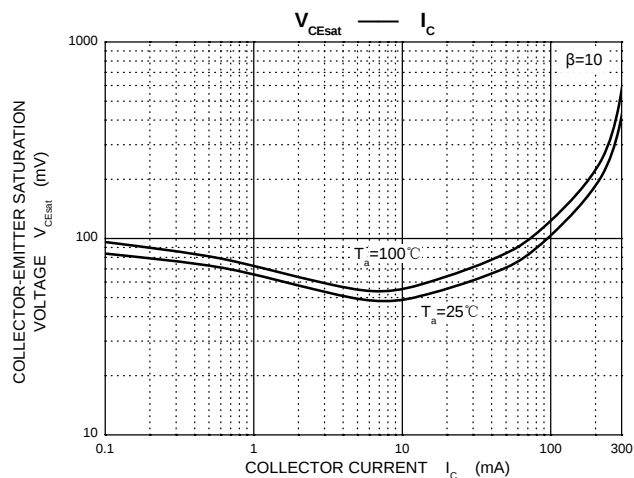
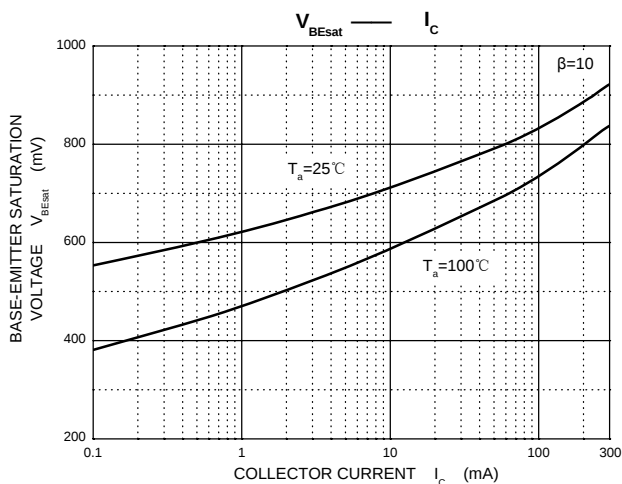
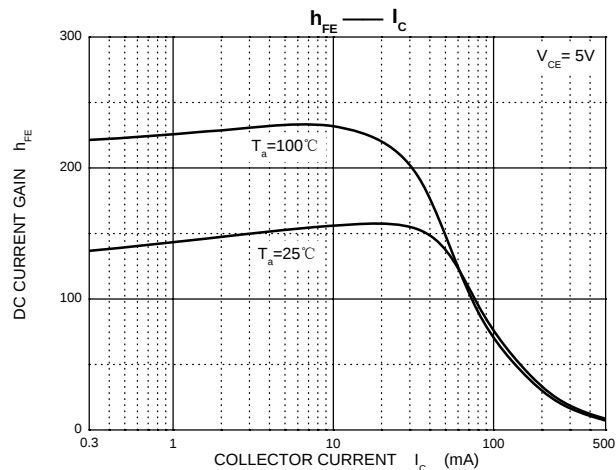
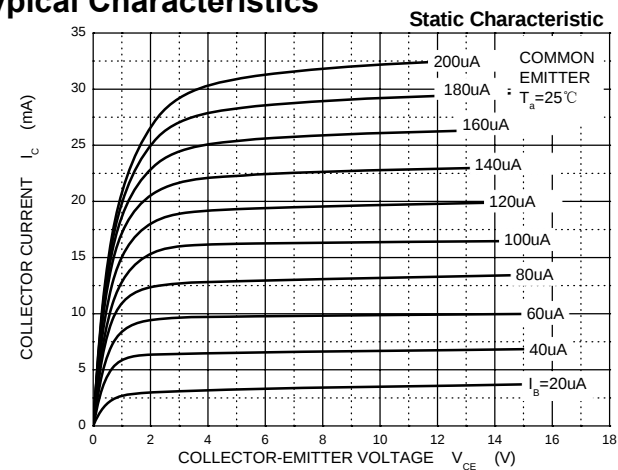
Dimensions in millimeters

MAXIMUM RATINGS ($T_a=25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Value	Unit
V_{CBO}	Collector-Base Voltage	180	V
V_{CEO}	Collector-Emitter Voltage	160	V
V_{EBO}	Emitter-Base Voltage	6	V
I_C	Collector Current -Continuous	0.6	A
P_C	Collector Power Dissipation	0.5	W
T_J, T_{stg}	Operation Junction and Storage Temperature Range	-55~150	$^{\circ}C$

ELECTRICAL CHARACTERISTICS ($T_a=25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Collector-base breakdown voltage	$V_{(BR)CBO}$	$I_C=100\mu A, I_E=0$	180			V
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	$I_C=1mA, I_B=0$	160			V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	$I_E=10\mu A, I_C=0$	6			V
Collector cut-off current	I_{CBO}	$V_{CB}=120V, I_E=0$			50	nA
Emitter cut-off current	I_{EBO}	$V_{EB}=4V, I_C=0$			50	nA
DC current gain	$h_{FE(1)}$	$V_{CE}=5V, I_C=1mA$	80			
	$h_{FE(2)}$	$V_{CE}=5V, I_C=10mA$	100		300	
	$h_{FE(3)}$	$V_{CE}=5V, I_C=50mA$	30			
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C=10mA, I_B=1mA$			0.15	V
	$V_{CE(sat)}$	$I_C=50mA, I_B=5mA$			0.2	V
Base-emitter voltage	$V_{BE(sat)}$	$I_C=10mA, I_B=1mA$			1	V
	$V_{BE(sat)}$	$I_C=50mA, I_B=5mA$			1	V
Transition frequency	f_T	$V_{CE}=10V, I_C=10mA, f=100MHz$	100			MHz
Collector output capacitance	C_{ob}	$V_{CB}=10V, I_E=0, f=1MHz$			6	pF
Noise figure	NF	$V_{CE}=5V, I_C=0.2mA, f=10Hz$ to $15.7KHz, R_s=10\Omega$			8	dB

Typical Characteristics




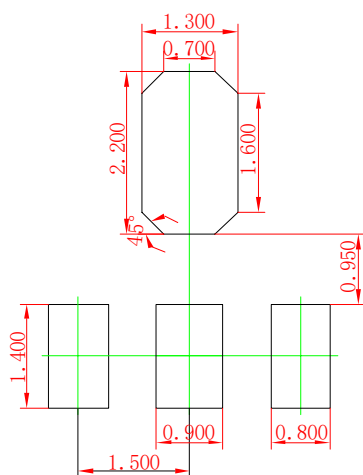
Pinning information

Pin	Simplified outline	Symbol
PinB Base PinC Collector PinE Emitter		

Marking

Type number	Marking code
CXT5551	5551 or 1G6

SOT-89-3L Suggested Pad Layout



Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.