

**General Description**

The 40N25 uses advanced planar stripe DMOS technology and design to provide excellent RDS(ON).

These devices are wellsuited for high efficiency switched mode power supplies, active power factor correction based on half bridge topology.

**Features**

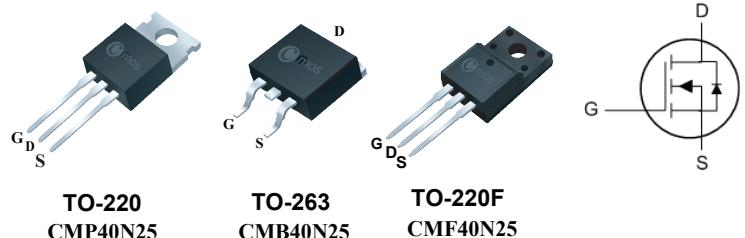
- Fast switching
- 100% avalanche tested
- Improve dv/dt capability
- RoHS compliant

**Product Summary**

BVDSS	RDS(ON)	ID
250V	90mΩ	40A

**Applications**

- Uninterruptible power supplies
- DC/DC converter
- DC/AC inverter

**TO-220/263/220F Pin Configuration****Absolute Maximum Ratings**

Symbol	Parameter	220/263	220F	Units
V <sub>DS</sub>	Drain-Source Voltage	250		V
V <sub>GS</sub>	Gate-Source Voltage	±30		V
I <sub>D</sub> @T <sub>c</sub> =25°C	Continuous Drain Current	40	40	A
I <sub>D</sub> @T <sub>c</sub> =100°C	Continuous Drain Current	25	25	A
I <sub>DM</sub>	Pulsed Drain Current <sup>1</sup>	160	160	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	684		mJ
P <sub>D</sub> @T <sub>c</sub> =25°C	Total Power Dissipation	160	40	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150		°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150		°C

**Thermal Data**

Symbol	Parameter	220/263	220F	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient	62.5	62.5	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-case	0.78	3	°C/W

Electrical Characteristics ( $T_J=25^\circ\text{C}$  , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	250	---	---	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$ , $I_D=20\text{A}$	---	---	90	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D =250\mu\text{A}$	2	---	4	V
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=250\text{V}$ , $V_{\text{GS}} =0\text{V}$	---	---	10	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=15\text{V}$ , $I_D=20\text{A}$	---	26	---	S
$Q_g$	Total Gate Charge	$I_D=20\text{ A}$	---	62	---	nC
$Q_{\text{gs}}$	Gate-Source Charge	$V_{\text{DS}}=125\text{V}$	---	16	---	
$Q_{\text{gd}}$	Gate-Drain Charge	$V_{\text{GS}}=10\text{ V}$	---	18	---	
$T_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DS}}=125\text{V}$	---	42	---	ns
$T_r$	Rise Time	$I_D=20\text{A}$	---	26	---	
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time	$R_G=25\Omega$	---	155	---	
$T_f$	Fall Time		---	32	---	
$C_{\text{iss}}$	Input Capacitance		---	3100	---	pF
$C_{\text{oss}}$	Output Capacitance	$V_{\text{DS}}=25\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	350	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	40	---	

## Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	40	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	160	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=20\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1.4	V

Note :

1.Repetitive rating; pulse width limited by maximum junction temperature.

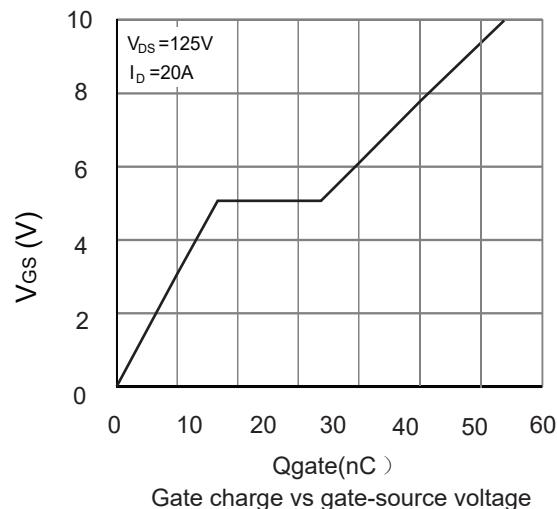
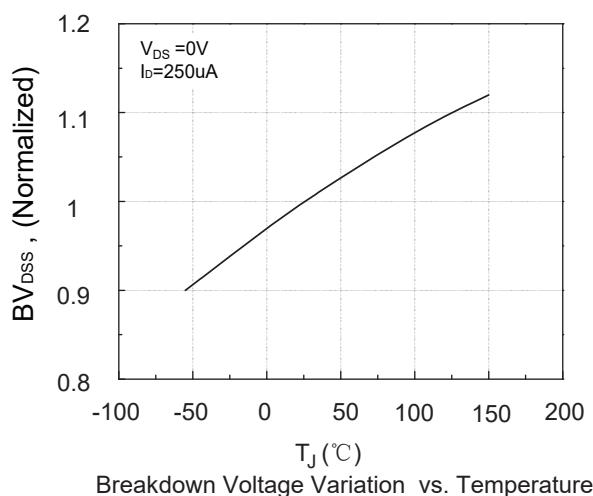
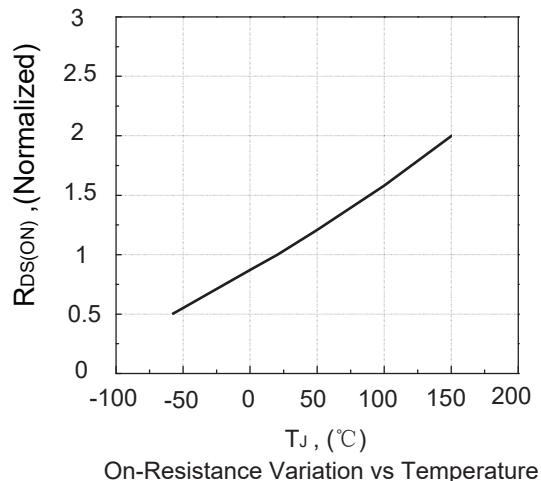
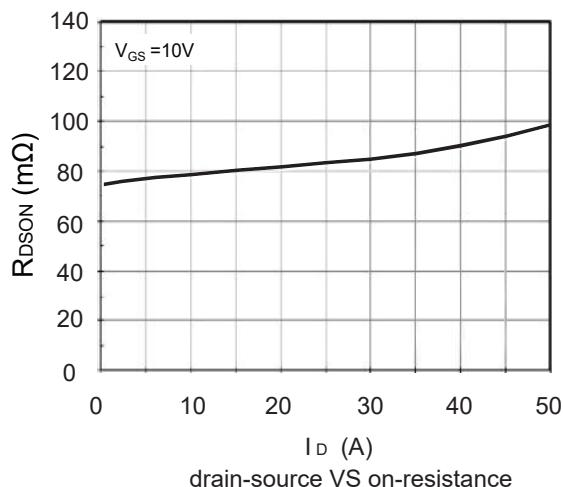
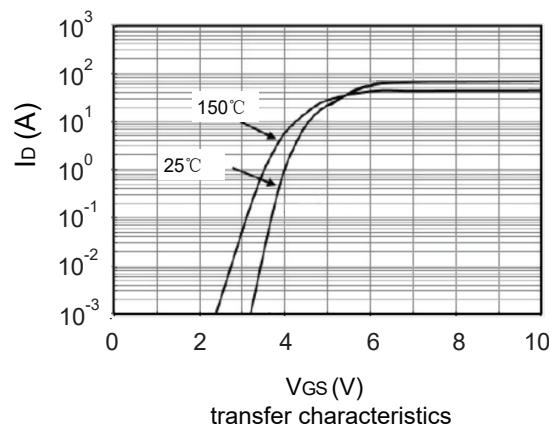
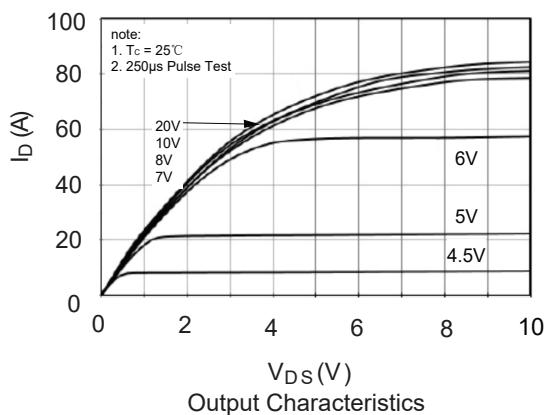
2.The test condition is  $V_{\text{DD}}=50\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $L=1\text{mH}$ , $I_{\text{AS}}=37\text{ A}$ .

This product has been designed and qualified for the consumer market.

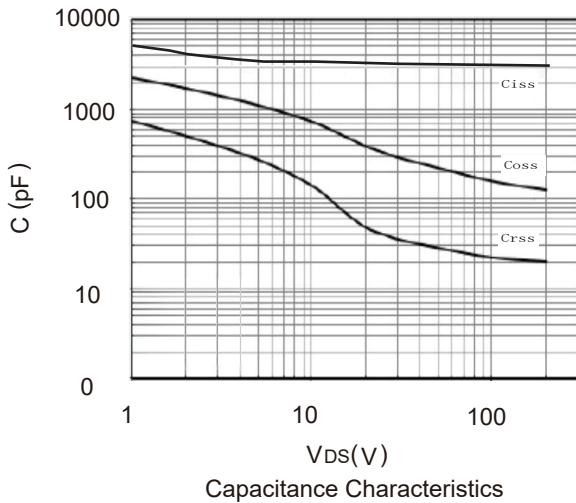
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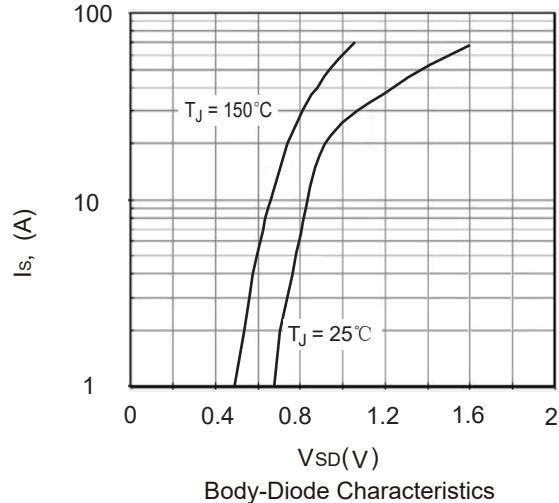
## Typical Characteristics



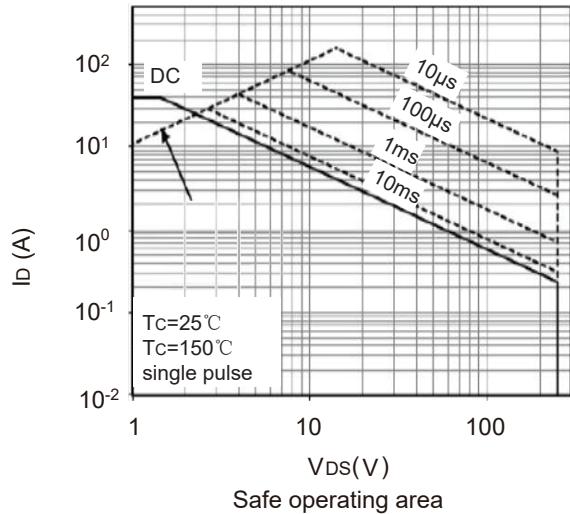
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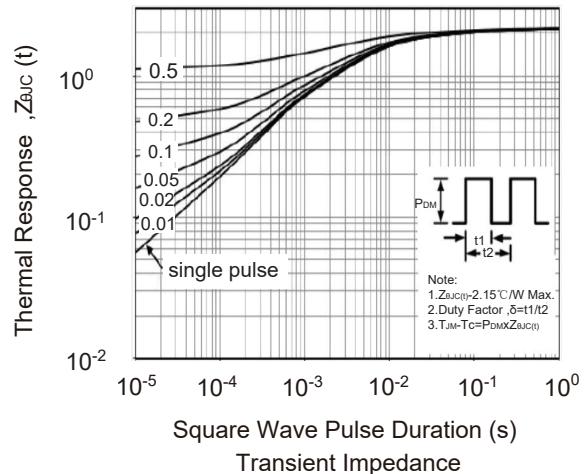
Capacitance Characteristics



Body-Diode Characteristics



Safe operating area



Transient Impedance