

General Description

The 240A have been fabricated using an advanced high voltage MOSFET process that is designed to deliver high levels of performance and robustness in popular AC-DC applications.

Features

- RDS(ON)<3.3mΩ @ VGS=10V
- RDS(ON)<4.8mΩ @ VGS=4.5V
- Reliable and Rugged
- Lead Free

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	40	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current	80	A
I _D @T _C =100°C	Continuous Drain Current	55	A
I _{DM}	Pulsed Drain Current	320	A
E _{AS}	Single Pulse Avalanche Energy	180	mJ
P _D @T _C =25°C	Total Power Dissipation	160	W
T _{STG}	Storage Temperature Range	-55 to 175	°C
T _J	Operating Junction Temperature Range	-55 to 175	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient (Steady-State)	---	50	°C/W
R _{θJC}	Thermal Resistance Junction -Case(Steady-State)	---	1.12	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	40	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=30\text{A}$	---	---	3.3	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_D=20\text{A}$	---	---	4.8	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	1	---	3	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=15\text{V}$, $I_D=11\text{A}$	---	20	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	2.3	---	Ω
Q_g	Total Gate Charge	$V_{\text{DS}}=20\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=80\text{A}$	---	42	---	nC
Q_{gs}	Gate-Source Charge		---	16	---	
Q_{gd}	Gate-Drain Charge		---	12	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=20\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_g=4.7\Omega$ $I_D=40\text{A}$	---	20	---	ns
T_r	Rise Time		---	18	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	27	---	
T_f	Fall Time		---	14	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=20\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	6300	---	pF
C_{oss}	Output Capacitance		---	890	---	
C_{rss}	Reverse Transfer Capacitance		---	60	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{SD}}=28\text{A}$	---	---	1	V

Note :

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 Cmos assumes no liability for customers' product design or applications.
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