

General Description

The CMS4410M uses advanced trench technology to provide excellent RDS(ON). This device is suitable for use as a synchronous switch in PWM applications.

Features

- Low On-Resistance
- Surface mount package.
- RoHS Compliant

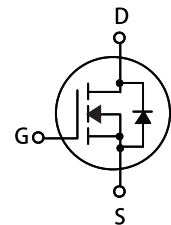
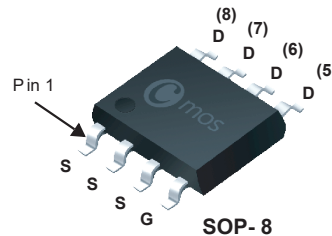
Product Summary

BVDSS	RDSON	ID
30V	8.2mΩ	12.5A

Applications

- DC/DC Converter
- Synchronous Rectifier
- Load Switch
- Battery protection

SOP-8 Pin Configuration



Type	Package	Marking
CMS4410M	SOP-8	CMS4410M

Absolute Maximum Ratings (TA=25°C Unless Otherwise Noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	30	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Continuous Drain Current	12.5	A
I _D @T _A =75°C	Continuous Drain Current	8.8	A
I _{DM}	Pulsed Drain Current	50	A
EAS	Single Pulse Avalanche Energy ¹	117	mJ
P _D @T _C =25°C	Total Power Dissipation	3	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient(at 10 seconds) ³	---	50	°C/W
R _{θJC}	Thermal Resistance Junction-case ²	---	25	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=10A$	---	7	8.2	m Ω
		$V_{GS}=4.5V, I_D=8A$	---	11	15	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1	---	2.5	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=24V, V_{GS}=0V$	---	---	1	μA
		$V_{DS}=24V, V_{GS}=0V, T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=5V, I_D=10A$	---	13	---	S
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1\text{MHz}$	---	2.5	---	Ω
Q_g	Total Gate Charge	$V_{DS}=15V, V_{GS}=0 \text{ to } 10V$ $I_D=12.5A$	---	28	---	nC
Q_{gs}	Gate-Source Charge		---	4.5	---	
Q_{gd}	Gate-Drain Charge		---	5	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DS}=15V, V_{GS}=10V, I_D=12.5A$ $R_{GS}=10\Omega$	---	10	---	ns
T_r	Rise Time		---	35	---	
$T_{d(off)}$	Turn-Off Delay Time		---	55	---	
T_f	Fall Time		---	20	---	
C_{iss}	Input Capacitance	$V_{DS}=25V, V_{GS}=0V, f=1\text{MHz}$	---	1350	---	pF
C_{oss}	Output Capacitance		---	180	---	
C_{rss}	Reverse Transfer Capacitance		---	120	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V$, Force Current	---	---	12.5	A
I_{SM}	Pulsed Source Current		---	---	50	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_F=10A, T_J=25^\circ\text{C}$	---	0.84	1.2	V

Notes:

- The EAS data shows Max. rating .The test condition is $V_{DS}=25V, V_{GS}=10V, L=1.5\text{mH}, I_{AS}=12.5A$.
- $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta JA}$ is determined by the user's board design.
- $R_{\theta JA}$ is measured with 1.0 in² copper on FR-4 board.

This product has been designed and qualified for the consumer market.

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Cmos reserves the right to improve product design ,functions and reliability without notice.

N-Channel Enhancement Mode MOSFET

Typical Characteristics

