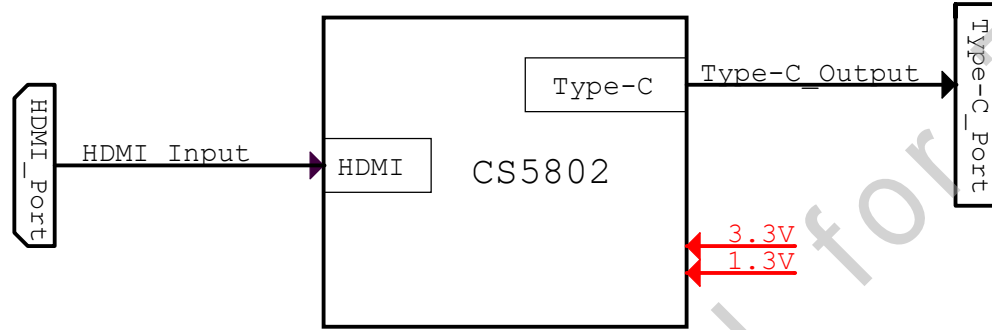
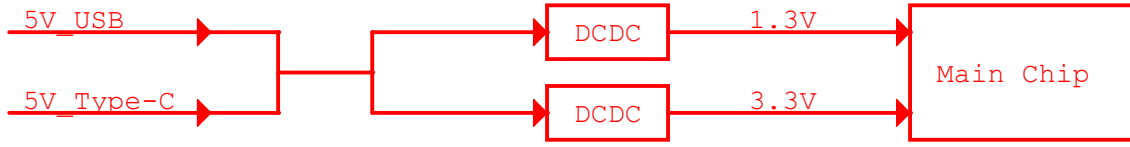


### Block Diagram

### Change History

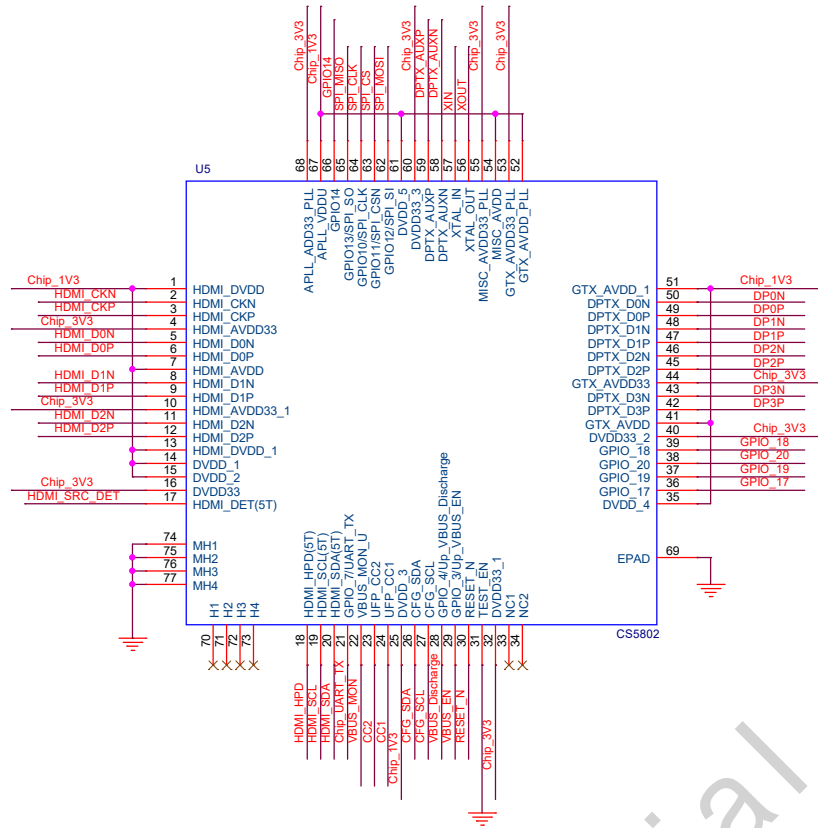
v1.0 2023.11.28 Initial version  
 v1.1 2024.02.29 Update HDMI DDC circuit,  
 HDMI\_SRC\_DET circuit, CS5802 LIB, 5V power supply circuit



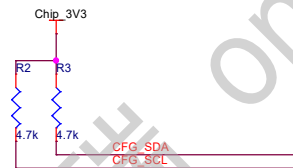
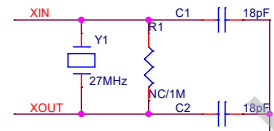
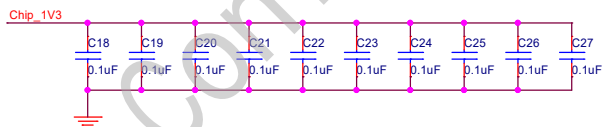
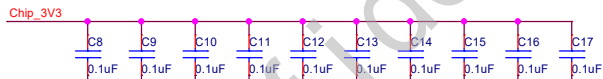
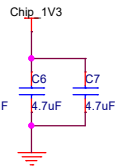
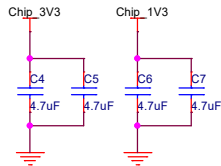
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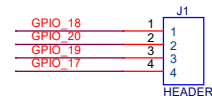
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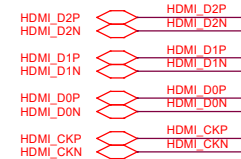
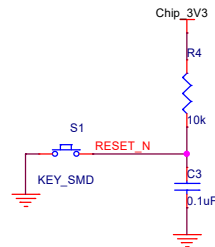
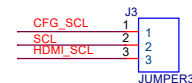
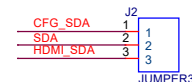
Note:  
The decoupling capacitor shall be as close to the power pin



Note:  
GPIO17/18/19/20 is reserved pin

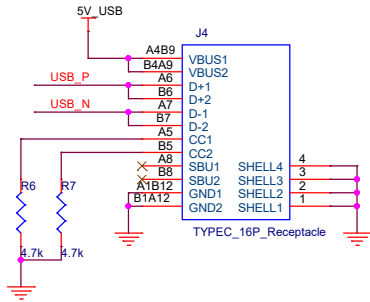


Note:  
CFG\_SCL/CFG\_SDA/UART\_TX test points need to be reserved, J2/J3 for debugging

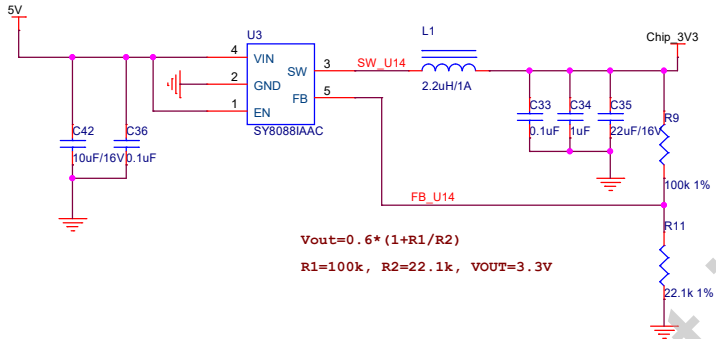


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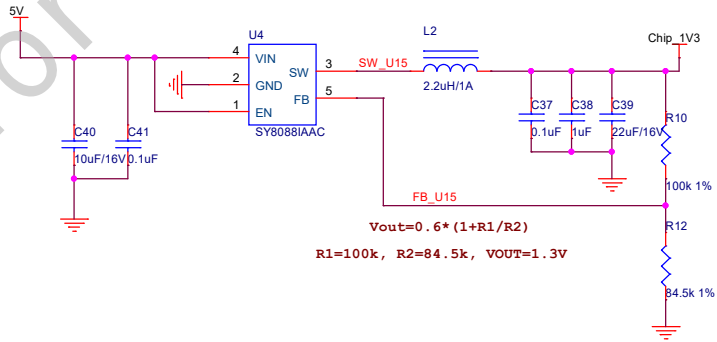
### External power supply USB Port



### 5V->Chip 3V3



### DCDC 5V-> 1V3 Chip Core

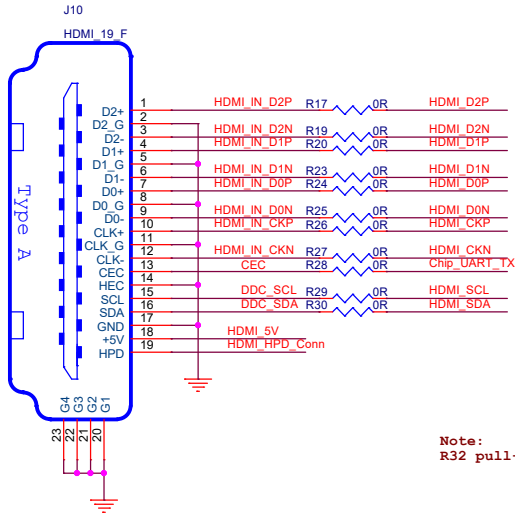


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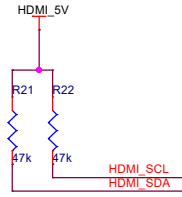


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# HDMI input

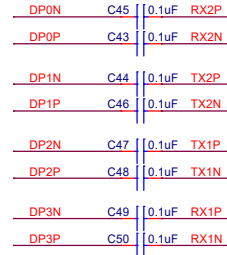


Note:  
HDMI\_DDC needs to be pulled up to HDMI\_5V

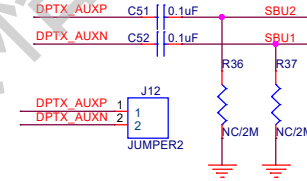


Note:  
DPTX pin swap

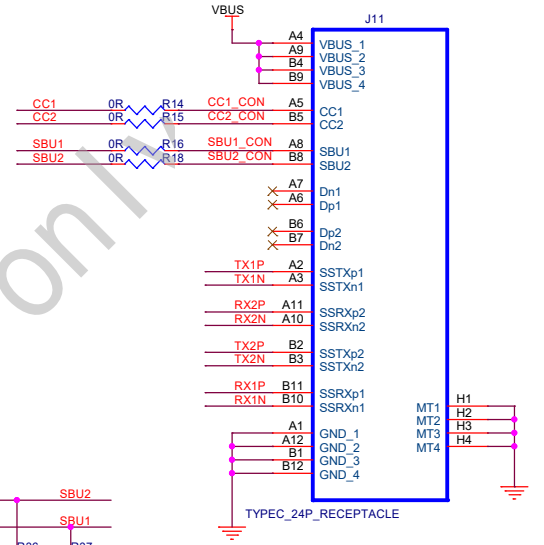
Note:  
DP high-speed transmission needs to ensure 100ohm transfer impedance



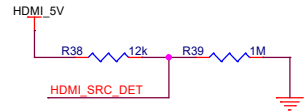
Note:  
J12 for debugging



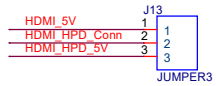
# Type-C output



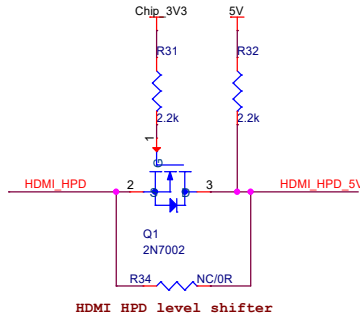
Note:  
HDMI input source detection circuit



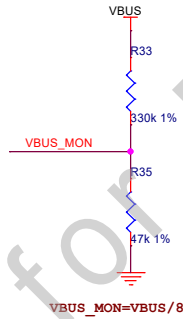
Note:  
J13 connect pin2-3



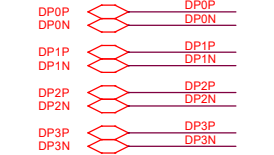
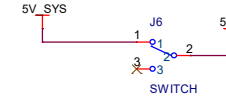
Note:  
R32 pull-up can not connect to HDMI\_5V



HDMI HPD level shifter

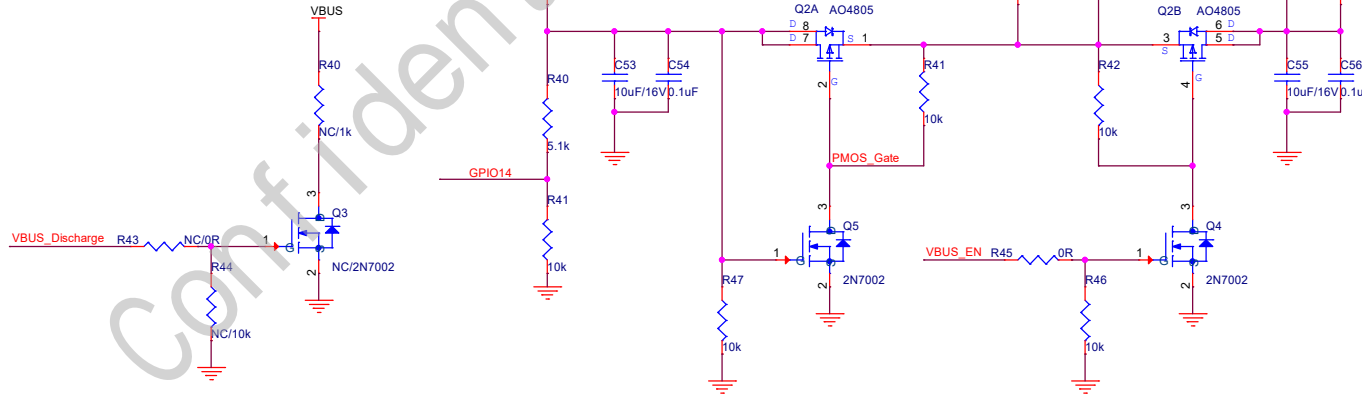
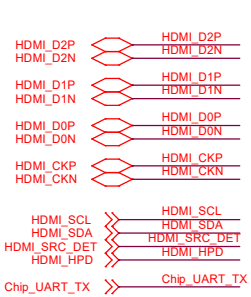


VBUS\_MON=VBUS/8



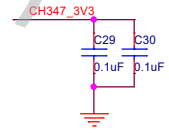
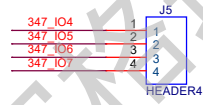
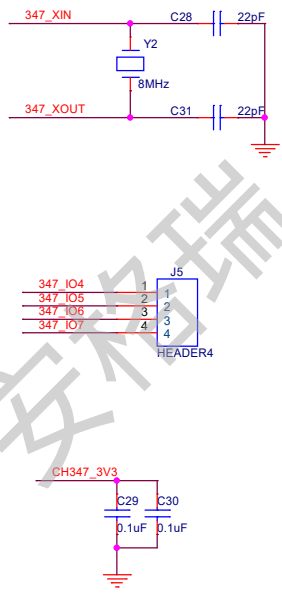
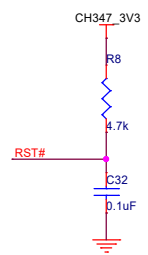
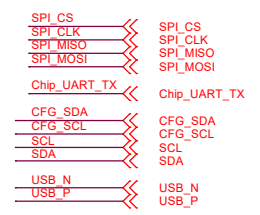
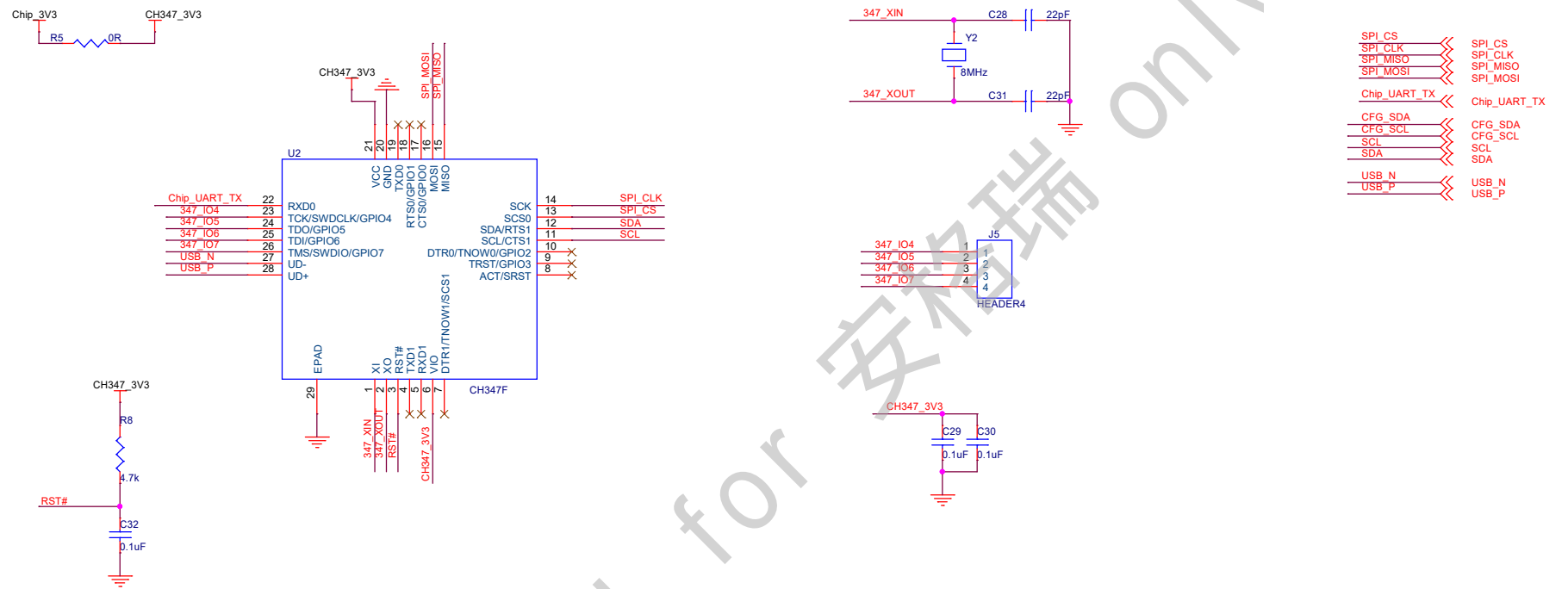
VBUS Power Control:  
1. GPIO14 is used to detect DRP roles  
2. When not plugged into USB for external power supply, power is supplied from Type-C VBUS

Note:  
VBUS\_Discharge is reserved



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Note:  
This page CH347 related circuits for debugging



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