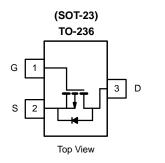
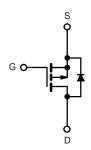


# **AP2309GEN-HF-VB Datasheet**

# P-Channel 30 V (D-S) MOSFET

PRODUC	CT SUMMARY		
V <sub>DS</sub> (V)	$R_{DS(on)}$ ( $\Omega$ ) Typ.	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)
	0.046 at V <sub>GS</sub> = - 10 V	- 5.6	
- 30	0.049 at V <sub>GS</sub> = - 6 V	- 5	11.4 nC
	0.054 at V <sub>GS</sub> = - 4.5 V	-4.5	





P-Channel MOSFET

#### **FEATURES**

- TrenchFET® Power MOSFET
- 100 % R<sub>g</sub> Tested



### **APPLICATIONS**

- For Mobile Computing
  - Load Switch
  - Notebook Adaptor Switch
  - DC/DC Converter

ABSOLUTE MAXIMUM RATIN	<b>GS</b> (T <sub>A</sub> = 25 °C	, unless oth	erwise noted)		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 30	V	
Gate-Source Voltage		$V_{GS}$	± 20		
	T <sub>C</sub> = 25 °C		- 5.6		
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 70 °C	]	- 5.1		
Continuous Dialit Current (1) = 130 °C)	T <sub>A</sub> = 25 °C	· I <sub>D</sub>	- 5.4 <sup>b,c</sup>		
	T <sub>A</sub> = 70 °C		- 4.3 <sup>b,c</sup>	Α	
Pulsed Drain Current (t = 100 μs)		I <sub>DM</sub>	- 18		
Continous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	I.	- 2.1		
Continious Source-Diairi Diode Current	T <sub>A</sub> = 25 °C	- I <sub>S</sub>	- 1 <sup>b,c</sup>		
	T <sub>C</sub> = 25 °C		2.5		
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	] ,	1.6	w	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	1.25 <sup>b,c</sup>	- vv	
	T <sub>A</sub> = 70 °C		0.8 <sup>b,c</sup>		
Operating Junction and Storage Temperature	Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient <sup>b,d</sup>	t ≤ 5 s	R <sub>thJA</sub>	75	100	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	40	50	C/VV		

#### Notes:

- a. Based on  $T_C = 25$  °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Maximum under steady state conditions is 166 °C/W.



<b>SPECIFICATIONS</b> ( $T_J = 25  ^{\circ}\text{C}$ , Parameter	Symbol	Test Conditions	Min.	Tvn	Max.	Unit	
Static	Symbol	rest Conditions	WIII.	Тур.	wax.	Unit	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = - 250 μA	- 30			V	
	ΔV <sub>DS</sub> /T <sub>J</sub>	VGS = 0 V, 1D = 200 μA	- 30	- 19		V	
V <sub>DS</sub> Temperature Coefficient		I <sub>D</sub> = - 250 μA		_		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	)/		4	0.0		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = - 250 μA	- 0.5		- 2.0	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 1 - 5	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le 0.000$ $V_{GS} = -10 \text{ V}$	- 2.5			Α	
On State Brain Surrent	·D(on)	$V_{GS} = -10 \text{ V}, I_{D} = -4.4 \text{ A}$	2.0	0.046			
Drain-Source On-State Resistance <sup>a</sup>	P	V <sub>GS</sub> = -6 V, I <sub>D</sub> = -4 A		0.049		Ω	
	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 3.6 A					
Farmered Transport duration and	~	V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -3.4 A		0.054		_	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 3.4 A		18	<u> </u>	S	
Dynamic <sup>b</sup>				1	1	1	
Input Capacitance	C <sub>iss</sub>			1295			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		150		pF	
Reverse Transfer Capacitance	$C_{rss}$			130			
Total Gate Charge	$Q_{g}$	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -5.4 \text{ A}$		24	36	nC	
<del>-</del>				11.4	17		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -5.4 \text{ A}$		3.4			
Gate-Drain Charge	$Q_{gd}$			3.8			
Gate Resistance	$R_g$	f = 1 MHz	1.5	7.7	15.4	Ω	
Turn-On Delay Time	$t_{d(on)}$			13	20		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 15 V, $R_L$ = 3.5 $\Omega$		4	8		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong -4.3 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$		38	57		
Fall Time	t <sub>f</sub>			6	12		
Turn-On Delay Time	t <sub>d(on)</sub>			28	42	ns -	
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_{L} = 3.5 \Omega$		16	24		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ - 4.3 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 1 $\Omega$		30	45		
Fall Time	t <sub>f</sub>	1		10	20		
Drain-Source Body Diode Characteristic	s			l			
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 2.1		
Pulse Diode Forward Current (t = 100 μs)	I <sub>SM</sub>				- 80	A	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = -4.3 A, V <sub>GS</sub> = 0 V		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>	- 55		15	23	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	1		7	14	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = -4.3 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 \text{ °C}$		8	<del>                                     </del>		
Reverse Recovery Rise Time	t <sub>b</sub>	-		7	-	ns	

#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



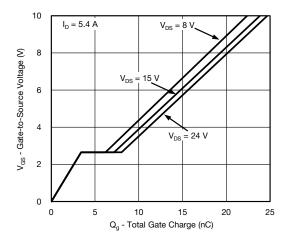
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



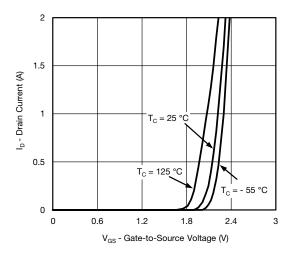
#### **Output Characteristics**



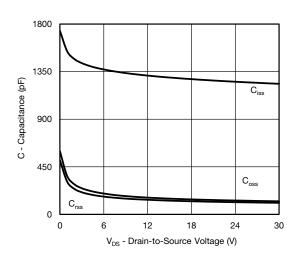
On-Resistance vs. Drain Current



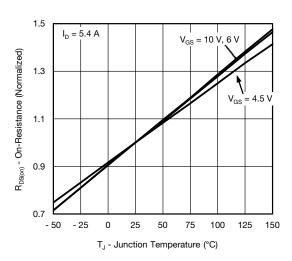
**Gate Charge** 



**Transfer Characteristics** 



Capacitance

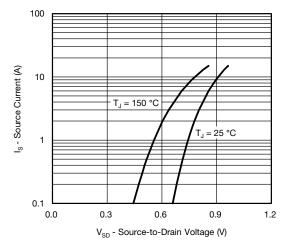


On-Resistance vs. Junction Temperature

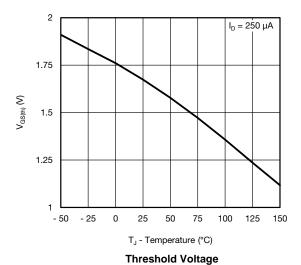


 $I_D = 5.4 A$ 

### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



#### Source-Drain Diode Forward Voltage



R<sub>DS(on)</sub> - On-Resistance (Ω) T<sub>J</sub> = 125 °C 0.040 T<sub>J</sub> = 25 °C 0.020

0.080

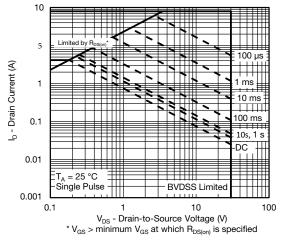
0.060

0.000

 $V_{\rm GS}$  - Gate-to-Source Voltage (V) On-Resistance vs. Gate-to-Source Voltage



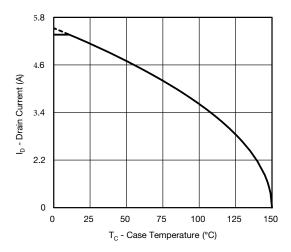
Single Pulse Power (Junction-to-Ambient)



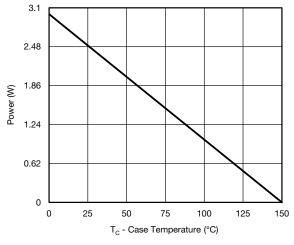
Safe Operating Area, Junction-to-Ambient

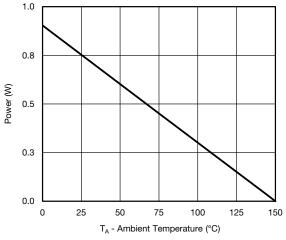


### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



#### **Current Derating\***





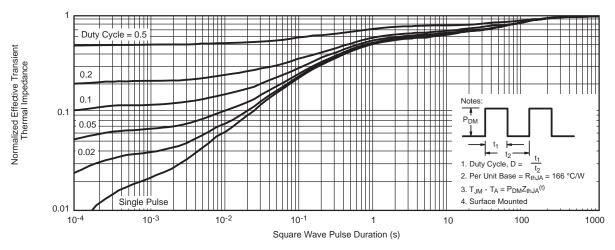
Power, Junction-to-Foot

Power, Junction-to-Ambient

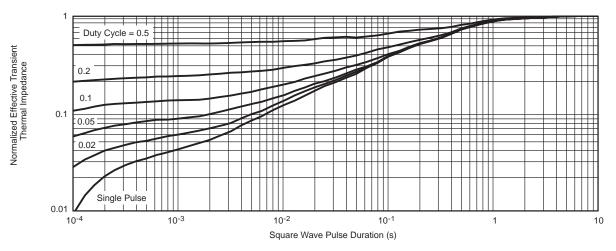
<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max.)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



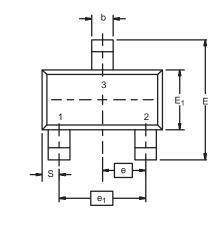
#### Normalized Thermal Transient Impedance, Junction-to-Ambient

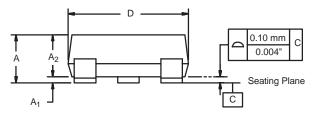


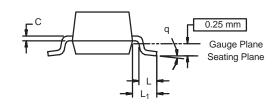
Normalized Thermal Transient Impedance, Junction-to-Foot



## SOT-23 (TO-236): 3-LEAD





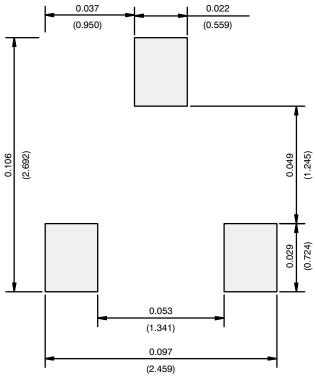


Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
Α	0.89	1.12	0.035	0.044
A <sub>1</sub>	0.01	0.10	0.0004	0.004
A <sub>2</sub>	0.88	1.02	0.0346	0.040
b	0.35	0.50	0.014	0.020
С	0.085	0.18	0.003	0.007
D	2.80	3.04	0.110	0.120
E	2.10	2.64	0.083	0.104
E <sub>1</sub>	1.20	1.40	0.047	0.055
е	0.95 BSC		0.037	4 Ref
e <sub>1</sub>	1.90 BSC		0.074	8 Ref
L	0.40	0.60	0.016	0.024
L <sub>1</sub>	0.64 Ref		0.025	Ref
S	0.50 Ref		0.020	) Ref
q	3°	8°	3°	8°

DWG: 5479



#### **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)



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