

### General Description

The CMSA6404 combines advanced trench MOSFET technology with a low resistance package to provide extremely low  $R_{DS(ON)}$ . This device is ideal for load switch and battery protection applications.

### Features

- Low ON-resistance
- 100% avalanche tested
- ESD protected
- Small Footprint (5x6mm) for Compact Design
- RoHS Compliant

### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D@T_C=25^\circ C$	Continuous Drain Current	85	A
$I_D@T_C=100^\circ C$	Continuous Drain Current	67	A
$I_{DM}$	Pulsed Drain Current	280	A
EAS	Single Pulse Avalanche Energy <sup>1</sup>	334	mJ
$P_D@T_C=25^\circ C$	Total Power Dissipation	83	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Steady-State) <sup>2,3</sup>	---	55	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction -Case (Steady-State)	---	1.5	$^\circ C/W$

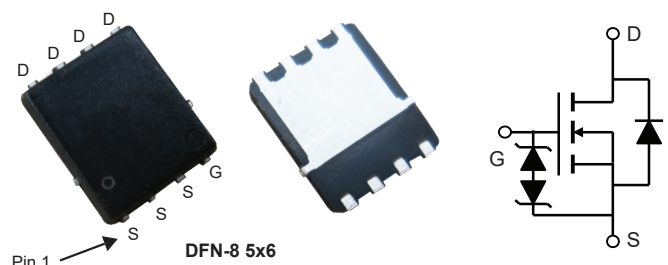
### Product Summary

BVDSS	RDSON	ID
30V	2.3m $\Omega$	85A

### Applications

- DC/DC Converters in Computing, Servers, and POL
- Isolated DC/DC Converters in Telecom and Industrial

### DFN-8 5x6 Pin Configuration



Type	Package	Marking
CMSA6404	DFN-8 5*6	CMSA6404

**Electrical Characteristics (T<sub>J</sub>=25°C , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30	---	---	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =20A	---	1.9	2.3	mΩ
		V <sub>GS</sub> =4.5V , I <sub>D</sub> =20A	---	2.4	3	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250μA	1.0	---	2.5	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = 30V , V <sub>GS</sub> =0V	---	---	1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±16V , V <sub>DS</sub> =0V	---	---	±10	μA
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =20A	---	75	---	S
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =15V , I <sub>D</sub> =20A V <sub>GS</sub> =10V	---	71	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	10	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	16	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DS</sub> =15V , V <sub>GS</sub> =10V , R <sub>L</sub> =0.75Ω R <sub>GEN</sub> =3Ω	---	10	---	ns
T <sub>r</sub>	Rise Time		---	7	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	76	---	
T <sub>f</sub>	Fall Time		---	18	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz	---	4300	---	pF
C <sub>oss</sub>	Output Capacitance		---	700	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	430	---	

**Diode Characteristics**

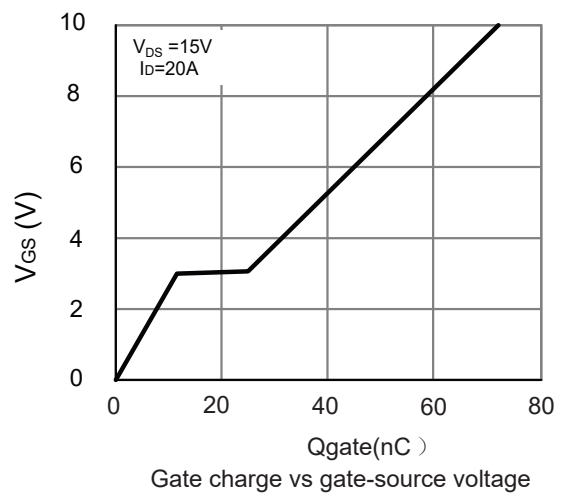
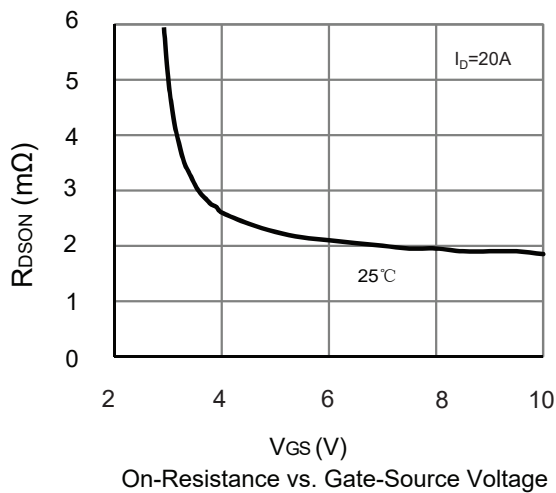
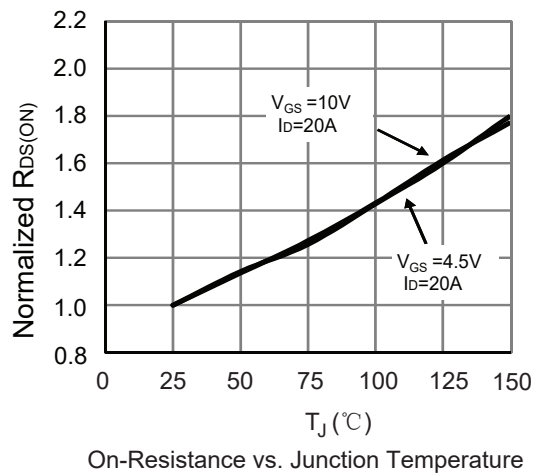
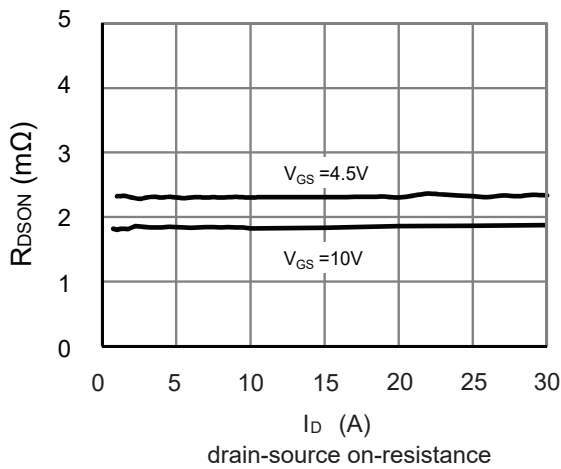
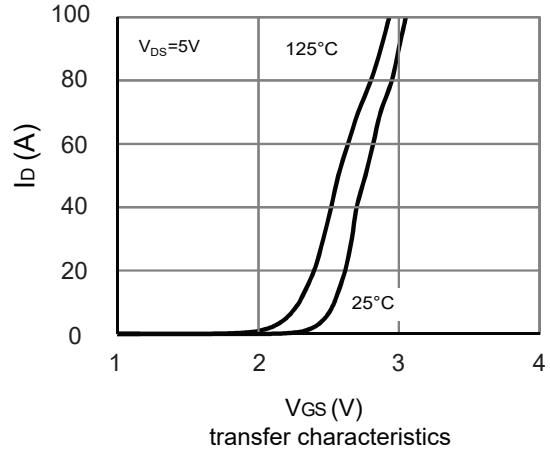
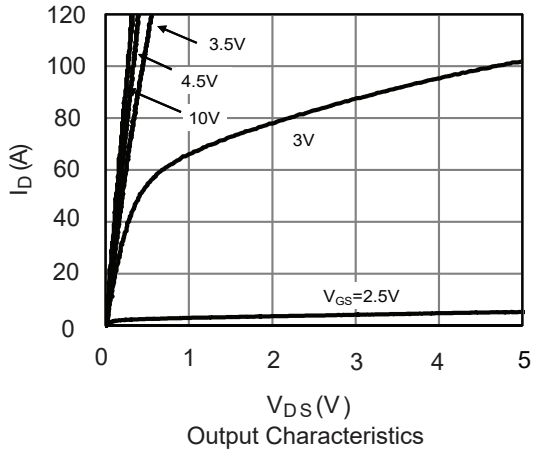
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Diode continuous forward current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current	---	---	85	A
I <sub>SM</sub>	Pulsed Source Current		---	---	280	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>F</sub> =20A , T <sub>J</sub> =25°C	---	0.79	1.2	V

Note:

- The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=20V , V<sub>GS</sub>=10V , L=0.5mH , I<sub>AS</sub>=28A.
- The value of R<sub>θJA</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub> =25°C. The Power dissipation P<sub>DSM</sub> is based on R<sub>θJA</sub> and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design, and the maximum temperature of 150°C may be used if the PCB allows it.
- The R<sub>θJA</sub> is the sum of the thermal impedance from junction to case R<sub>θJC</sub> and case to ambient.

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Typical Characteristics



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