

CMF60P06

P-Channel Enhancement Mode MOSFET

General Description

The CMF60P06 uses advanced trench technology and design to provide excellent RDS(ON). It can be used in a wide variety of applications.

Features

- Fast switching
- 100% avalanche tested
- Lower On-resistance
- RoHS Compliant

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-60	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C = 25^\circ\text{C}$	Continuous Drain Current	-60	A
$I_D @ T_C = 100^\circ\text{C}$	Continuous Drain Current	-48	A
I_{DM}	Pulsed Drain Current	-180	A
EAS	Single Pulse Avalanche Energy	340	mJ
$P_D @ T_C = 25^\circ\text{C}$	Total Power Dissipation	60	W
T_{STG}	Storage Temperature Range	-55 to 175	°C
T_J	Operating Junction Temperature Range	-55 to 175	°C

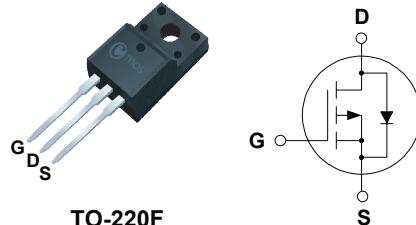
Product Summary

BVDSS	RDS(ON)	ID
-60V	19.5mΩ	-60A

Applications

- Inverters
- Motor drive
- DC / DC converter

TO-220 Pin Configuration



Type	Package	Marking
CMF60P06	TO-220F	CMF60P06

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient	---	40	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-case	---	1.2	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=-250\mu\text{A}$	-60	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}$, $I_D=-20\text{A}$	---	---	19.5	$\text{m}\Omega$
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-4.5\text{V}$, $I_D=-20\text{A}$	---	---	22	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = -250\mu\text{A}$	-1	---	-3	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-60\text{V}$, $V_{\text{GS}}=0\text{V}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}= \pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-10\text{V}$, $I_D=-10\text{A}$	---	32	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	9	---	Ω
Q_g	Total Gate Charge	$I_D=-55\text{A}$	---	37	---	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}} = -30\text{V}$	---	15	---	
Q_{gd}	Gate-Drain Charge	$V_{\text{GS}} = -4.5\text{V}$	---	20	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=-2\text{V}$, $I_{\text{DS}} = -10\text{A}$	---	11	---	ns
T_r	Rise Time	$R_L=2\Omega$	---	6	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time	$R_G=1\Omega$	---	71	---	
T_f	Fall Time	$V_{\text{GEN}}=-10\text{V}$	---	41	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=-25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	5800	---	pF
C_{oss}	Output Capacitance		---	390	---	
C_{rss}	Reverse Transfer Capacitance		---	290	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	-60	A
I_{SM}	Pulsed Source Current		---	---	-180	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=-20\text{A}$, $T_J=25^\circ\text{C}$	---	---	-1.2	V

Note :

This product has been designed and qualified for the consumer market.
 Cmos assumes no liability for customers' product design or applications.
 Cmos reserves the right to improve product design, functions and reliability without notice.