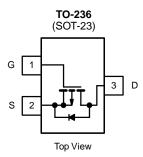


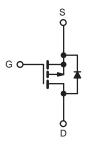
## **AP2331GN-HF-VB Datasheet** P-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	- 60				
R <sub>DS(on)</sub> (Ω)	V <sub>GS</sub> = - 10 V	0.05			
Q <sub>g</sub> (Max.) (nC)	12				
Q <sub>gs</sub> (nC)	3.8				
Q <sub>gd</sub> (nC)	5.1				
Configuration	Single				

### **FEATURES**

- · Isolated Package
- High Voltage Isolation =  $2.5 \text{ kV}_{\text{RMS}}$  (t = 60 s; f = 60 Hz
- Sink to Lead Creepage Distance = 4.8 mm
- P-Channel
- 175 °C Operating Temperature
- Dynamic dV/dt Rating
- · Low Thermal Resistance
- Lead (Pb)-free Available





P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T	c = 25 °C, u	nless otherw	vise noted			
PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-Source Voltage			V <sub>DS</sub>	- 60	V	
Gate-Source Voltage			V <sub>GS</sub>	± 20	v	
Continuous Drain Current	Vac at 10 V	$\frac{T_{C} = 25 \text{ °C}}{T_{C} = 100 \text{ °C}}$	۱ <sub>D</sub>	- 5.2		
Continuous Drain Current	$v_{GS}$ at - 10 v			- 3.8	A	
Pulsed Drain Current <sup>a</sup>			I <sub>DM</sub>	- 21		
Linear Derating Factor				0.18	W/°C	
Single Pulse Avalanche Energy <sup>b</sup>			E <sub>AS</sub>	120	mJ	
Repetitive Avalanche Currenta			I <sub>AR</sub>	- 5.2	А	
Repetitive Avalanche Energy <sup>a</sup>			E <sub>AR</sub>	2.7	mJ	
Maximum Power Dissipation	T <sub>C</sub> = 25 °C		P <sub>D</sub>	27	W	
Peak Diode Recovery dV/dt <sup>c</sup>			dV/dt	- 4.5	V/ns	
Operating Junction and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 175	℃	
Soldering Recommendations (Peak Temperature)	for 10 s			300 <sup>d</sup>		
Mounting Torque	6-32 or M3 screw			10	lbf · in	
				1.1	N · m	

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

b.  $V_{DD} = -25 \text{ V}$ , starting  $T_J = 25 \text{ °C}$ , L = 5.0 mH,  $R_G = 25 \Omega$ ,  $I_{AS} = -5.3 \text{ A}$  (see fig. 12). c.  $I_{SD} \leq -6.7 \text{ A}$ , dl/dt  $\leq 90 \text{ A/}\mu\text{s}$ ,  $V_{DD} \leq V_{DS}$ ,  $T_J \leq 175 \text{ °C}$ .

d. 1.6 mm from case.

## AP2331GN-HF-VB

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PARAMETER	SYMBOL	TYP		MAX.		UNIT		
Maximum Junction-to-Ambient	R <sub>thJA</sub>	- 65 - 5.5						
Maximum Junction-to-Case (Drain)	R <sub>thJC</sub>				°C/W			
SPECIFICATIONS T <sub>J</sub> = 25 °C,	unless otherv	wise noted						
PARAMETER	SYMBOL	TES	T CONDITI	ONS	MIN.	TYP.	MAX.	UNI
Static								
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = - 250 μA		- 60	-	-	V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_J$	Reference	e to 25 °C, I	<sub>D</sub> = - 1 mA	-	- 0.060	-	V/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	$V_{GS}$ , $I_D = -2$	250 µA	- 1.0	-	- 2.5	V
Gate-Source Leakage	I <sub>GSS</sub>	,	V <sub>GS</sub> = ± 20 V	V	-	-	± 100	nA
Zana Osta Mallana Dasia Osmanl		V <sub>DS</sub> =	V <sub>DS</sub> = - 60 V, V <sub>GS</sub> = 0 V			-	- 100	μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 48 V <sub>GS</sub> = 0 V, T <sub>J</sub> = 150 °C		-	-	- 500		
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V	I <sub>D</sub> =	- 3.2 A <sup>b</sup>	-	0.05	-	Ω
Forward Transconductance	9 <sub>fs</sub>	V <sub>DS</sub> =	- 25 V, I <sub>D</sub> =	- 3.2 A <sup>b</sup>	1.6	-	-	S
Dynamic		•						
Input Capacitance	C <sub>iss</sub>		N 0.V			270	-	
Output Capacitance	C <sub>oss</sub>		V <sub>GS</sub> = 0 V, V <sub>DS</sub> = - 25 V, f = 1.0 MHz, see fig. 5		-	170	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.			-	31	-	
Drain to Sink Capacitance	С	f = 1.0 MHz		-	12	-	1	
Total Gate Charge	Qg	$V_{GS} = -10 V$ $I_D = -4.7 A, V_{DS} = -48 V,$		-	-	12	nC	
Gate-Source Charge	Q <sub>gs</sub>			-	-	3.8		
Gate-Drain Charge	Q <sub>gd</sub>	1	see fig. 6 and 13 <sup>b</sup>		-	-	5.1	1
Turn-On Delay Time	t <sub>d(on)</sub>				-	11	-	<u> </u>
Rise Time	t <sub>r</sub>		- 30 V, I <sub>D</sub> =		-	63	-	1
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_{G} = 24 \Omega, R_{D} = 4.0 \Omega,$ see fig. 10 <sup>b</sup>		-	9.6	-	ns	
Fall Time	t <sub>f</sub>			-	31	-		
Internal Drain Inductance	L <sub>D</sub>	Between lead, 6 mm (0.25") from package and center of die contact		-	4.5	-	nH	
Internal Source Inductance	L <sub>S</sub>			-	7.5	-		
Drain-Source Body Diode Characteristic	s							
Continuous Source-Drain Diode Current	I <sub>S</sub>	MOSFET symbol showing the integral reverse p - n junction diode		-	-	- 5.2	A	
Pulsed Diode Forward Current <sup>a</sup>	I <sub>SM</sub>			-	-	- 21		
Body Diode Voltage	$V_{SD}$	T <sub>J</sub> = 25 °C,	I <sub>S</sub> = - 5.2 A	, $V_{GS} = 0 V^{b}$	-	-	- 5 .5	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	- $T_J = 25 \text{ °C}, I_F = -4.7 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}^b$		-	80	160	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			-	0.096	0.19	μC	
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is do			ninated by	loand	D) _	

#### Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

b. Pulse width  $\leq$  300  $\mu s;$  duty cycle  $\leq$  2 %.



### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

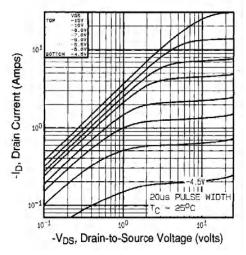


Fig. 1 - Typical Output Characteristics, T<sub>C</sub>= 25 °C

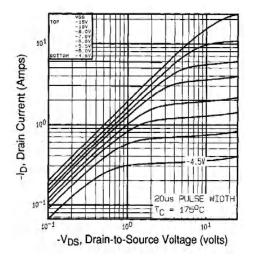


Fig. 2 - Typical Output Characteristics,  $T_C$ = 175 °C

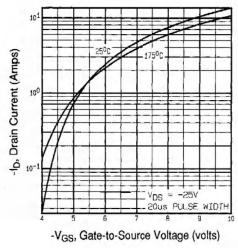


Fig. 3 - Typical Transfer Characteristics

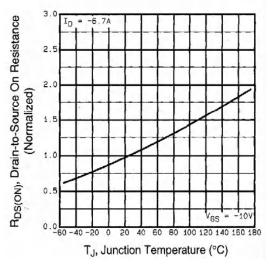


Fig. 4 - Normalized On-Resistance vs. Temperature

### AP2331GN-HF-VB



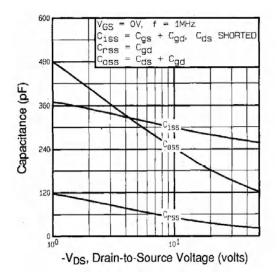
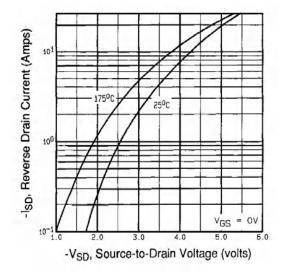


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage





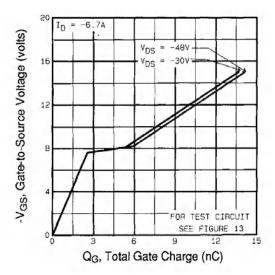


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

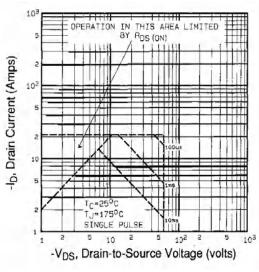


Fig. 8 - Maximum Safe Operating Area



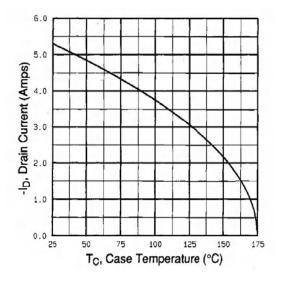


Fig. 9 - Maximum Drain Current vs. Case Temperature

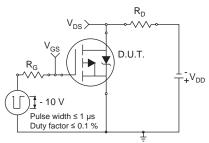


Fig. 10a - Switching Time Test Circuit

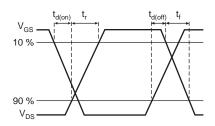
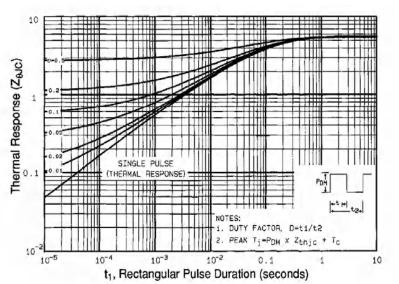
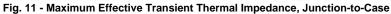


Fig. 10b - Switching Time Waveforms





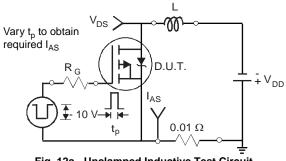


Fig. 12a - Unclamped Inductive Test Circuit

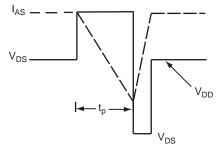
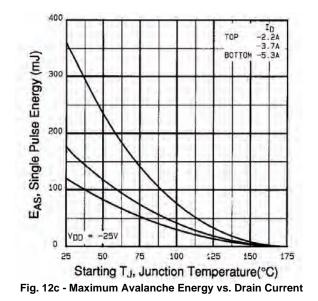
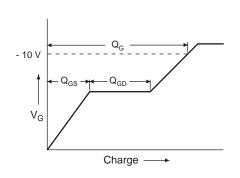


Fig. 12b - Unclamped Inductive Waveforms









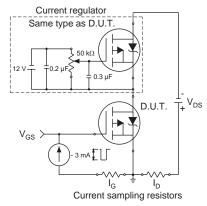
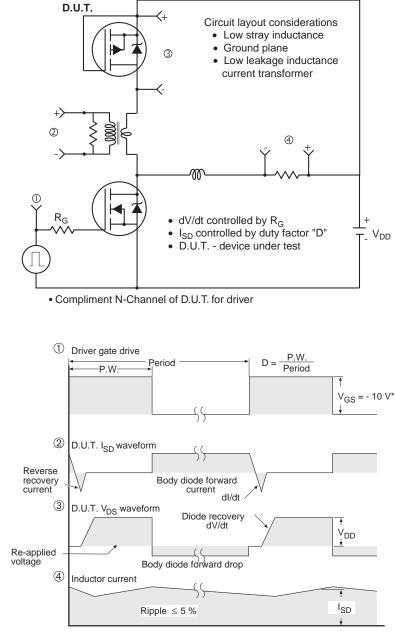
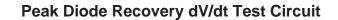
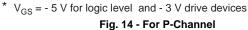


Fig. 13b - Gate Charge Test Circuit





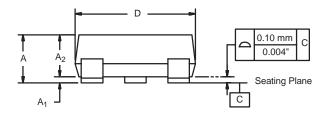






### SOT-23 (TO-236): 3-LEAD



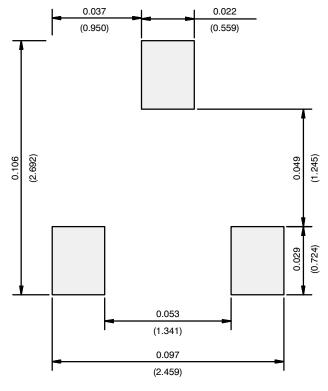




Dim	MILLI	METERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A <sub>1</sub>	0.01	0.10	0.0004	0.004	
A <sub>2</sub>	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E <sub>1</sub>	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e <sub>1</sub>	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L <sub>1</sub>	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
<b>q</b> ECN: S-03946-Rev. K, 09- DWG: 5479	÷	8°	3°	8°	



### **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)



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