

General Description

The 012N10 uses advanced technology and design to provide excellent RDS(ON) .

This device is suitable for PWM, load switching and general purpose applications.

Features

- Low On-Resistance
- 100% avalanche tested
- RoHS Compliant

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_c = 25^\circ\text{C}$	Continuous Drain Current	60	A
$I_D @ T_c = 100^\circ\text{C}$	Continuous Drain Current	41	A
I_{DM}	Pulsed Drain Current	240	A
EAS	Single Pulse Avalanche Energy ¹	60	mJ
P_D	Total Power Dissipation	30	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Product Summary

BVDSS	RDS(ON)	ID
100V	10mΩ	60A

Applications

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

TO-220F Pin Configuration

Type	Package	Marking
CMF012N10	TO-220F	CMF012N10

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient (PCB mount) ²	---	50	°C/W
$R_{\theta JC}$	Thermal Resistance Junction -Case	---	4.15	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	100	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=20\text{A}$	---	8.7	10	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_D=10\text{A}$	---	11	13	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D =250\mu\text{A}$	1	---	3	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}= 100\text{V}$, $V_{\text{GS}}=0\text{V}$	---	---	1	uA
		$V_{\text{DS}}= 100\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}= \pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_D=10\text{A}$	---	20	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	1	---	Ω
Q_g	Total Gate Charge		---	35	---	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=20\text{A}$	---	11	---	
Q_{gd}	Gate-Drain Charge		---	6	---	
$T_{\text{d(on)}}$	Turn-On Delay Time		---	13	---	ns
T_r	Rise Time	$V_{\text{DS}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_L=2\Omega$	---	8.6	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	30	---	
T_f	Fall Time		---	4	---	
C_{iss}	Input Capacitance		---	1700	---	pF
C_{oss}	Output Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	950	---	
C_{rss}	Reverse Transfer Capacitance		---	100	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	60	A
I_{SM}	Pulsed Source Current		---	---	240	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=10\text{A}$	---	0.81	1.2	V

Note :

1.The EAS data shows Max. rating .The test condition is $V_{\text{DS}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=1\text{mH}$, $I_{\text{AS}}=11\text{A}$.2.Surface mounted on 1 in² copper pad of FR4 board

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