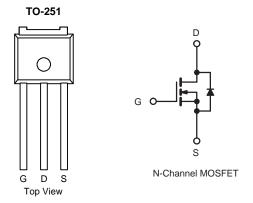


## 20N3LG TO251-VB Datasheet

# N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}$ ( $m\Omega$ )	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)		
30	7 at V <sub>GS</sub> = 10 V	50	19 nC		
	9 at V <sub>GS</sub> = 4.5 V	45	19110		



#### **FEATURES**

- · Halogen-free
- TrenchFET® Gen III Power MOSFET
- 100 % R<sub>g</sub> Tested
   100 % UIS Tested



#### **APPLICATIONS**

- DC/DC Conversion
  - System Power

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30	V	
Gate-Source Voltage		$V_{GS}$	± 20	v	
	T <sub>C</sub> = 25 °C		50		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	$T_C = 70  ^{\circ}C$ $T_A = 25  ^{\circ}C$	I <sub>D</sub>	45 14 <sup>b, c</sup>	Α Α	
T <sub>A</sub> = 70 °C		I <sub>DM</sub>	10 <sup>b, c</sup> 150		
Avalanche Current	1 0.1 ml l	I <sub>AS</sub>	25		
Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	40	mJ	
Continuous Source-Drain Diode Current $ T_C = 25 \text{ °C} $ $ T_A = 25 \text{ °C} $		I <sub>S</sub>	15 2.9 <sup>b, c</sup>	Α	
Marianua Davia Diaria atian	$T_C = 25 ^{\circ}\text{C}$ $T_C = 70 ^{\circ}\text{C}$		28 18		
Maximum Power Dissipation	$T_A = 25 ^{\circ}\text{C}$ $T_A = 70 ^{\circ}\text{C}$	P <sub>D</sub>	3.5 <sup>b, c</sup> 2.2 <sup>b, c</sup>	W	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	
Soldering Recommendations (Peak Tempera		260			

THERMAL RESISTANCE RAT	RMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient	t ≤ 10 s	$R_{thJA}$	29	36	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R <sub>thJC</sub>	3.6	4.5	0/11	

- a. Based on  $T_C$  = 25 °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s.



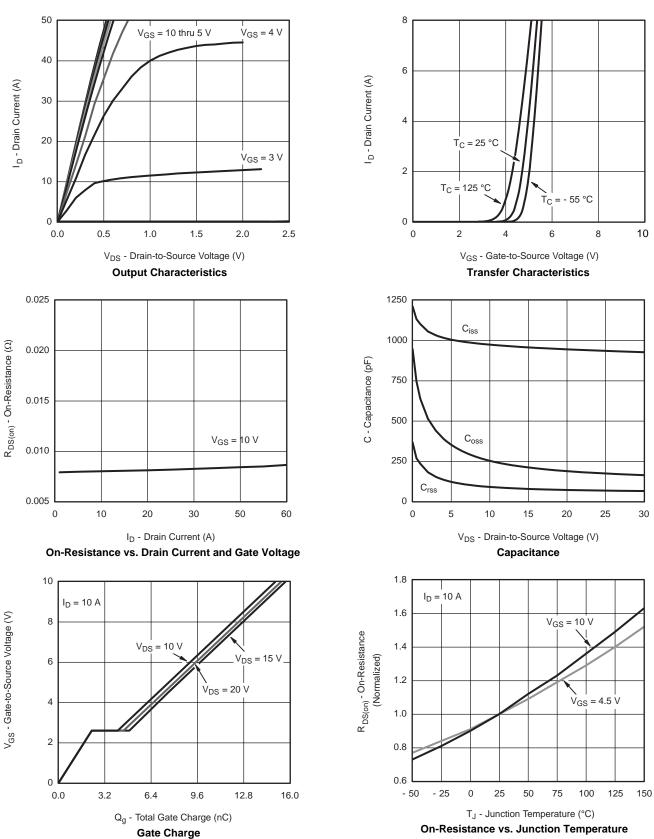
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static	-				L	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 050 A		33		1400
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 5		mV/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_{D} = 250 \mu A$	1.2		3.0	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
7 0		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	15			Α
D : 0		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A		7		0
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_{D} = 7 \text{ A}$		9		mΩ
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 10 A		24		S
Dynamic <sup>b</sup>			I.	•	I.	
Input Capacitance	C <sub>iss</sub>			1700		
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		200		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			150		
Total Cata Chausa	0	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$		33		
Total Gate Charge	$Q_g$			18		-0
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 10 \text{ A}$		7.3		nC
Gate-Drain Charge	$Q_{gd}$			6.2		
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.2	0.8	1.6	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			15	30	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 1.5 $\Omega$		12	24	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 10 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		13	26	
Fall Time	t <sub>f</sub>			10	20	
Turn-On Delay Time	t <sub>d(on)</sub>			9	18	ns
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 1.5 $\Omega$		9	18	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ 10 A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$		14	28	
Fall Time	t <sub>f</sub>			8	16	
<b>Drain-Source Body Diode Characteristi</b>	cs					
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			16	А
Pulse Diode Forward Current	I <sub>SM</sub>				32	
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = 3 A, V <sub>GS</sub> = 0 V		0.78	1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			17	34	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 10 A, dl/dt = 100 A/µs, T <sub>.I</sub> = 25 °C		9.5	19	nC
Reverse Recovery Fall Time	t <sub>a</sub>	i <sub>F</sub> = 10 Λ, αι/αι = 100 Λ/μs, 1 <sub>J</sub> = 25 °C		10		20
Reverse Recovery Rise Time	t <sub>b</sub>			7		ns

#### Notes:

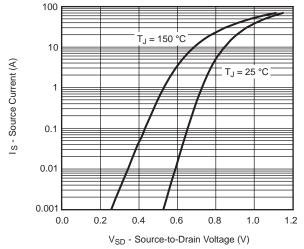
- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

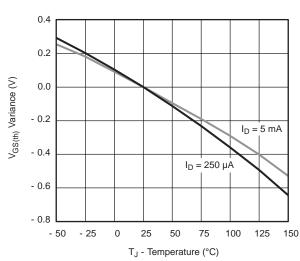




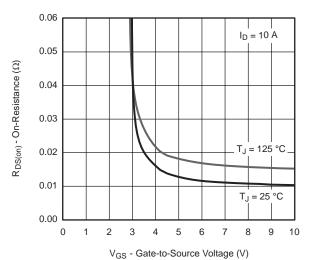




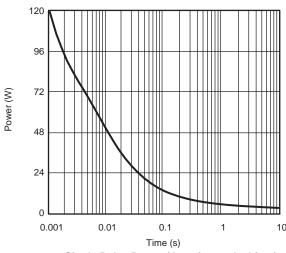
Source-Drain Diode Forward Voltage



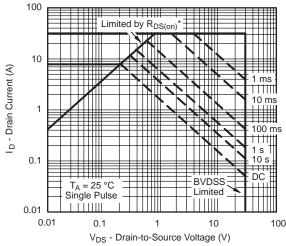
**Threshold Voltage** 



On-Resistance vs. Gate-to-Source Voltage



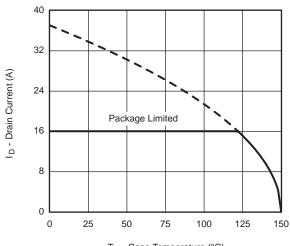
Single Pulse Power (Junction-to-Ambient)



\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

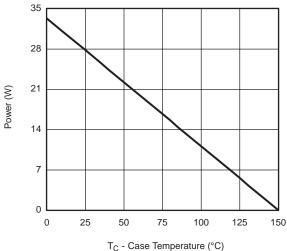
Safe Operating Area, Junction-to-Ambient





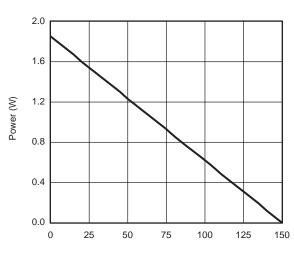
T<sub>C</sub> - Case Temperature (°C)

#### **Current Derating\***



Power, Junction-to-Case



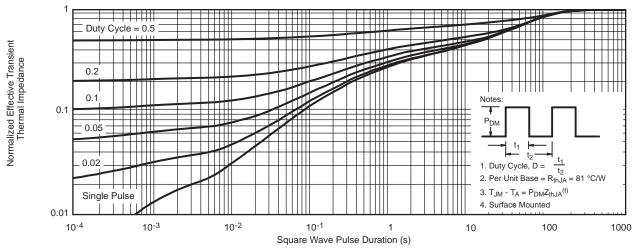


T<sub>A</sub> - Case Temperature (°C)

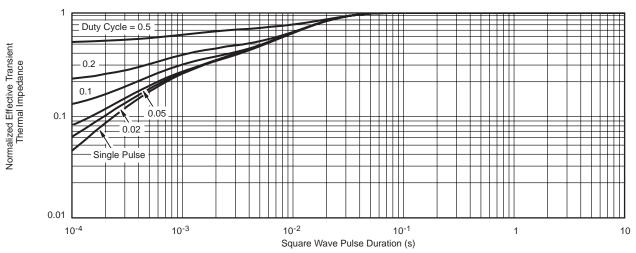
Power, Junction-to-Ambient

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





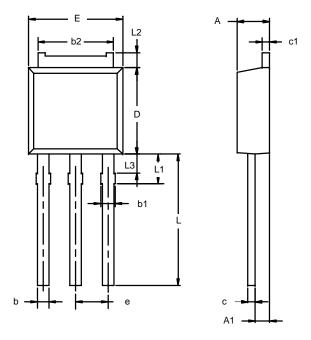
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case



## TO-251AA (DPAK)



Note: Dimension L3 is for reference only.

	MILLIM	IETERS	INC	HES		
Dim	Min	Max	Min	Max		
Α	2.21	2.38	0.087	0.094		
<b>A</b> 1	0.89	1.14	0.035	0.045		
b	0.71	0.89	0.028	0.035		
b1	0.76	1.14	0.030	0.045		
b2	5.23	5.43	0.206	0.214		
С	0.46	0.58	0.018	0.023		
с1	0.46	0.58	0.018	0.023		
D	5.97	6.22	0.235	0.245		
Е	6.48	6.73	0.255	0.265		
е	2.28	BSC	0.090	0.090 BSC		
L	3.89	9.53	0.153	0.375		
L1	1.91	2.28	0.075	0.090		
L2	0.89	1.27	0.035	0.050		
L3	1.15	1.52	0.045	0.060		
ECN: S-0 DWG: 53	3946—Rev. E 346	, 09-Jul-01				



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